

SANYO	No. 1614B	2SC3636
NPN Triple Diffused Planar Silicon Transistor VERY HIGH-DEFINITION DISPLAY HORIZONTAL DEFLECTION OUTPUT APPLICATIONS		

Features

- . High reliability (Adoption of HVP process)
- . Fast speed.
- . High breakdown voltage.
- . Adoption of MBIT process.

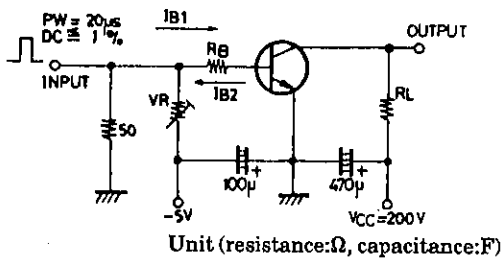
Absolute Maximum Ratings at Ta=25°C

Collector-to-Base Voltage	V_{CB0}	900	V	unit
Collector-to-Emitter Voltage	V_{CE0}	500	V	
Emitter-to-Base Voltage	V_{EB0}	7	V	
Collector Current	I_C	7	A	
Peak Collector Current	i_{cp}	14	A	
Collector Dissipation	P_C	80	W	
Junction Temperature	T_j	150	°C	
Storage Temperature	T_{stg}	-55 to +150	°C	

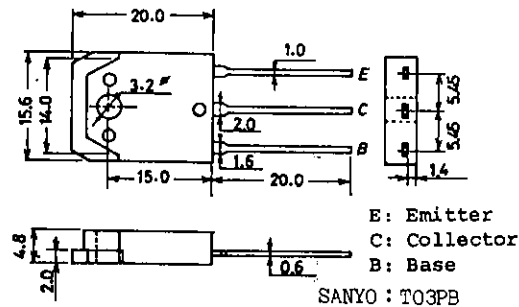
Electrical Characteristics at Ta=25°C

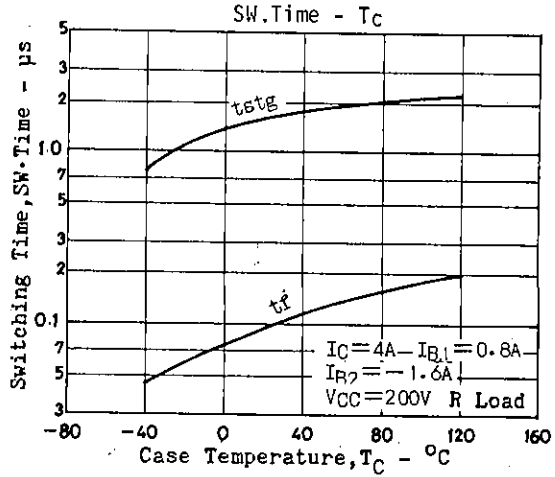
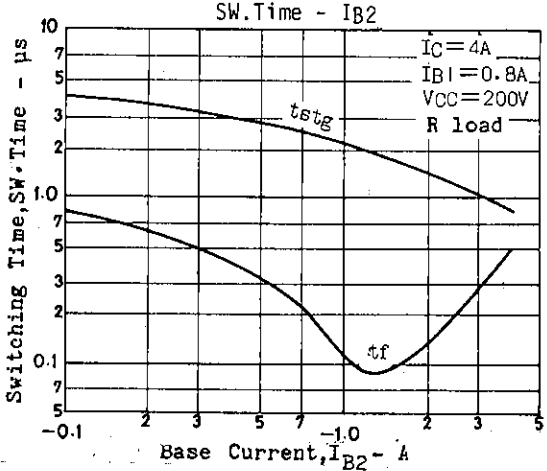
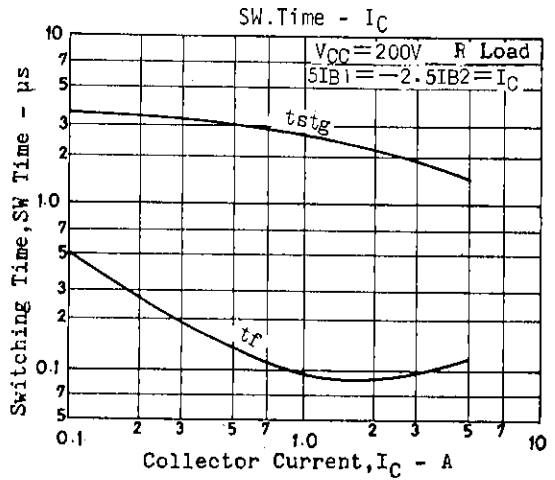
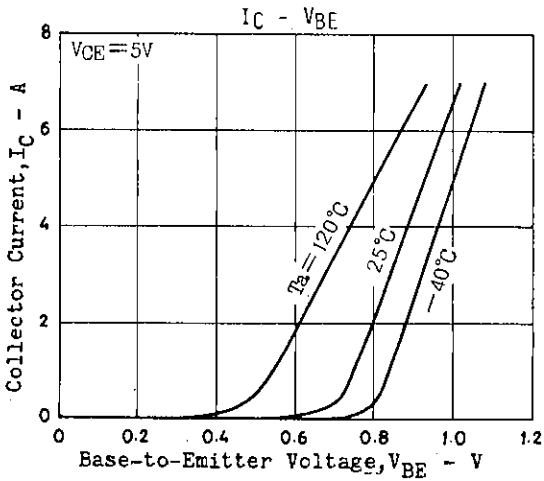
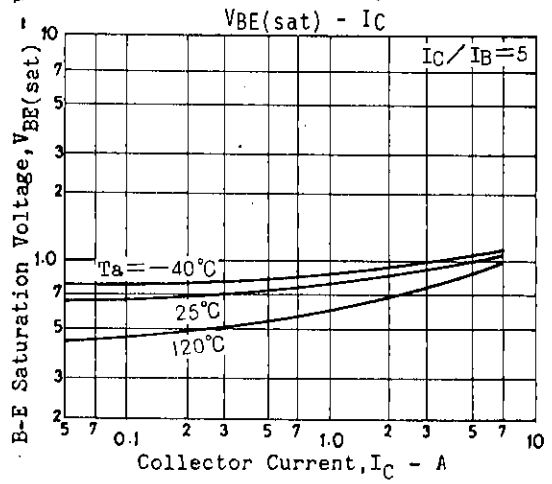
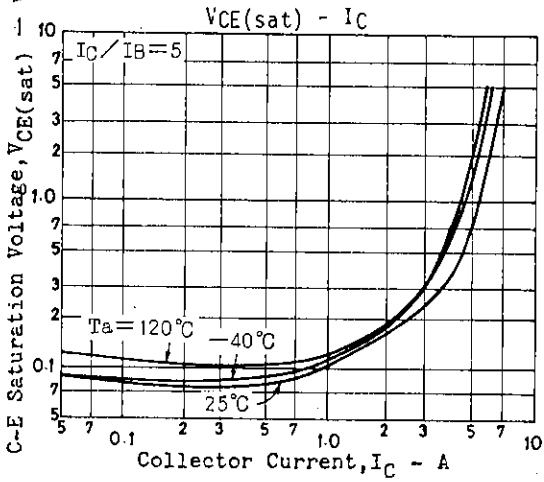
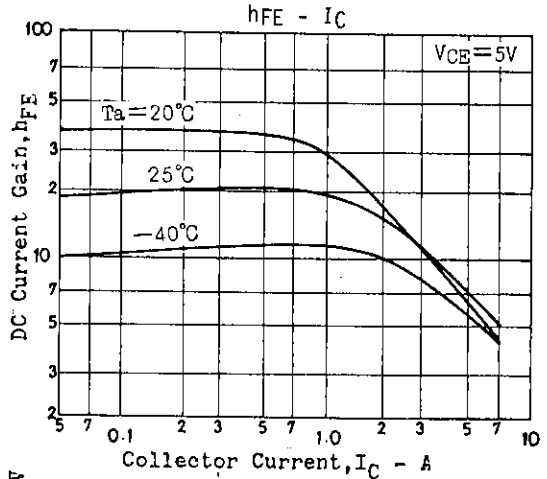
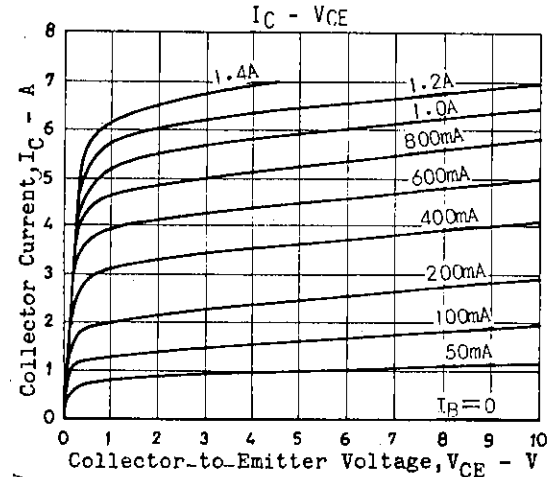
			min	typ	max	unit
Collector Cutoff Current	I_{CBO}	$V_{CB}=500V, I_E=0$			10	μA
Collector-to-Emitter Sustain Voltage	$V_{CEO(sus)}$	$V_{CE}=900V, R_{BE}=0$ $I_C=100mA, I_B=0$	500			V
Emitter Cutoff Current	I_{EBO}	$V_{EB}=5V, I_C=0$			1	mA
Collector-to-Emitter Saturation Voltage	$V_{CE(sat)}$	$I_C=4A, I_B=0.8A$			2	V
Base-to-Emitter Saturation Voltage	$V_{BE(sat)}$	$I_C=4A, I_B=0.8A$			1.5	V
DC Current Gain	f_{FE}	$V_{CE}=5V, I_C=0.8A$	8			
Storage Time	t_{stg}	$I_C=4A, I_{B1}=0.8A,$ $I_{B2}=-1.6A$			3.0	μs
Fall Time	t_f	$I_C=4A, I_{B1}=0.8A,$ $I_{B2}=-1.6A$	0.1	0.2		μs

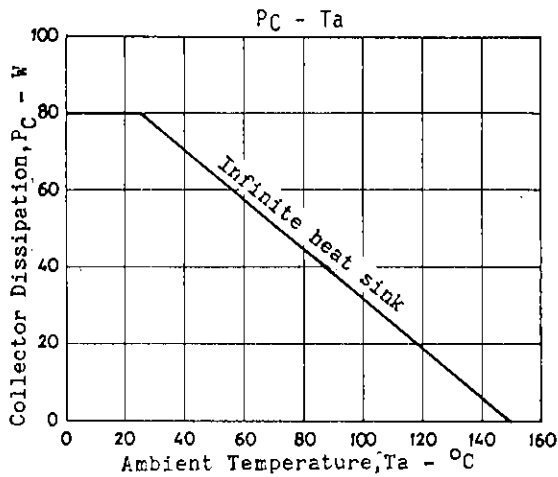
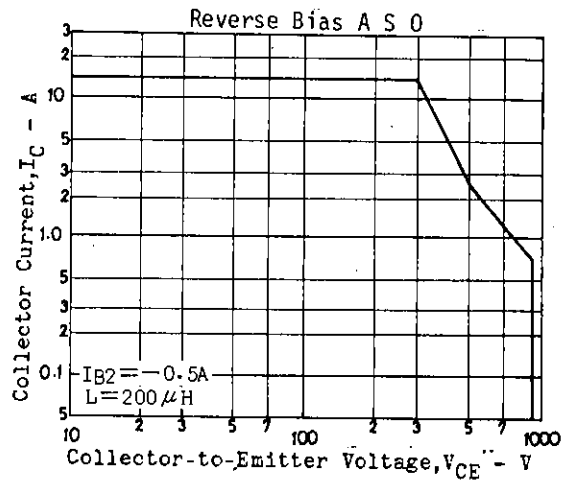
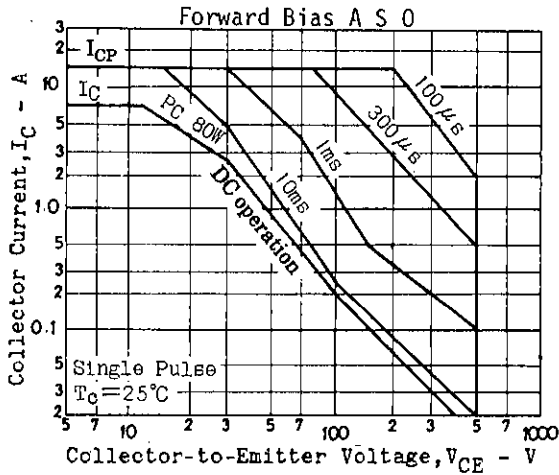
Switching Time Test Circuit



Package Dimensions 2022 (unit: mm)







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