

SILICON POWER TRANSISTOR 2SC4336

NPN SILICON EPITAXIAL TRANSISTOR FOR HIGH-SPEED SWITCHING

DESCRIPTION

The 2SC4336 is a mold power transistor developed for high-speed switching and features a very low collector-to-emitter saturation. This transistor is ideal for use in switching power supplies, DC/DC converters, motor drivers, solenoid drivers, and other low-voltage power supply devices, as well as for high-current switching.

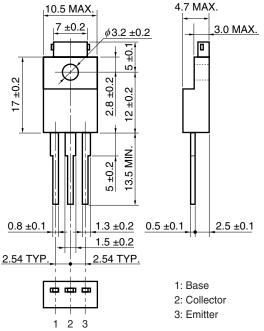
FEATURES

- Mold package that does not require an insulating board or insulation bushing
- · Fast switching speed
- Low collector-to-emitter saturation voltage
 V_{CE(sat)} ≤ 0.3 V MAX. (Ic = 6.0 A)

ORDERING INFORMATION

PART NUMBER	PACKAGE
2SC4336	Isolated TO-220 (MP-45)

PACKAGE DRAWING (Unit: mm)



ABSOLUTE MAXIMUM RATINGS ($T_A = 25^{\circ}C$)

Collector to base voltage	VcBo	100	V
Collector to emitter voltage	Vceo	100	V
Emitter to base voltage	V_{EBO}	7.0	V
Collector current (DC)	Ic(DC)	10	Α
Collector current (pulse) Note	$I_{C(pulse)}$	20	Α
Base current (DC)	I _{B(DC)}	6.0	Α
Total power dissipation (Tc = 25°C)	Рт	30	W
Total power dissipation (T _A = 25°C)	Рт	2.0	W
Junction temperature	T_{j}	150	°C
Storage temperature	T_{stg}	-55 to +150	°C

Note PW \leq 300 μ s, Duty Cycle \leq 10%

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ELECTRICAL CHARACTERISTICS (TA = 25°C)

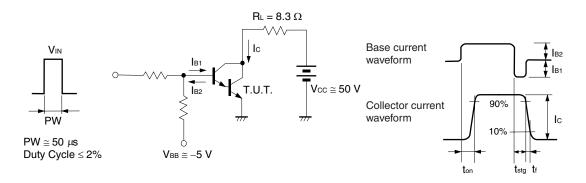
Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Collector to Emitter Voltage	VCEO(SUS)	Ic = 5.0 A, I _B = 0.6 A, L = 1 mH	100			V
	VCEX(SUS)	$I_C = 5.0 \text{ A}, I_{B1} = -I_{B2} = 0.6 \text{ A},$ $V_{BE(OFF)} = -1.5 \text{ V}, L = 180 \ \mu\text{H}, clamped$	100			V
Collector Cut-off Current	Ісво	V _{CB} = 100 V, I _E = 0			10	μΑ
	ICER	V_{CE} = 100 V, R_{BE} = 50 Ω, T_{A} = 125°C			1.0	mA
	Icex1	Vce = 100 V, VBE(OFF) = -1.5 V			10	μΑ
	ICEX2	V _{CE} = 100 V, V _{BE(OFF)} = -1.5 V, T _A = 125°C			1.0	mA
Emitter Cut-off Current	ІЕВО	V _{EB} = 5.0 V, I _C = 0			10	μΑ
DC Current Gain Note	h _{FE1}	Vce = 2.0 V, Ic = 1.0 A	100			
	h _{FE2}	Vce = 2.0 V, Ic = 2.0 A	100	200	400	
	h _{FE3}	VcE = 2.0 V, Ic = 6.0 A	60			
Collector Saturation Voltage Note	VCE(sat)1	Ic = 6.0 A, I _B = 0.3 A			0.3	V
	VCE(sat)2	Ic = 8.0 A, I _B = 0.4 A			0.5	V
Base Saturation Voltage Note	V _{BE(sat)1}	Ic = 6.0 A, I _B = 0.3 A			1.2	V
	V _{BE(sat)2}	Ic = 8.0 A, I _B = 0.4 A			1.5	V
Collector Capacitance	Cob	V _{CB} = 10 V, I _E = 0, f = 1.0 MHz		120		pF
Gain Bandwidth Product	f⊤	VcE = 10 V, Ic = 0.5 A		150		MHz
Turn-on Time	ton	Ic = 6.0 A, R _L = 8.3 Ω,			0.3	μs
Storage Time	tstg	I _{B1} = −I _{B2} = 0.3 A, V _{CC} ≅ 50 V Refer to the test circuit.			1.5	μs
Fall Time	tf	There to the test chount.			0.3	μs

Note Pulsed: PW \leq 350 μ s, Duty Cycle \leq 2%

hfe CLASSIFICATION

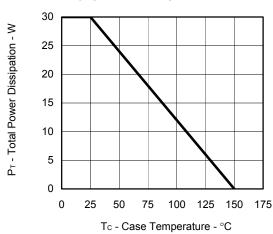
Marking	M	L	K
h _{FE2}	100 to 200	150 to 300	200 to 400

SWITCHING TIME (ton, tstg, tf) TEST CIRCUIT

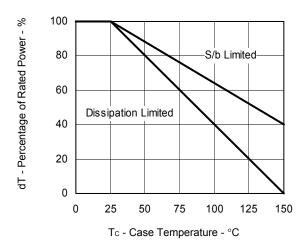


TYPICAL CHARACTERISTICS (T_A = 25°C)

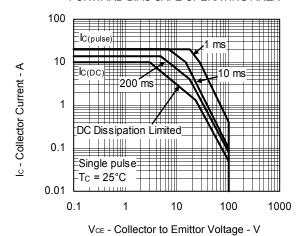
TOTAL POWER DISSIPATION vs. CASE TEMPERATURE

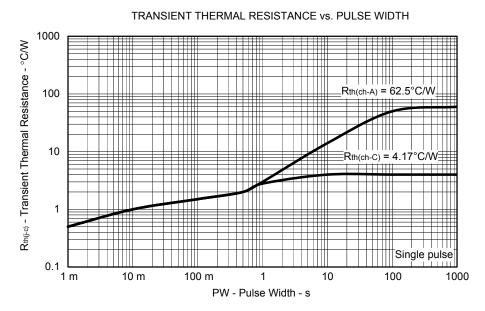


DERATING CURVE OF SAFE OPERATING AREA

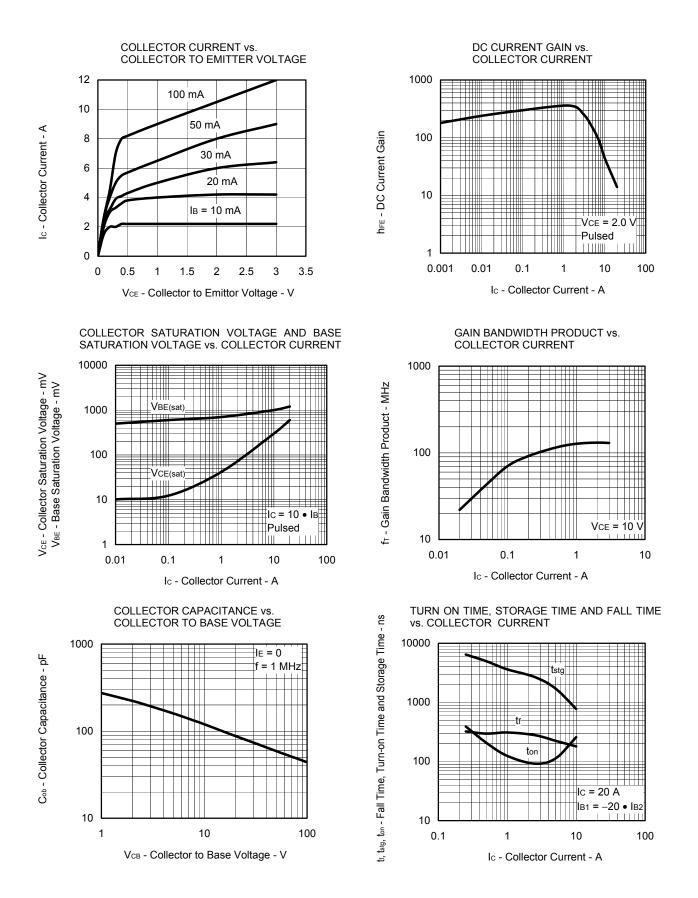


FORWARD BIAS SAFE OPERATING AREA





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