

FUJI POWER MOSFET Super FAP-G Series

N-CHANNEL SILICON POWER MOSFET

■ Features

- High speed switching
- Low on-resistance
- No secondary breakdown
- Low driving power
- Avalanche-proof

■ Applications

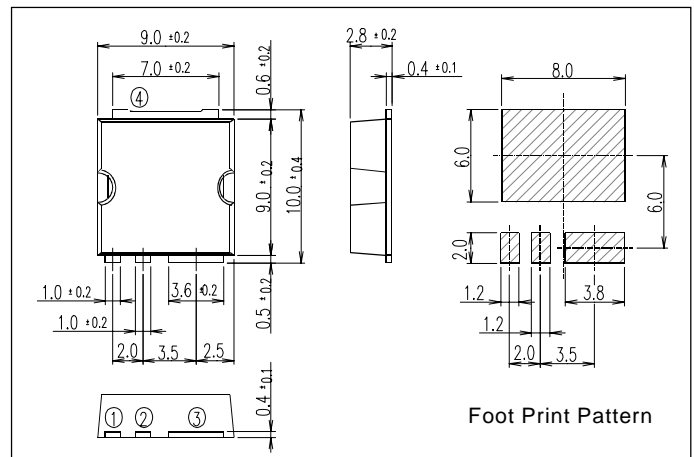
- Switching regulators
- UPS (Uninterruptible Power Supply)
- DC-DC converters

■ Maximum ratings and characteristic

Absolute maximum ratings

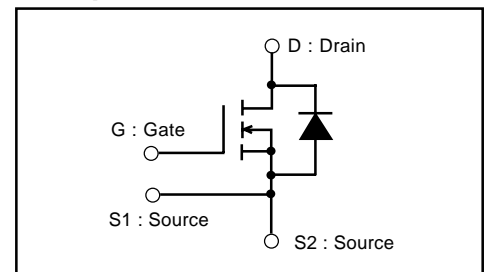
● (T_c=25°C unless otherwise specified)

■ Outline Drawings (mm)



Item	Symbol	Ratings	Unit
Drain-source voltage	V _{DS}	200	V
	V _{DSX} *5	170	V
Continuous drain current	I _D	T _c =25°C	±45
		T _a =25°C	±4.3 **
Pulsed drain current	I _{D(puls)}	±180	A
Gate-source voltage	V _{GS}	±30	V
Non-repetitive Avalanche current	I _{AS} *2	45	A
Maximum Avalanche Energy	E _{AS} *1	258.9	mJ
Maximum Drain-Source dV/dt	dV _{DS} /dt *4	20	kV/μs
Peak Diode Recovery dV/dt	dV/dt *3	5	kV/μs
Max. power dissipation	P _D	T _c =25°C	270
		T _a =25°C	2.4 **
Operating and storage temperature range	T _{ch}	+150	°C
	T _{stg}	-55 to +150	°C

■ Equivalent circuit schematic



** Surface mounted on 1000mm², t=1.6mm FR-4 PCB(Drain pad area : 500mm²) T_a=25°C

*1 L=205μH, V_{CC}=48V, T_{ch}=25°C, See to Avalanche Energy Graph *2 T_{ch}≤150°C

*3 I_F≤-I_D, -di/dt=50A/μs, V_{CC}≤BV_{DSS}, T_{ch}≤150°C *4 V_{DS}≤200V *5 V_{GS}=-30V

● Electrical characteristics (T_c =25°C unless otherwise specified)

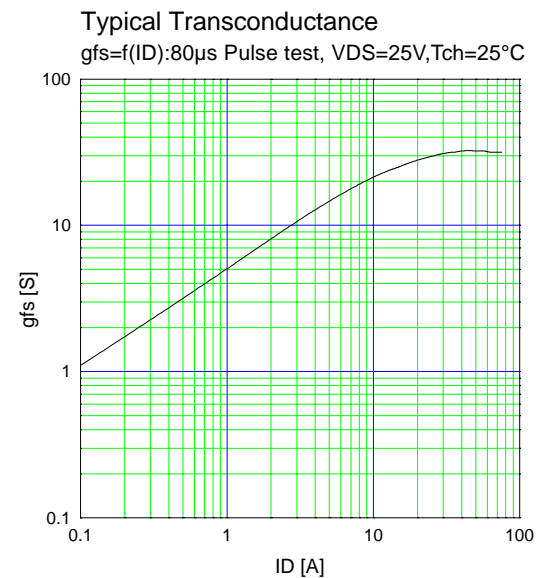
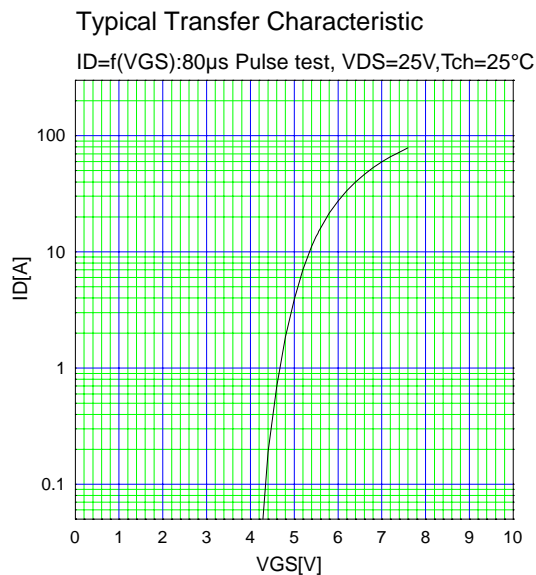
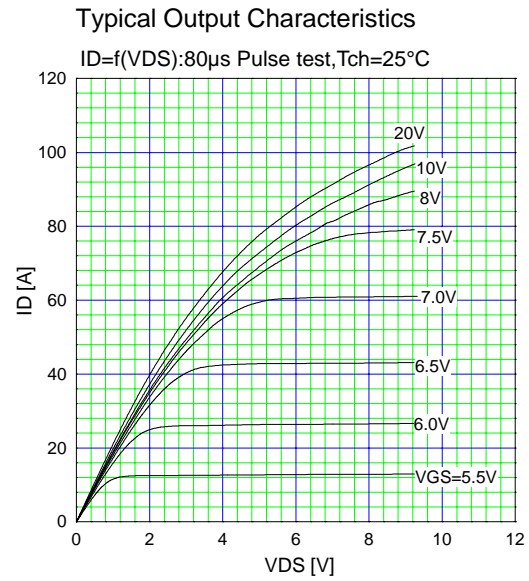
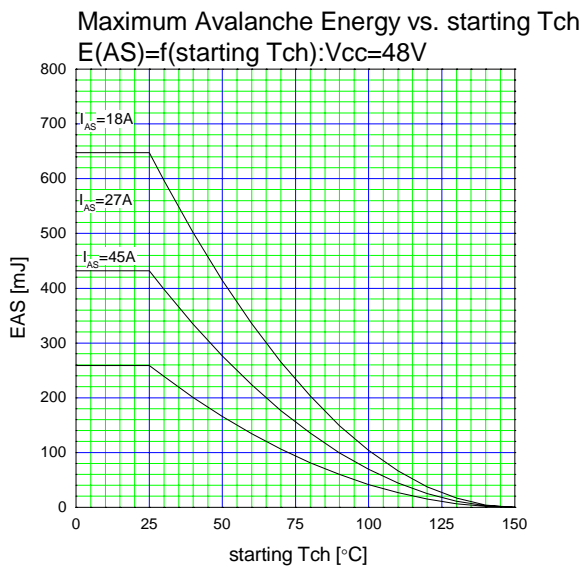
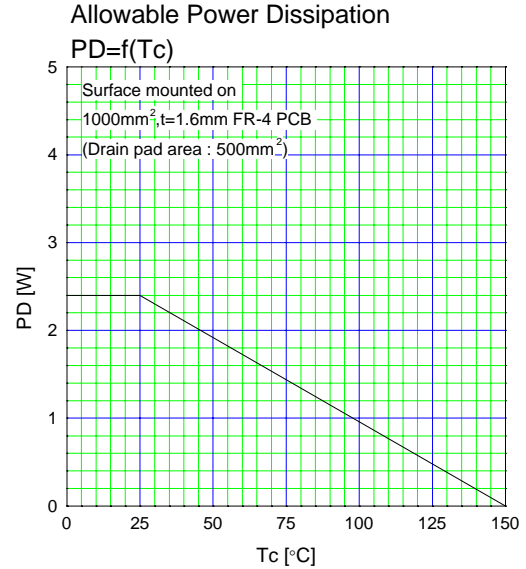
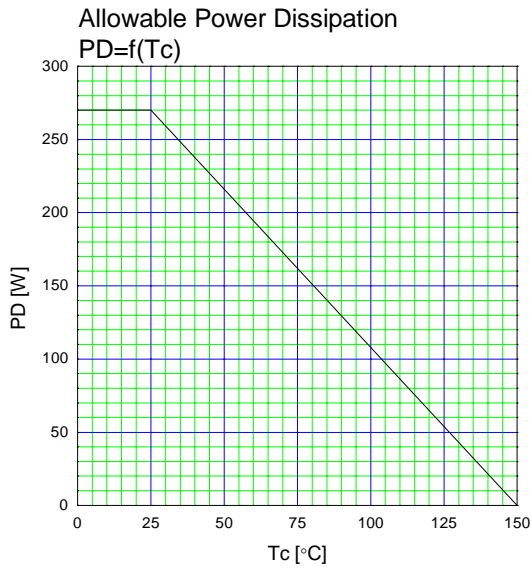
Item	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Drain-source breakdown voltage	V _{(BR)DSS}	I _D =250μA V _{GS} =0V	200			V
Gate threshold voltage	V _{GS(th)}	I _D =250μA V _{DS} =V _{GS}	3.0		5.0	V
Zero gate voltage drain current	I _{DSS}	V _{DS} =200V V _{GS} =0V			25	μA
		V _{DS} =160V V _{GS} =0V			250	
Gate-source leakage current	I _{GSS}	V _{GS} =±30V V _{DS} =0V		10	100	nA
Drain-source on-state resistance	R _{DS(on)}	I _D =15A V _{GS} =10V		50	66	mΩ
Forward transconductance	g _{fs}	I _D =15A V _{DS} =25V	12.5	25		S
Input capacitance	C _{iss}	V _{DS} =75V		1960	2940	pF
Output capacitance	C _{oss}	V _{GS} =0V		260	390	
Reverse transfer capacitance	C _{rss}	f=1MHz		18	27	
Turn-on time t _{on}	td(on)	V _{CC} =48V I _D =15A		20	30	ns
	t _r	V _{GS} =10V		17	26	
Turn-off time t _{off}	td(off)	R _{GS} =10 Ω		53	80	
	t _f			19	29	
Total Gate Charge	Q _G	V _{CC} =100V		51	76.5	nC
Gate-Source Charge	Q _{GS}	I _D =30A		15	22.5	
Gate-Drain Charge	Q _{GD}	V _{GS} =10V		16	24	
Avalanche capability	I _{AV}	L=205μH T _{ch} =25°C	45			A
Diode forward on-voltage	V _{SD}	I _F =30A V _{GS} =0V T _{ch} =25°C		1.10	1.65	V
Reverse recovery time	t _{rr}	I _F =30A V _{GS} =0V		0.19		μs
Reverse recovery charge	Q _{rr}	-di/dt=100A/μs T _{ch} =25°C		1.4		μC

● Thermal characteristics

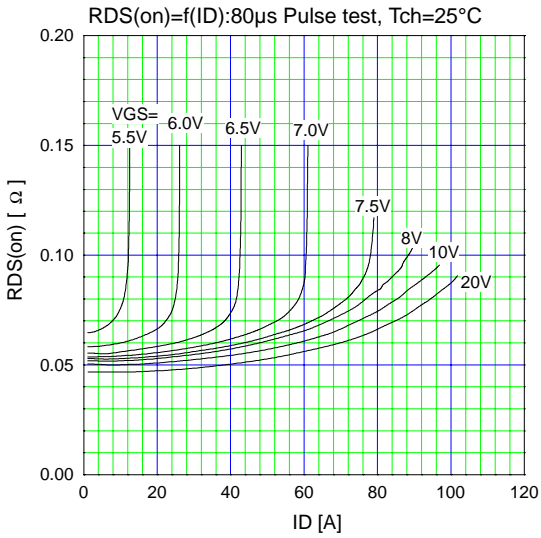
Item	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Thermal resistance	R _{th(ch-c)}	channel to case			0.463	°C/W
	R _{th(ch-a)}	channel to ambient			87.0	°C/W
	R _{th(ch-a)} **	channel to ambient			52.0	

** Surface mounted on 1000mm², t=1.6mm FR-4 PCB(Drain pad area : 500mm²)

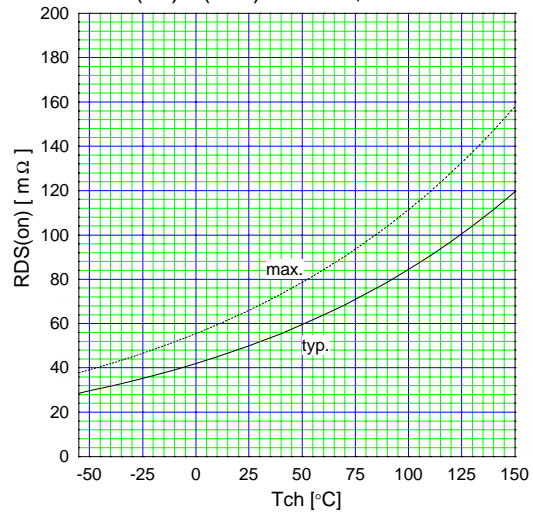
Characteristics



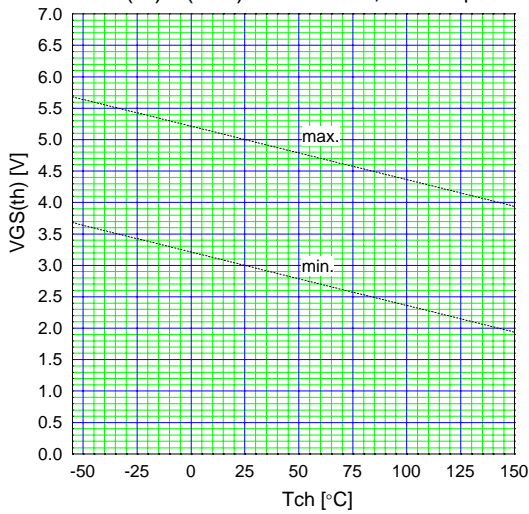
Typical Drain-Source on-state Resistance



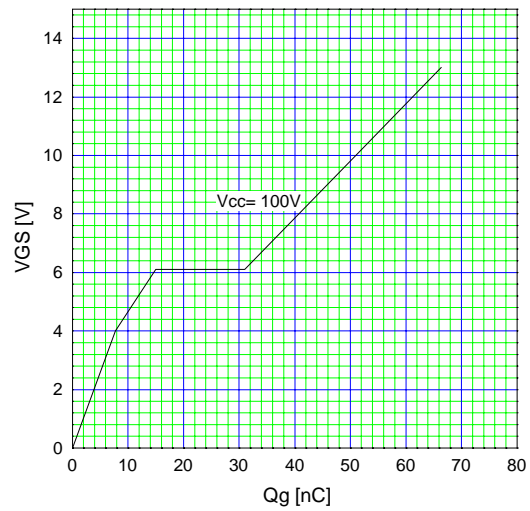
Drain-Source On-state Resistance
 $R_{DS(on)}=f(T_{ch}):I_D=15A, V_{GS}=10V$



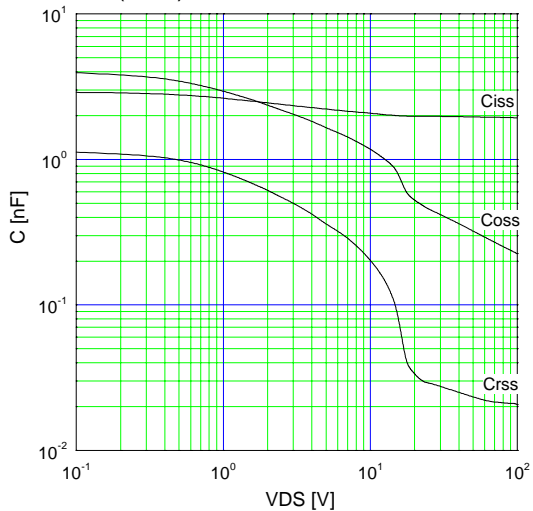
Gate Threshold Voltage vs. T_{ch}
 $V_{GS(th)}=f(T_{ch}):V_{DS}=V_{GS}, I_D=250\mu A$



Typical Gate Charge Characteristics
 $V_{GS}=f(Q_g):I_D=30A, T_{ch}=25^\circ C$



Typical Capacitance
 $C=f(V_{DS}):V_{GS}=0V, f=1MHz$



Typical Forward Characteristics of Reverse Diode
 $I_F=f(V_{SD}):80\mu s$ Pulse test, $T_{ch}=25^\circ C$

