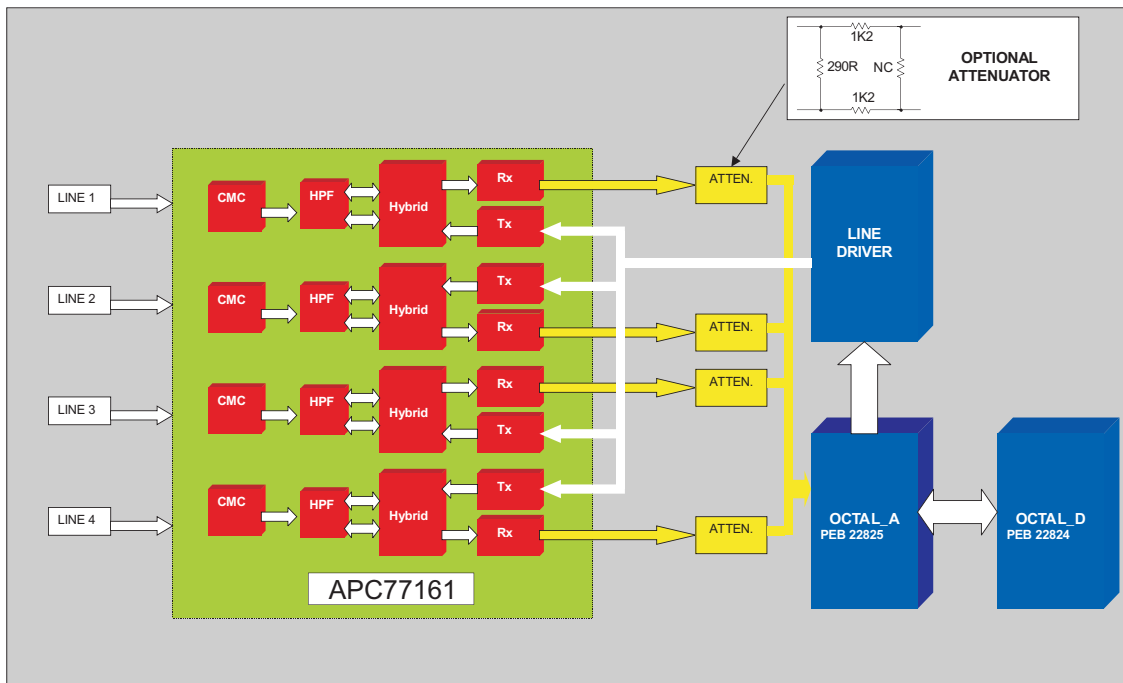


Asymmetric 2 Band VDSL Application APC77161

FEATURES

- Plan 998 Compatible
- Incorporates xDSL choke >35dBmin
- Miniature footprint for increased Line-card density
- Provides 1500Vrms isolation voltage

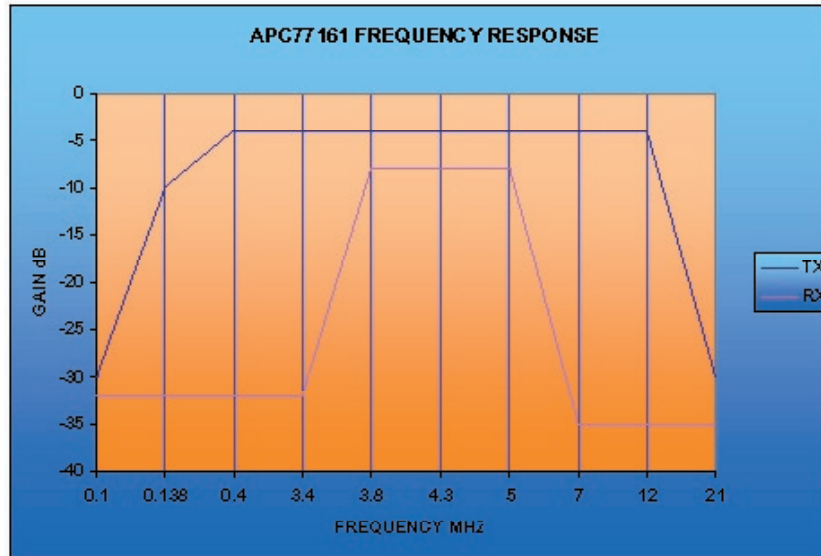
BLOCK DIAGRAM





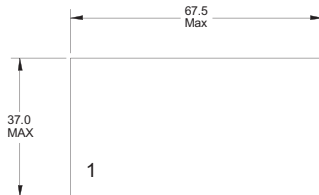
Asymmetric 2 Band VDSL Application APC77161

FREQUENCY ALLOCATION

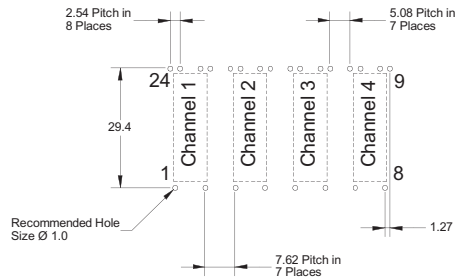


MECHANICAL

Top View



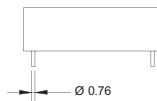
Recommended PCB Layout



Side View



End View



Signal	Channel 1	Channel 2	Channel 3	Channel 4
Line +	Pin 1	Pin 3	Pin 5	Pin 7
Line -	Pin 2	Pin 4	Pin 6	Pin 8
Tx +	Pin 21	Pin 19	Pin 13	Pin 11
Tx -	Pin 22	Pin 20	Pin 14	Pin 12
Rx +	Pin 23	Pin 17	Pin 15	Pin 9
Rx -	Pin 24	Pin 18	Pin 16	Pin 10

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