Digital transistors (built-in resistors) DTD123YK / DTD123YS

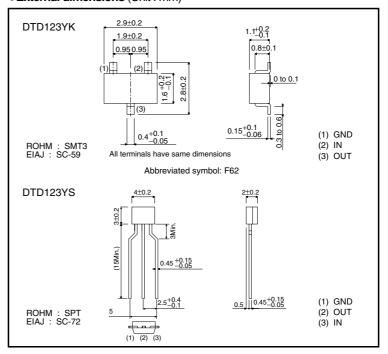
Features

- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit).
- 2) The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of almost completely eliminating parasitic effects.
- Only the on / off conditions need to be set for operation, making device design easy.

Structure

NPN digital transistor (Built-in resistor type)

●External dimensions (Unit : mm)



● Absolute maximum ratings (Ta=25°C)

Parameter	Cumphal	Limits(D	Unit		
	Symbol	К	S	Offic	
Supply voltage	Vcc	50		V	
Input voltage	Vin	−5 to +12		V	
Output current	lc	500		mA	
Power dissipation	Pd	200	300	mW	
Junction temperature	Tj	150		ဗ	
Storage temperature	Tstg	-55 to +150		ာ	

●Equivalent circuit

Rev.A

●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions	
Input voltage	VI(off)	-	_	0.3	٧	Vcc=5V, Io=100μA	
	VI(on)	2	_	_		Vo=0.3V, Io=20mA	
Output voltage	V _{O(on)}	-	0.1	0.3	V	lo/l⊫50mA/2.5mA	
Input current	lı	_	_	3.6	mA	V _I =5V	
Output current	IO(off)	-	_	0.5	μΑ	Vcc=50V, V⊫0V	
DC current gain	Gı	56	_	_	_	Vo=5V, Io=50mA	
Input resistance	R ₁	1.54	2.2	2.86	kΩ	-	
Resistance ratio	R2/R1	3.6	4.5	5.5	_	-	
Transition frequency	fτ	_	200	_	MHz	Vc=10V, I=-50mA, f=100MHz *	

^{*}Transition frequency of the device

Package specifications

	Package	SMT3	SPT
	Packaging type	Taping	Taping
	Code	T146	TP
Part No.	Basic ordering unit (pieces)	3000	5000
DTD123YK		0	_
DTD123YS		_	0

•Electrical characteristics curves

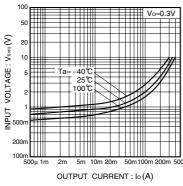


Fig.1 Input voltage vs. output current (ON characteristics)

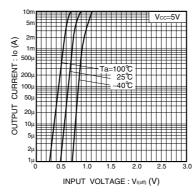


Fig.2 Output current vs. input voltage (OFF characteristics)

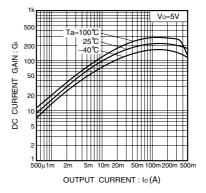


Fig.3 DC current gain vs. output current

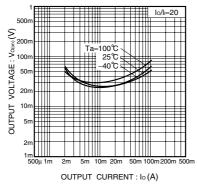


Fig.4 Output voltage vs. output current

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