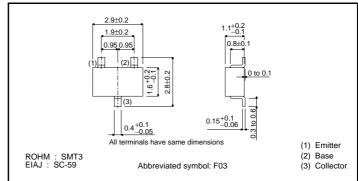
Digital transistors (built-in resistor) DTD143TK

Features

- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit).
- 2) The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of almost completely eliminating parasitic effects.
- Only the on / off conditions need to be set for operation, making device design easy.

●External dimensions (Unit : mm)



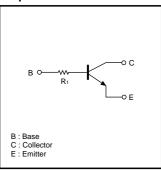
Structure

NPN digital transistor (Built-in resistor type)

● Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit			
Collector-base voltage	Vсво	50	V			
Collector-emitter voltage	Vceo	40	V			
Emitter-base voltage	Vево	5	V			
Collector current	lc	500	mA			
Collector power dissipation	Pc	200	mW			
Junction temperature	Tj	150	င			
Storage temperature	Tstg	-55 to +150	င			

●Equivalent circuit



Rev.A

●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Collector-base breakdown voltage	ВУсво	50	_	_	V	Ic=50μA
Collector-emitter breakdown voltage	BVceo	40	_	_	V	Ic=1mA
Emitter-base breakdown voltage	ВУЕВО	5	_	_	V	Iε=50μA
Collector cutoff current	Ісво	_	_	0.5	μΑ	Vcb=50V
Emitter cutoff current	ІЕВО	_	_	0.5	μΑ	V _{EB} =4V
Collector-emitter saturation voltage	VcE(sat)	_	_	0.3	V	Ic/I _B =50mA/2.5mA
DC current transfer ratio	hfe	100	250	600	_	VcE=5V, Ic=50mA
Input resistance	R ₁	3.29	4.7	6.11	kΩ	-
Transition frequency	f⊤	_	200	_	MHz	Vc=10V, I=-50mA, f=100MHz *

^{*}Transition frequency of the device

Packaging specifications

	Package	SMT3				
	Packaging type	Taping				
	Code	T146				
Part No.	Basic ordering unit (pieces)	3000				
DTD143TK		0				

•Electrical characteristic curves

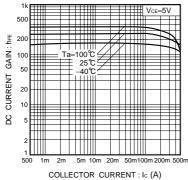


Fig.1 DC current gain vs. collector current

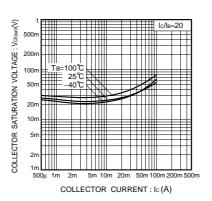


Fig.2 Collector-emitter saturation voltage vs. collector current

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