

GP801DDS18

Dual Switch Low $V_{\text{CE(SAT)}}$ IGBT Module

Replaces January 2000 version, DS235-3.0

DS5235-4.1 January 2001

FEATURES

- Low V_{CE(SAT)}
- Non Punch Through Silicon
- Isolated Copper Baseplate
- Low Inductance Internal Construction
- 800A Per Arm

APPLICATIONS

- High Reliability Inverters
- Motor Controllers
- Traction Drives
- Resonant Converters

The Powerline range of high power modules includes dual and single switch configurations covering voltages from 600V to 3300V and currents up to 4800A.

The GP801DDS18 is a dual switch 1800V, n channel enhancement mode, insulated gate bipolar transistor (IGBT) Designed with low $V_{\text{CE(SAT)}}$ to minimise conduction losses, the module is of particular relevance in low to medium frequency applications. The IGBT has a wide reverse bias safe operating area (RBSOA) ensuring reliability in demanding applications.

The module incorporates an electrically isolated base plate and low inductance construction enabling circuit designers to optimise circuit layouts and utilise earthed heat sinks for safety.

ORDERING INFORMATION

Order As:

GP801DDS18

Note: When ordering, please use the whole part number.

KEY PARAMETERS

V _{CES}		1800V
V _{CE(sat)}	(typ)	2.6V
I _C	(max)	A008
I _{C(PK)}	(max)	1600A

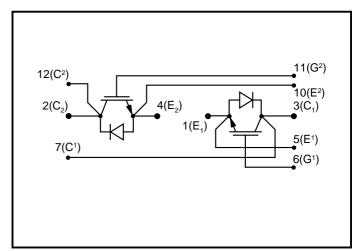


Fig. 1 Dual switch circuit diagram

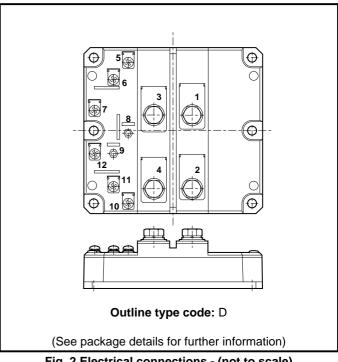


Fig. 2 Electrical connections - (not to scale)



ABSOLUTE MAXIMUM RATINGS - PER ARM

Stresses above those listed under 'Absolute Maximum Ratings' may cause permanent damage to the device. In extreme conditions, as with all semiconductors, this may include potentially hazardous rupture of the package. Appropriate safety precautions should always be followed. Exposure to Absolute Maximum Ratings may affect device reliability.

T_{case} = 25°C unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units
V _{CES}	Collector-emitter voltage	$V_{GE} = 0V$	1800	V
V_{GES}	Gate-emitter voltage	-	±20	V
I _c	Continuous collector current	$T_{case} = 70^{\circ}C$	800	Α
I _{C(PK)}	Peak collector current	1ms, T _{case} = 105°C	1600	Α
P_{max}	Max. transistor power dissipation	$T_{case} = 25^{\circ}C, T_{j} = 150^{\circ}C$	6000	W
V _{isol}	Isolation voltage	Commoned terminals to base plate. AC RMS, 1 min, 50Hz	4000	V

THERMAL AND MECHANICAL RATINGS

Symbol	Parameter	Test Conditions	Min.	Max.	Units
R _{th(j-c)}	Thermal resistance - transistor (per arm)	Continuous dissipation -	-	21	°C/kW
		junction to case			
R _{th(j-c)}	Thermal resistance - diode (per arm)	Continuous dissipation -	-	40	°C/kW
		junction to case			
R _{th(c-h)}	Thermal resistance - case to heatsink (per module)	Mounting torque 5Nm	-	8	°C/kW
		(with mounting grease)			
T _j	Junction temperature	Transistor	-	150	°C
		Diode	-	125	°C
T _{stg}	Storage temperature range	-	-40	125	°C
-	Screw torque	Mounting - M6	-	5	Nm
		Electrical connections - M4	-	2	Nm
		Electrical connections - M8	-	10	Nm



ELECTRICAL CHARACTERISTICS

 T_{case} = 25°C unless stated otherwise.

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
I _{CES}	Collector cut-off current	$V_{GE} = 0V$, $V_{CE} = V_{CES}$	-	-	1	mA
		V _{GE} = 0V, V _{CE} = V _{CES} , T _{case} = 125°C	-	-	25	mA
I _{GES}	Gate leakage current	$V_{GE} = \pm 20V, V_{CE} = 0V$	-	-	4	μА
V _{GE(TH)}	Gate threshold voltage	$I_{\rm C}$ = 40mA, $V_{\rm GE}$ = $V_{\rm CE}$	4.5	5.5	6.5	V
V _{CE(sat)}	Collector-emitter saturation voltage	V _{GE} = 15V, I _C = 800A	-	2.6	3.2	V
		$V_{GE} = 15V, I_{C} = 800A, T_{case} = 125^{\circ}C$	-	3.3	4	V
I _F	Diode forward current	DC	-	-	800	Α
I _{FM}	Diode maximum forward current	t _p = 1ms	-	-	1600	Α
V _F	Diode forward voltage	I _F = 800A	-	2.2	2.5	V
		I _F = 800A, T _{case} = 125°C	-	2.3	2.6	V
C _{ies}	Input capacitance	V _{CE} = 25V, V _{GE} = 0V, f = 1MHz	-	90	-	nF
L _M	Module inductance	-	-	20	-	nΗ



ELECTRICAL CHARACTERISTICS

T_{case} = 25°C unless stated otherwise

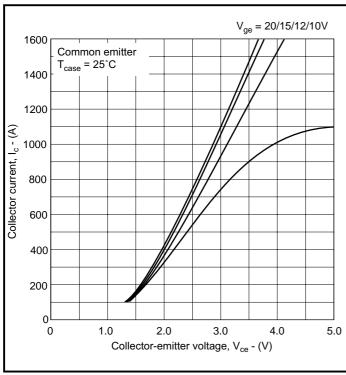
Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
t _{d(off)}	Turn-off delay time	I _c = 800A	-	1000	1200	ns
t _f	Fall time	$V_{GE} = \pm 15V$	-	250	350	ns
E _{OFF}	Turn-off energy loss	V _{CE} = 900V	-	500	600	mJ
t _{d(on)}	Turn-on delay time	$R_{G(ON)} = R_{G(OFF)} = 2.2\Omega$	-	300	400	ns
t _r	Rise time	L ~ 100nH	-	200	300	ns
E _{on}	Turn-on energy loss		-	300	400	mJ
Q _{rr}	Diode reverse recovery charge	$I_F = 800A, V_R = 50\% V_{CES}$	-	180	240	μС
I _{rr}	Diode reverse current	dI _F /dt = 3500A/μs	-	450	-	А
E _{REC}	Diode reverse recovery energy		-	120	-	mJ

T_{case} = 125°C unless stated otherwise

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
t _{d(off)}	Turn-off delay time	I _C = 800A	-	1200	1400	ns
t _f	Fall time	$V_{GE} = \pm 15V$	-	300	400	ns
E _{OFF}	Turn-off energy loss	V _{CE} = 900V	-	600	700	mJ
t _{d(on)}	Turn-on delay time	$R_{G(ON)} = R_{G(OFF)} = 2.2\Omega$	-	400	550	ns
t _r	Rise time	L ~ 100nH	-	250	350	ns
E _{on}	Turn-on energy loss		-	450	550	mJ
Q _{rr}	Diode reverse recovery charge	$I_F = 800A, V_R = 50\% V_{CES},$	-	300	400	μС
l _"	Diode reverse current	dl _F /dt = 3000A/μs	-	525	-	А
E _{rec}	Diode reverse recovery energy		-	190	-	mJ



TYPICAL CHARACTERISTICS



V_{ge} = 20/15/12/10V 1600 Common emitter T_{case} = 125°C 1400 1200 Collector current, I_c - (A) 1000 800 600 400 200 0 1.0 2.0 3.0 4.0 6.0 5.0 0 Collector-emitter voltage, V_{ce} - (V)

Fig. 3 Typical output characteristics

Fig. 4 Typical output characteristics

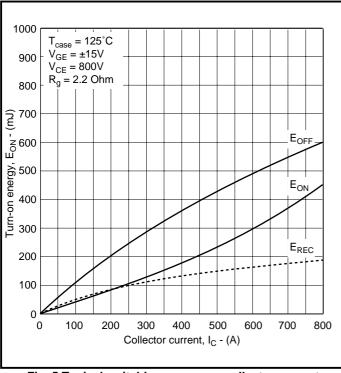


Fig. 5 Typical switching energy vs collector current

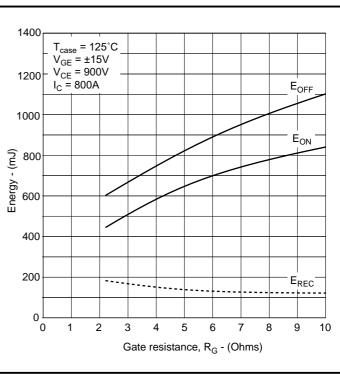
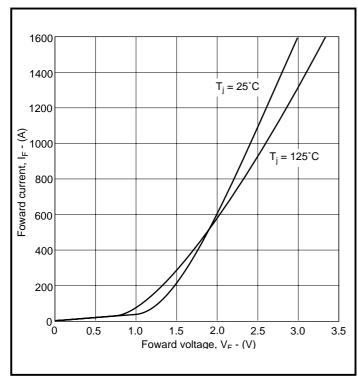


Fig. 6 Typical switching energy vs gate resistance





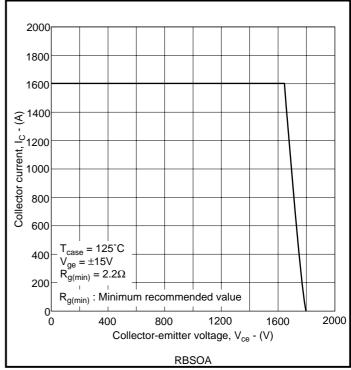
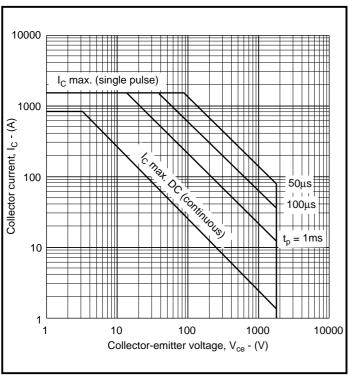
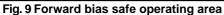


Fig. 7 Diode typical forward characteristics

Fig. 8 Reverse bias safe operating area





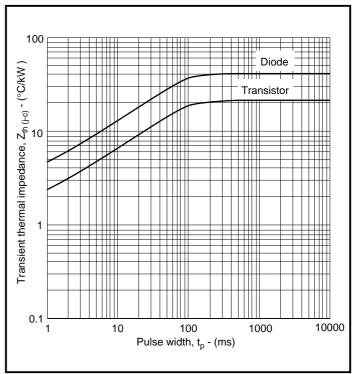
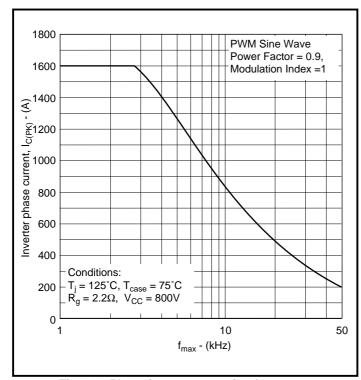
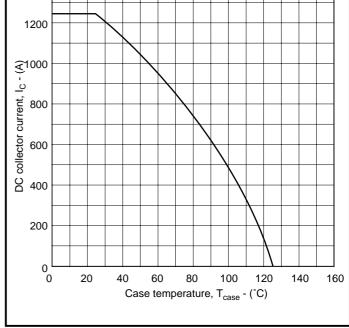


Fig. 10 Transient thermal impedance







1400

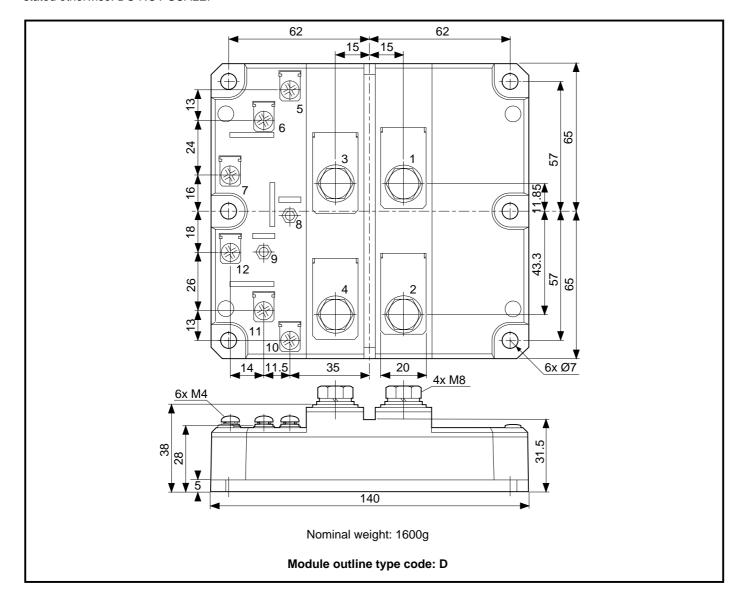
Fig. 11 3 Phase inverter operating frequency

Fig. 12 DC current rating vs case temperature



PACKAGE DETAILS

For further package information, please visit our website or contact your nearest Customer Service Centre. All dimensions in mm, unless stated otherwise. DO NOT SCALE.





ASSOCIATED PUBLICATIONS

Title	Application Note	·
	Number	
Electrostatic handling precautions	AN4502	
An introduction to IGBTs	AN4503	
IGBT ratings and characteristics	AN4504	
Heatsink requirements for IGBT modules	AN4505	
Calculating the junction temperature of power semiconductors	AN4506	
Gate drive considerations to maximise IGBT efficiency	AN4507	
Parallel operation of IGBTs – punch through vs non-punch through characteristics	AN4508	
Guidance notes for formulating technical enquiries	AN4869	
Principle of rating parallel connected IGBT modules	AN5000	
Short circuit withstand capability in IGBTs	AN5167	
Driving Dynex Semincoductor IGBT modules with Concept gate drivers	AN5384	

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We offer an extensive range of air and liquid cooled assemblies covering the full range of circuit designs in general use today. The Assembly group continues to offer high quality engineering support dedicated to designing new units to satisfy the growing needs of our customers.

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