

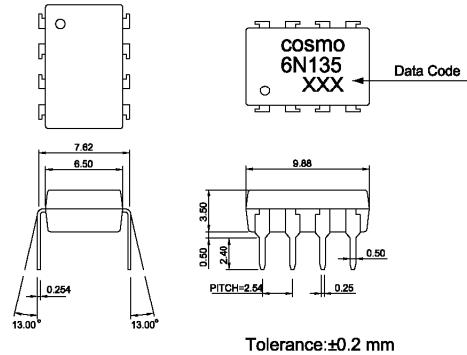
## Features

1. High speed response  $t_{PLH}$ ,  $t_{PHL}$   
(MAX.1.5us at  $R_L=4.1k\Omega$ )
2. High common mode rejection voltage  
(CM:TYP.1kV/us)
3. Standard dual-in-line package

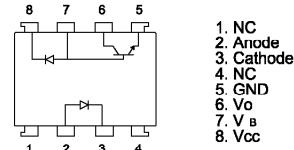
## Applications

1. Computers, measuring instruments, control equipment.
2. High speed line receivers high speed logic.
3. Telephone sets.
4. Signal transmission between circuits of different potentials and impedances.

## Outside Dimension:Unit (mm)



## Schematic: Top View



## Absolute Maximum Ratings

(Ta=25°C)

Parameter		Symbol	Rating	Unit
Input	Forward current	I_F	25	mA
	*1 Peak forward current	I_F	50	mA
	*2 Peak transient forward current	I_FM	1	A
	Reverse voltage	V_R	5	V
	Power dissipation	P	45	mW
Output	Supply voltage	Vcc	-0.5 to 15	V
	Output voltage	Vo	-0.5 to 15	V
	Emitter-base reverse with-stand voltage (Pin 5 to 7)	VEBO	5	V
	Average output current	Io	8	mA
	Peak output current	Iop	16	mA
	Base current (Pin 7)	I_B	5	mA
	Power dissipation	Po	100	mW
	*3 Isolation voltage 1 minute	Viso	2500	Vrms
	Operating temperature	Topr	-55 to +100	°C
Storage temperature		Tstg	-55 to +125	°C
*4 Soldering temperature		Tsol	260	°C

\*1 50% duty cycle, Pulse width : 1mS

Decreases at the rate of 1.6mA/°C if the external temperature is 70°C or more.

\*2 Pulse width <= 1uS, 300pulse/sec

\*3 40 to 60% RH, AC for 1 minute

\*4 For 10 seconds

**Electro-optical Characteristics**

(Ta=0 to +70°C unless otherwise specified )

Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
* <sup>5</sup> Current transfer ratio	CTR (1)	Ta= 25°C , I <sub>F</sub> =16mA V <sub>O</sub> = 0.4V , V <sub>CC</sub> = 4.5V	7	40	-	%
	CTR (2)	I <sub>F</sub> =16mA V <sub>O</sub> = 0.5V , V <sub>CC</sub> = 4.5V	5	43	-	%
Logic (0) output voltage	V <sub>OL</sub>	* <sup>6</sup> V <sub>CC</sub> = 4.5V,I <sub>F</sub> =16mA	-	0.1	0.4	V
Logic (1) output current	I <sub>OH(1)</sub>	Ta= 25°C , I <sub>F</sub> =0 V <sub>O</sub> = V <sub>CC</sub> = 5.5V	-	3.0	500	nA
	I <sub>OH(2)</sub>	Ta= 25°C , I <sub>F</sub> =0 V <sub>O</sub> = V <sub>CC</sub> = 15V	-	0.01	1.0	uA
	I <sub>OH(3)</sub>	V <sub>CC</sub> = V <sub>O</sub> = 15V,I <sub>F</sub> =0	-	-	50	uA
Logic (0) supply current	I <sub>CCL</sub>	I <sub>F</sub> =16mA V <sub>O</sub> = open , V <sub>CC</sub> = 15V	-	200	-	uA
Logic (1) supply current	I <sub>CCH(1)</sub>	Ta= 25°C , I <sub>O</sub> =0 V <sub>F</sub> = open , V <sub>CC</sub> = 15V	-	0.02	1.0	uA
	I <sub>CCH(2)</sub>	I <sub>O</sub> =0 V <sub>O</sub> = open , V <sub>CC</sub> = 15V	-	-	2.0	uA
Input forward voltage	V <sub>F</sub>	Ta= 25°C , I <sub>F</sub> = 16mA	-	1.7	1.95	V
Input forward voltage temperature coefficient	ΔV <sub>F</sub> /ΔT <sub>A</sub>	I <sub>F</sub> = 16mA	-	-1.9	-	mV/°C
Input reverse voltage	B <sub>AR</sub>	Ta= 25°C , I <sub>R</sub> = 10uA	5.0	-	-	V
Input capacitance	C <sub>IN</sub>	V <sub>F</sub> =0 , f=1MHz	-	60	-	pF
* <sup>7</sup> Leak current(input-output)	I <sub>I-O</sub>	Ta= 25°C , 45 % RH V <sub>I-O</sub> =3kVDC , t = 5s	-	-	1.0	uA
* <sup>7</sup> Isolation resistance(input-output)	R <sub>I-O</sub>	V <sub>I-O</sub> =500VDC	-	10 <sup>12</sup>	-	Ω
* <sup>7</sup> Capacitance(input-output)	C <sub>I-O</sub>	f=1MHz	-	0.6	-	pF
Transistor current amplification factor	h <sub>FE</sub>	V <sub>O</sub> = 5V , I <sub>O</sub> = 3mA	-	70	-	

\*<sup>5</sup> Current transfer ratio is the ratio of input current and output current expressed in %\*<sup>6</sup> I<sub>O</sub> = 1.1mA\*<sup>7</sup> Measured as 2-pin element (Short 1,2,3,4 and 5,6,7,8)**Switching Characteristics**(Ta=25°C,V<sub>CC</sub>=5V,I<sub>F</sub>=16mA)

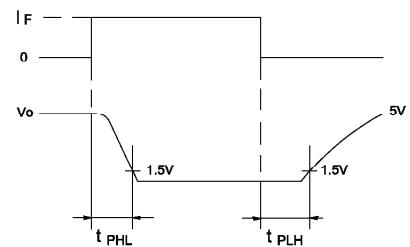
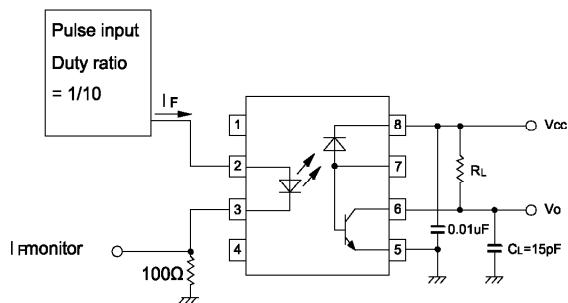
Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
* <sup>8</sup> Propagation delay time Output (1)-->(0)	t <sub>PHL</sub>	R <sub>L</sub> = 4.1kΩ	-	0.3	1.5	uS
* <sup>8</sup> Propagation delay time Output (0)-->(1)	t <sub>PLH</sub>	R <sub>L</sub> = 4.1kΩ	-	0.4	1.5	uS
* <sup>10</sup> Instantaneous common mode rejection voltage "Output (1)"	C <sub>MH</sub>	I <sub>F</sub> =0,V <sub>CM</sub> =10V <sub>p-p</sub>	-	1000	-	V/uS
* <sup>10</sup> Instantaneous common mode rejection voltage "Output (0)"	C <sub>ML</sub>	I <sub>F</sub> =16mA,V <sub>CM</sub> =10V <sub>p-p</sub>	-	-1000	-	V/uS
* <sup>12</sup> Bandwidth	BW	R <sub>L</sub> = 100Ω	-	2.0	-	MHz

\*<sup>8</sup> R = 4.1kΩ is equivalent to one LSTTL and 6.1kΩ pull-up resistor.\*<sup>10</sup> Instantaneous common mode rejection voltage "output(1)" representsa common mode voltage variation that can hold the output above (1) level (V<sub>O</sub> > 2.0V)

Instantaneous common mode rejection voltage "output(0)" represents

a common mode voltage variation that can hold the output above (0) level (V<sub>O</sub> < 0.8V)\*<sup>12</sup> Bandwidth represents a point where AC input goes down by 3dB.

\*9 Tset Circuit Propagation Delay Time



\*11 Tset Circuit for Instantaneous Common Mode Rejection Voltage

