## Features

- 75 Ohm Impedance
- Input Terminated
- Positive Voltage Control
- High Isolation: 65 dB at 870 MHz
- 0.5 micron GaAs PHEMT Process
- Lead-Free 4 mm 20-Lead PQFN Package
- 100\% Matte Tin Plating over Copper
- Halogen-Free "Green" Mold Compound
- RoHS* Compliant and $260^{\circ} \mathrm{C}$ Reflow Compatible


## Description

M/A-COM's MASWSS0103 is a GaAs PHEMT MMIC single pole double throw (SPDT) switch in a lead-free 4 mm 20-lead PQFN package. The MASWSS0103 is ideally suited for applications where low control voltage, high isolation, small size and low cost are required.

Typical applications are to replace mechanical relays in CATV systems. This part can be used in all $75 \Omega$ systems operating up to 1 GHz .

The MASWSS0103 is fabricated using a 0.5 micron gate length GaAs PHEMT process. The process features full passivation for performance and reliability.

## Ordering Information

| Part Number | Package |
| :---: | :---: |
| MASWSS0103TR-3000 | 13 inch, 3000 piece reel |
| MASWSS0103SMB | Sample Test Board <br> (Includes 5 Samples) |

Note: Reference Application Note M513 for reel size information.

Functional Schematic


## Pin Configuration ${ }^{1}$

| Pin No. | Pin Name | Description |
| :---: | :---: | :---: |
| 1 | GND | Ground |
| 2 | GND | Ground |
| 3 | RF1 | RF Port 1 |
| 4 | GND | Ground |
| 5 | GT1 | RF Ground |
| 6 | GI1 | RF Ground |
| 7 | GND | Ground |
| 8 | RFC | RF Common Port |
| 9 | GND | Ground |
| 10 | GI2 | RF Ground |
| 11 | GT2 | RF Ground |
| 12 | GND | Ground |
| 13 | RF2 | RF Port 2 |
| 14 | GND | Ground |
| 15 | GND | Ground |
| 16 | G2 | RF Ground |
| 17 | V1 | Control 1 |
| 18 | V2 | Control 2 |
| 19 | V1 | Control 1 |
| 20 | G1 | RF Ground |

1. The exposed pad centered on the package bottom must be connected to RF and DC ground.
[^0]- Asia/Pacific Tel: 81.44.844.8296 / Fax: 81.44.844.8298

Visit www.macom.com for additional data sheets and product information.

Electrical Specifications: $\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}, \mathrm{Z}_{0}=75 \Omega^{2}, \mathrm{~V}_{\mathrm{C}}=0 \mathrm{~V} / 2.9 \mathrm{~V}, \mathrm{P}_{\mathrm{IN}}=10 \mathrm{dBm}$

| Parameter | Test Conditions | Units | Min. | Tур. | Max. |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Insertion Loss | $\begin{gathered} 5-50 \mathrm{MHz} \\ 50-1000 \mathrm{MHz} \end{gathered}$ | $\begin{aligned} & \mathrm{dB} \\ & \mathrm{~dB} \end{aligned}$ | — | $\begin{gathered} 0.75 \\ 1.0 \end{gathered}$ | $\overline{1.2}$ |
| Isolation | $\begin{gathered} 5-50 \mathrm{MHz} \\ 50-1000 \mathrm{MHz} \end{gathered}$ | $\begin{aligned} & \mathrm{dB} \\ & \mathrm{~dB} \end{aligned}$ | $\overline{60}$ | $\begin{gathered} 100 \\ 63 \end{gathered}$ | - |
| Return Loss (On) | $\begin{gathered} 5-50 \mathrm{MHz} \\ 50-1000 \mathrm{MHz} \end{gathered}$ | $\begin{aligned} & \mathrm{dB} \\ & \mathrm{~dB} \end{aligned}$ | - | $\begin{aligned} & 28 \\ & 16 \end{aligned}$ | - |
| Return Loss (Off) | $\begin{gathered} 5-50 \mathrm{MHz} \\ 50-1000 \mathrm{MHz} \end{gathered}$ | $\begin{aligned} & \mathrm{dB} \\ & \mathrm{~dB} \end{aligned}$ | - | $\begin{aligned} & 26 \\ & 16 \end{aligned}$ | - |
| IP3 | $\begin{gathered} \text { Two Tone, }+15 \mathrm{dBm} / \text { tone, } 6 \mathrm{MHz} \text { spacing, }>50 \mathrm{MHz} \\ \mathrm{~V}_{\mathrm{C}}=0 \mathrm{~V} / 2.9 \mathrm{~V} \\ \mathrm{~V}_{\mathrm{C}}=0 \mathrm{~V} / 5.0 \mathrm{~V} \end{gathered}$ | dBm dBm | - | $\begin{aligned} & 47 \\ & 52 \end{aligned}$ | - |
| Trise, Tfall | 10\% to $90 \%$ RF, $90 \%$ to $10 \%$ RF | nS | - | 8 | - |
| Ton, Toff | 50\% control to 90\% RF, 50\% control to 10\% RF | nS | - | 18 | - |
| Transients | In Band | mV | - | 70 | - |
| Control Current | $\left\|\mathrm{V}_{\mathrm{C}}\right\|=2.9 \mathrm{~V}$ | $\mu \mathrm{A}$ | - | 5 | 10 |

2. External $0.01 \mu$ F DC blocking capacitors are required on all RF In/Out and RF ground ports. See Application Schematic.

## Absolute Maximum Ratings ${ }^{3,4}$

| Parameter | Absolute Maximum |
| :---: | :---: |
| Input Power | +32 dBm |
| $(5-1000 \mathrm{MHz}, 2.9 \mathrm{~V}$ Control $)$ | +8.5 volts |
| Operating Voltage | $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ |
| Operating Temperature | $-65^{\circ} \mathrm{C}$ to $+150^{\circ} \mathrm{C}$ |
| Storage Temperature |  |

3. Exceeding any one or combination of these limits may cause permanent damage.
4. M/A-COM does not recommend sustained operation near these survivability limits.

## Truth Table ${ }^{5}$

| V1 | V2 | RFC - RF1 | RFC - RF2 |
| :---: | :---: | :---: | :---: |
| 1 | 0 | On | Off |
| 0 | 1 | Off | On |

5. $1=+2.9$ to $+5 \mathrm{~V}, 0=0 \pm 0.2 \mathrm{~V}$.

## Application Schematic



C1-C9 $=0.01 \mu \mathrm{~F}$, RF Bypass Capacitors
C10-C11 = 0.01 $\mu \mathrm{F}$, Logic Control Decoupling Capacitors

- Europe Tel: 44.1908.574.200 / Fax: 44.1908.574.300
- Asia/Pacific Tel: 81.44.844.8296 / Fax: 81.44.844.8298

Visit www.macom.com for additional data sheets and product information.

## Typical Performance Curves

## Insertion Loss



RF1 Isolation


On Return Loss


RF2 Isolation


Off Return Loss


- Asia/Pacific Tel: 81.44.844.8296 / Fax: 81.44.844.8298

4Асеп
SPDT High Isolation CATV Switch
$5-1000 \mathrm{MHz}$

Lead-Free 4 mm 20-Lead PQFN ${ }^{\dagger}$

$\dagger$ Reference Application Note M538 for lead-free solder reflow recommendations.

## Handling Procedures

Please observe the following precautions to avoid damage:

## Static Sensitivity

Gallium Arsenide Integrated Circuits are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these devices.


[^0]:    * Restrictions on Hazardous Substances, European Union Directive 2002/95/EC.

