NC7SV17 TinyLogic® ULP-A Single Buffer with Schmitt Trigger Input



NC7SV17

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General Description

The NC7SV17 is a single buffer with Schmitt trigger input from Fairchild's Ultra Low Power-A (ULP-A) Series of TinyLogic®. ULP-A is ideal for applications that require extreme high speed, high drive and low power. This product is designed for a wide low voltage operating range (0.9V to 3.6V V_{CC}) and applications that require more drive and speed than the TinyLogic ULP series, but still offer best in class low power operation.

The NC7SV17 is uniquely designed for optimized power and speed, and is fabricated with an advanced CMOS technology to achieve high-speed operation while maintaining low CMOS power dissipation.

Features

- 0.9V to 3.6V V_{CC} supply operation
- 3.6V overvoltage tolerant I/O's at V_{CC} from 0.9V to 3.6V
- Extremely High Speed t_{PD}

1.5 ns typ for 2.7V to 3.6V $V_{\rm CC}$

1.8 ns typ for 2.3V to 2.7V V_{CC}

2.0 ns typ for 1.65V to 1.95V V_{CC}

3.2 ns typ for 1.4V to 1.6V V_{CC}

5.9 ns typ for 1.1V to 1.3V V_{CC}

12.0 ns typ for 0.9V V_{CC}

- Power-Off high impedance inputs and outputs
- High Static Drive (I_{OH}/I_{OL})

±24 mA @ 3.00V V_{CC}

±18 mA @ 2.30V V_{CC}

±6 mA @ 1.65V V_{CC}

±4 mA @ 1.4V V_{CC}

 ± 2 mA @ 1.1V V_{CC}

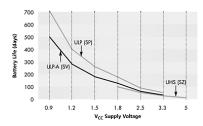
 $\pm 0.1 \text{ mA}$ @ 0.9V V_{CC}

- Uses patented Quiet Series[™] noise/EMI reduction circuitry
- Ultra small MicroPak™ leadfree package
- Ultra low dynamic power

Ordering Code:

Order Number	Package Number	Product Code Top Mark	Package Description	Supplied As
NC7SV17P5X	MAA05A	V17	5-Lead SC70, EIAJ SC-88a, 1.25mm Wide	3k Units on Tape and Reel
NC7SV17L6X	MAC06A	G5	6-Lead MicroPak, 1.0mm Wide	5k Units on Tape and Reel

Battery Life vs. V_{CC} Supply Voltage



TinyLogic ULP and ULP-A with up to 50% less power consumption can extend your battery life significantly. Battery Life = $(V_{battery}^{*})/(P_{device})/24hrs/day$

Where, $P_{device} = (I_{CC} * V_{CC}) + (C_{PD} + C_L) * V_{CC}^2 * f$

Assumes ideal 3.6V Lithium Ion battery with current rating of 900mAH and derated 90% and device frequency at 10MHz, with $C_L = 15 \, pF$ load

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Logic Symbol

IEEE/IEC

Pin Descriptions

Pin Name	Description
Α	Input
Y	Output
NC	No Connect

Function Table

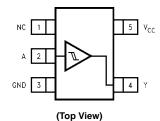
 $Y = \overline{A}$

Input	Output
Α	Y
L	L
Н	Н

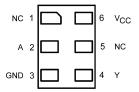
H = HIGH Logic Level L = LOW Logic Level

Connection Diagrams

Pin Assignments for SC70



Pad Assignments for MicroPak



(Top Thru View)

±24 mA

Absolute Maximum Ratings(Note 1)

Supply Voltage (V_{CC}) -0.5V to +4.6V DC Input Voltage (V_{IN}) -0.5V to +4.6V

DC Output Voltage (V_{OUT}) HIGH or LOW State (Note 2)

-0.5V to V_{CC} +0.5V $V_{CC} = 0V$ -0.5V to +4.6VDC Input Diode Current (I_{IK}) $V_{IN} < 0V$ ±50 mA

DC Output Diode Current (I_{OK})

-50 mA $V_{OUT} < 0V$ V_{OUT} > V_{CC} +50 mA DC Output Source/Sink Current (I_{OH}/I_{OL}) \pm 50 mA

DC V_{CC} or Ground Current per

Supply Pin (I_{CC} or Ground) \pm 50 mA Storage Temperature Range (T_{STG}) -65°C to +150°C

Recommended Operating Conditions (Note 3)

0.9V to 3.6V Supply Voltage 0V to 3.6V Input Voltage (V_{IN})

Output Voltage (V_{OUT})

 $V_{CC} = 0.0V$ 0V to 3.6V HIGH or LOW State 0V to V_{CC}

Output Current in I_{OH}/I_{OL}

 $V_{CC} = 3.0V$ to 3.6V $V_{CC} = 2.3V \text{ to } 2.7V$ ±18 mA $V_{CC} = 1.65V \text{ to } 1.95V$ ±6 mA $V_{CC} = 1.4V$ to 1.6V $\pm 4 \text{ mA}$

 $V_{CC} = 1.1V \text{ to } 1.3V$ ±2 mA $V_{CC} = 0.9V$ ±0.1 mA

Free Air Operating Temperature (T_A) -40°C to +85°C

Minimum Input Edge Rate (Δt/ΔV)

 $V_{IN} = 0.8V$ to 2.0V, $V_{CC} = 3.0V$ 10 ns/V

Note 1: Absolute Maximum Ratings: are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the Electrical Characteristics tables are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

Note 2: IO Absolute Maximum Rating must be observed.

Note 3: Unused inputs must be held HIGH or LOW. They may not float.

DC Electrical Characteristics

Symbol	Parameter	V _{CC}	T _A =	+25°C	T _A = -40°	C to +85°C	Units	Conditions
Cymbol	i ai ailletei	(V)	Min	Max	Min	Max	Units	Conditions
V _P	Positive Threshold Voltage	0.90	0.3	0.7	0.3	0.7		
		1.10	0.4	1.0	0.4	1.0		
		1.40	0.5	1.4	0.5	1.4	V	
		1.65	0.7	1.5	0.7	1.5	v	
		2.30	1.0	1.8	1.0	1.8		
		2.70	1.5	2.2	1.5	2.2		
V _N	Negative Threshold Voltage	0.90	0.10	0.6	0.10	0.6		
		1.10	0.15	0.7	0.15	0.7		
		1.40	0.20	0.8	0.20	8.0	V	
		1.65	0.25	0.9	0.25	0.9	v	
		2.30	0.4	1.15	0.4	1.15		
		2.70	0.6	1.5	0.6	1.5		
V _H	Hysteresis Voltage	0.90	0.07	0.5	0.07	0.5		
		1.10	0.08	0.6	0.08	0.6		
		1.40	0.09	8.0	0.09	8.0	V	
		1.65	0.15	1.0	0.15	1.0	v	
		2.30	0.25	1.1	0.25	1.1		
		2.70	0.60	1.2	0.60	1.2		

DC Electrical Characteristics (Continued)

Symbol	Parameter	V _{CC}	T _A = ∃	-25°C	$T_A = -40^{\circ}C \text{ to } +85^{\circ}C$		Units	Conditions
Syllibol	Parameter	(V)	Min	Max	Min	Max	Ullis	Conditions
/он	HIGH Level	0.90	V _{CC} - 0.1		V _{CC} - 0.1			
	Output Voltage	$1.10 \le V_{CC} \le 1.30$	V _{CC} - 0.1		V _{CC} - 0.1			
		$1.40 \le V_{CC} \le 1.60$	V _{CC} - 0.2		V _{CC} - 0.2			
		$1.65 \le V_{CC} \le 1.95$	V _{CC} - 0.2		V _{CC} - 0.2			$I_{OH} = -100 \mu A$
		$2.30 \le V_{CC} < 2.70$	V _{CC} - 0.2		V _{CC} - 0.2			
		$2.70 \le V_{CC} \le 3.60$	V _{CC} - 0.2		V _{CC} - 0.2			
		$1.10 \le V_{CC} \le 1.30$	0.75 x V _{CC}		0.75 x V _{CC}			$I_{OH} = -2 \text{ mA}$
		$1.40 \le V_{CC} \le 1.60$	0.75 x V _{CC}		0.75 x V _{CC}		V	$I_{OH} = -4 \text{ mA}$
		$1.65 \le V_{CC} \le 1.95$	1.25		1.25			
		$2.30 \le V_{CC} < 2.70$	2.0		2.0			$I_{OH} = -6 \text{ mA}$
		$2.30 \le V_{CC} < 2.70$	1.8		1.8			I _{OH} = -12 mA
		$2.70 \leq V_{CC} \leq 3.60$	2.2		2.2			
		$2.30 \le V_{CC} < 2.70$	1.7		1.7			I _{OH} = -18 mA
		$2.70 \le V_{CC} \le 3.60$	2.4		2.4			10H = -16 IIIA
		$2.70 \leq V_{CC} \leq 3.60$	2.2		2.2			$I_{OH} = -24 \text{ mA}$
OL.	LOW Level	0.90		0.1		0.1		
	Output Voltage	$1.10 \le V_{CC} \le 1.30$		0.1		0.1		
		$1.40 \le V_{CC} \le 1.60$		0.2		0.2		I _{OL} = 100 μA
		$1.65 \leq V_{CC} \leq 1.95$		0.2		0.2		
		$2.30 \le V_{CC} < 2.70$		0.2		0.2		
		$2.70 \leq V_{CC} \leq 3.60$		0.2		0.2		
		$1.10 \le V_{CC} \le 1.30$		0.25 x V _{CC}		0.25 x V _{CC}	V	$I_{OL} = 2 \text{ mA}$
		$1.40 \le V_{CC} \le 1.60$		0.25 x V _{CC}		0.25 x V _{CC}	v	I _{OL} = 4 mA
		$1.65 \le V_{CC} \le 1.95$		0.3		0.3		I _{OL} = 6 mA
		$2.30 \le V_{CC} < 2.70$		0.4		0.4		I _{OL} = 12 mA
		$2.70 \leq V_{CC} \leq 3.60$		0.4		0.4		I _{OL} = 12 IIIA
		$2.30 \le V_{CC} < 2.70$		0.6		0.6		I _{OL} = 18 mA
		$2.70 \leq V_{CC} \leq 3.60$		0.4		0.4		IOL = TO THA
		$2.70 \le V_{CC} \le 3.60$		0.55		0.55		I _{OL} = 24 mA
IN	Input Leakage Current	0.90 to 3.60		±0.1		±0.5	μΑ	$0 \le V_I \le 3.6V$
OFF	Power Off Leakage Current	0		0.5		0.5	μΑ	$0 \le (V_I, V_O) \le 3.6$
CC	Quiescent Supply Current	0.90 to 3.60		0.9		0.9	^	$V_I = V_{CC}$ or GND
		0.90 to 3.60				±0.9	μA	$V_{CC} \le V_I \le 3.6V$

AC Electrical Characteristics

Symbol	Parameter	v _{cc}	T _A = +25°C		$T_A = -40^{\circ}C \text{ to } +85^{\circ}C$		Units	Conditions	Figure	
Cymbol	i arameter	(V)	Min	Тур	Max	Min	Max	Oille	N	Number
t _{PHL}	Propagation Delay	0.90		12					$C_L = 15 \text{ pF}, R_L = 1 \text{ M}\Omega$	
t _{PLH}		$1.10 \le V_{CC} \le 1.30$	2.0	5.9	10.0	1.0	14.9		$C_L = 15 pF,$	
		$1.40 \leq V_{CC} \leq 1.60$	1.0	3.2	6.1	0.9	7.0	ns	$R_L=2\;k\Omega$	Figures
		$1.65 \le V_{CC} \le 1.95$	1.0	2.0	5.2	0.7	6.2	115	C _L = 30 pF	1, 2
		$2.30 \leq V_{CC} < 2.70$	8.0	1.8	3.7	0.6	4.4		$R_L=500\Omega$	
		$2.70 \leq V_{CC} \leq 3.60$	0.7	1.5	3.3	0.5	3.8			
C _{IN}	Input Capacitance	0		2.0				pF		
C _{OUT}	Output Capacitance	0		4.5				pF		
C _{PD}	Power Dissipation	0.90 to 3.60		10				pF	$V_I = 0V \text{ or } V_{CC}$	
	Capacitance	0.90 to 3.00		10				рі	f = 10 MHz	

AC Loading and Waveforms

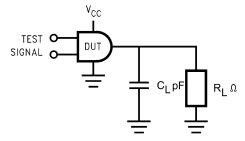


FIGURE 1. AC Test Circuit

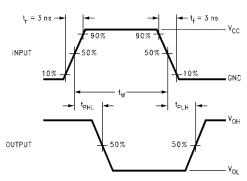


FIGURE 2. AC Waveforms

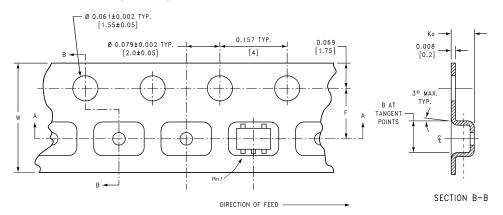
Symbol	V _{CC}							
0,	$3.3V \pm 0.3V$	$2.5V \pm 0.2V$	$1.8V \pm 0.15V$	$1.5V \pm 0.10V$	$1.2V \pm 0.10V$	0.9V		
V _{mi}	1.5V	V _{CC} /2						
V _{mo}	1.5V	V _{CC} /2						

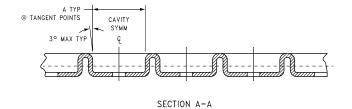
Tape and Reel Specification

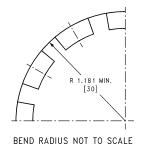
TAPE FORMAT for SC70

1741 = 1 014111741 101 4	30.0			
Package	Tape	Number	Cavity	Cover Tape
Designator	Section	Cavities	Status	Status
	Leader (Start End)	125 (typ)	Empty	Sealed
P5X	Carrier	3000	Filled	Sealed
	Trailer (Hub End)	75 (typ)	Empty	Sealed

TAPE DIMENSIONS inches (millimeters)







Tape and Reel Specification (Continued) TAPE FORMAT for MicroPak Package Tape Number Cavity Cover Tape Status Designator Section Cavities Status Leader (Start End) Sealed 125 (typ) Empty L6X Carrier 5000 Filled Sealed Trailer (Hub End) 75 (typ) **Empty** Sealed TAPE DIMENSIONS inches (millimeters) 1.75±0.10 3.50±0.05 8.00 +0.30 -0.10 1.15±0.05 ∽ø 0.50 ±0.05 SECTION B-B SCALE:10X DIRECTION OF FEED-0.254±0.020 ┌ 0.70±0.05 5° MAX 1.60±0.05 SECTION A-A **REEL DIMENSIONS** inches (millimeters) TAPE SLOT DETAIL X **DETAIL X** SCALE: 3X W1 W2 W3 Tape В С D N 7.0 0.059 0.512 0.795 2.165 0.331 + 0.059/-0.000 0.567 W1 + 0.078/-0.039 8 mm (177.8)(1.50)(13.00)(20.20)(55.00)(8.40 + 1.50 / -0.00)(14.40)(W1 + 2.00/-1.00)

NOTES:

- A. CONFORMS TO EIAJ REGISTERED OUTLINE DRAWING SC88A.
- B. DIMENSIONS DO NOT INCLUDE BURRS OR MOLD FLASH.
- C. DIMENSIONS ARE IN MILLIMETERS.

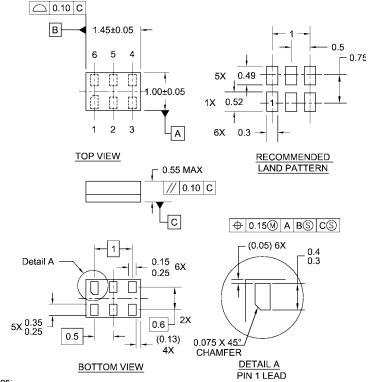
MAA05ARevC

-- 0.425 NOMINAL

DETAIL A

5-Lead SC70, EIAJ SC-88a, 1.25mm Wide Package Number MAA05A

Physical Dimensions inches (millimeters) unless otherwise noted (Continued)



Notes:

- 1. JEDEC PACKAGE REGISTRATION IS ANTICIPATED 2. DIMENSIONS ARE IN MILLIMETERS
- 3. DRAWING CONFORMS TO ASME Y14.5M-1994

MAC06ARevB

6-Lead MicroPak, 1.0mm Wide Package Number MAC06A

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Datasheet Identification	Product Status	Definition
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