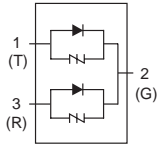


## Twin SLIC Protector



Subscriber Line Interface Circuits (SLIC) are highly susceptible to transient voltages, such as lightning and power cross conditions. To minimize this threat, Teccor provides this dual-chip, fixed-voltage SLIC protector device.

For specific design criteria, see details in Figure 3.29.

### Electrical Parameters

Part Number *	V <sub>DRM</sub> Volts	V <sub>S</sub> Volts	V <sub>T</sub> Volts	V <sub>F</sub> Volts	I <sub>DRM</sub> $\mu$ Amps	I <sub>S</sub> mAmps	I <sub>T</sub> Amps	I <sub>H</sub> mAmps	C <sub>O</sub> pF
	Pins 1-2, 3-2								
P0641CA2	58	77	4	5	5	800	1	120	60
P0721CA2	65	88	4	5	5	800	1	120	60
P0901CA2	75	98	4	5	5	800	1	120	60
P1101CA2	95	130	4	5	5	800	1	120	60

\* For surge ratings, see table below.

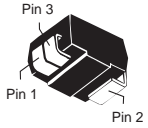
#### General Notes:

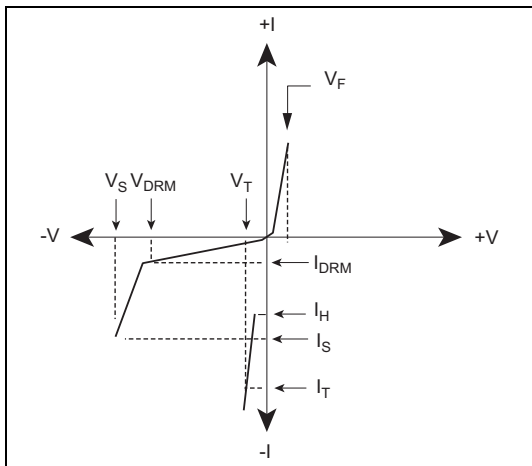
- All measurements are made at an ambient temperature of 25 °C. I<sub>PP</sub> applies to -40 °C through +85 °C temperature range.
- I<sub>PP</sub> is a repetitive surge rating and is guaranteed for the life of the product.
- V<sub>DRM</sub> is measured at I<sub>DRM</sub>.
- V<sub>S</sub> and V<sub>F</sub> are measured at 100 V/ $\mu$ s.
- Special voltage (V<sub>S</sub> and V<sub>DRM</sub>) and holding current (I<sub>H</sub>) requirements are available upon request.
- Off-state capacitance is measured across pins 1-2 or 3-2 at 1 MHz with a 2 V bias. Capacitance across pins 1-3 is approximately half.
- Parallel capacitive loads may affect electrical parameters.
- Compliance with GR 1089 or UL 60950 power cross tests may require special design considerations. Contact the factory for further information.

### Surge Ratings (Preliminary Data)

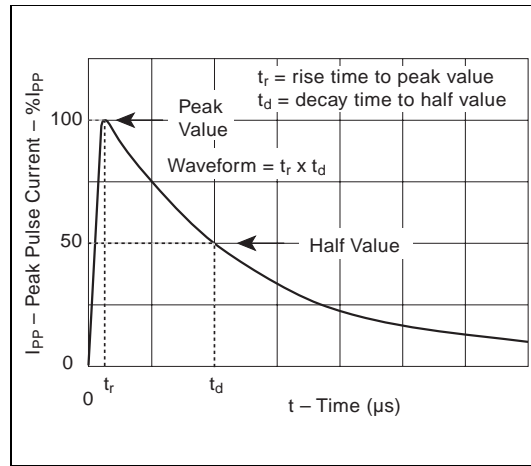
Series	I <sub>PP</sub> 2x10 $\mu$ s Amps	I <sub>PP</sub> 8x20 $\mu$ s Amps	I <sub>PP</sub> 10x160 $\mu$ s Amps	I <sub>PP</sub> 10x560 $\mu$ s Amps	I <sub>PP</sub> 10x1000 $\mu$ s Amps	I <sub>TSM</sub> 60 Hz Amps	di/dt Amps/ $\mu$ s
A	150	150	90	50	45	20	500

Thermal Considerations

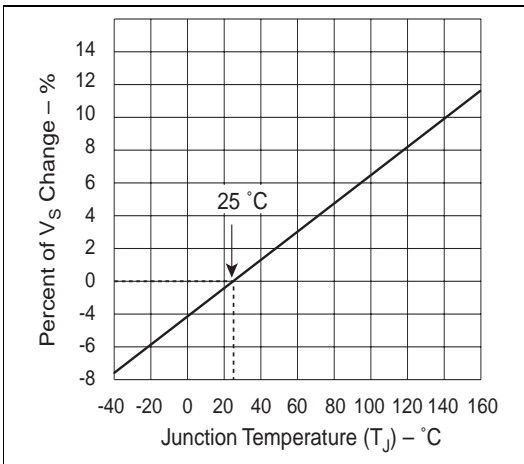
Package	Symbol	Parameter	Value	Unit
Modified DO-214AA 	$T_J$	Operating Junction Temperature Range	-40 to +150	$^{\circ}\text{C}$
	$T_S$	Storage Temperature Range	-65 to +150	$^{\circ}\text{C}$
	$R_{\theta JA}$	Thermal Resistance: Junction to Ambient	85	$^{\circ}\text{C/W}$



V-I Characteristics



$t_r \times t_d$  Pulse Wave-form



Normalized  $V_S$  Change versus Junction Temperature



Normalized DC Holding Current versus Case Temperature

Data Sheets