

ELECTRICAL SPECIFICATIONS:

1.0 TURNS RATIO: (P5-P6-P4) : (J6-J3) : 1CT : 1CT $\pm 2\%$ (P3-P1-P2) : (J2-J1) : 1CT : 1CT $\pm 2\%$

2.0 INDUCTANCE: (P3-P2); (P5-P4) : 350 uH MIN. @ 0.1V, 100KHz, 8 mA DC BIAS

3.0 LEAKAGE INDUCTANCE: P3-P2 (WITH J1 AND J2 SHORT) : 0.3uH MAX. @ 1MHz P5-P4 (WITH J6 AND J3 SHORT) : 0.3uH MAX. @ 1MHz

4.0 INTERWINDING CAPACITANCE: (P3-P2): (J1-J2) : 30pf MAX @ 1MHz (P5-P4): (J6-J3) : 30pf MAX @ 1MHz

5.0 DC RESISTANCE: (J6-J3); (J2-J1) : 1.2 ohms Max.

Bel Stewart Connector
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DRAWING NO.

SI-53006

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7.0 DIELECTRIC WITHSTAND: (J1,J2) TO (P10,P12); (J5,J4) TO (P3-P2) (J3,J6) TO (P5,P4); (J8,J7) TO (P9, P8) : 1500 VAC

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8.0 INSERTION LOSS: RS=RL=100 ohms

100KHz TO 125MHz : -1.1 dB TYP

9.0 RISE TIME: RS=100 OHMS AND RL = 100 OHMS

OUTPUT VOLTAGE = 1 V peak: 3.0 nS MAX

PULSE WIDTH= 112nS : 3.0 nS MAX

10.0 CROSS TALK: 1-100 MHz : -35dB TYP

11.0 COMMON TO COMMON MODE ATTENUATION: 1MHz TO 100MHz : -35dB TYP

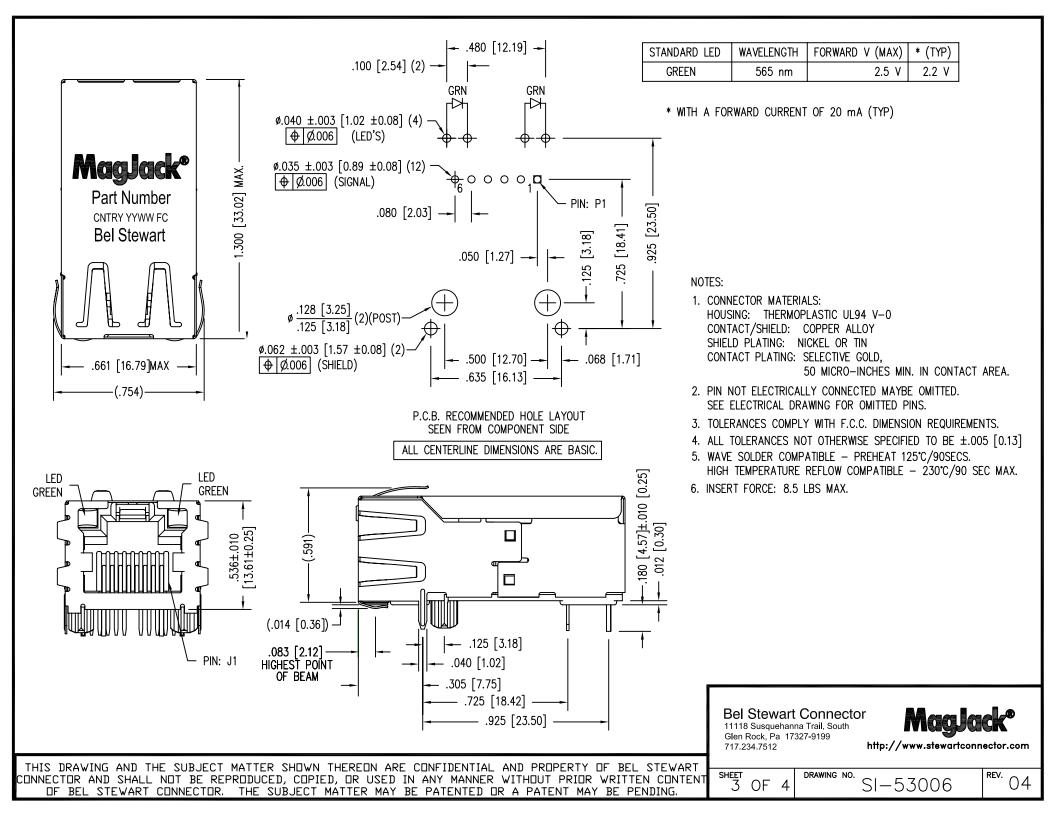
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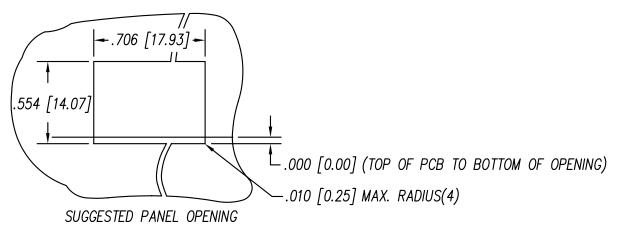
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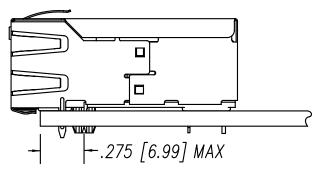
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- 1. THE SUGGESTED PANEL OPENING IS INTENDED TO GIVE THE USER THE ABILITY TO HAVE REASONABLE JACK / PANEL CLEARANCES YET MAINTAIN RELIABLE GROUNDING CAPABILITY.
- 2. ALL TOLERANCES NOT OTHERWISE SPECIFIED TO BE ±.005 [0.13]

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