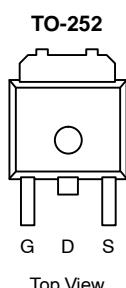


N-Channel 30-V (D-S) 175°C MOSFET

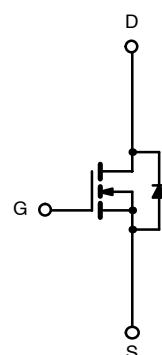
PRODUCT SUMMARY		
V_{DS} (V)	$r_{DS(on)}$ (Ω)	I_D (A) ^a
30	0.0043 @ $V_{GS} = 10$ V	33
	0.0065 @ $V_{GS} = 4.5$ V	27



Drain Connected to Tab

Top View

Ordering Information: SUD70N03-04P



N-Channel MOSFET

FEATURES

- TrenchFET® Power MOSFET
- 175°C Junction Temperature
- Optimized for Low-Side Synchronous Rectifier Operation
- 100% R_g Tested

APPLICATIONS

- DC/DC Converters
- Synchronous Rectifiers

ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$ UNLESS OTHERWISE NOTED)				
Parameter		Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}		30	V
Gate-Source Voltage		V_{GS}	± 20	
Continuous Drain Current ^a	$T_A = 25^\circ\text{C}$	I_D	33	A
	$T_C = 25^\circ\text{C}$		70 ^b	
Pulsed Drain Current		I_{DM}	100	
Continuous Source Current (Diode Conduction) ^a		I_S	8.3 ^a	
Maximum Power Dissipation	$T_C = 25^\circ\text{C}$	P_D	88	W
	$T_A = 25^\circ\text{C}$		8.3 ^a	
Operating Junction and Storage Temperature Range		T_J, T_{stg}	-55 to 175	°C

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient ^a	$t \leq 10$ sec	R_{thJA}	15	18	°C/W
	Steady State		40	50	
Maximum Junction-to-Case		R_{thJC}	1.2	1.5	

Notes

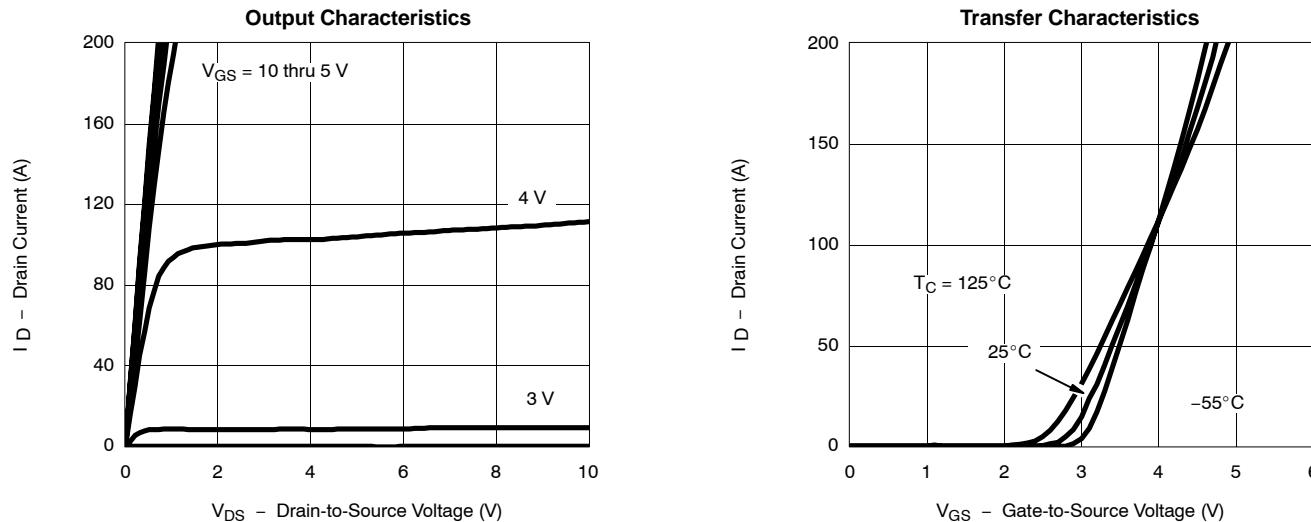
- a. Surface Mounted on FR4 Board, $t \leq 10$ sec.
- b. Limited by package.

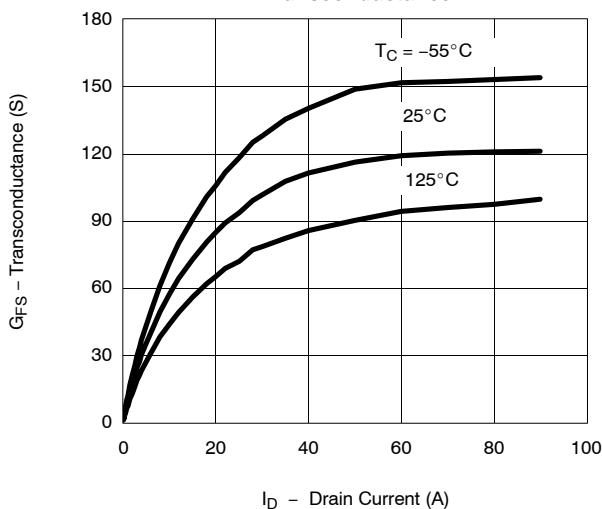
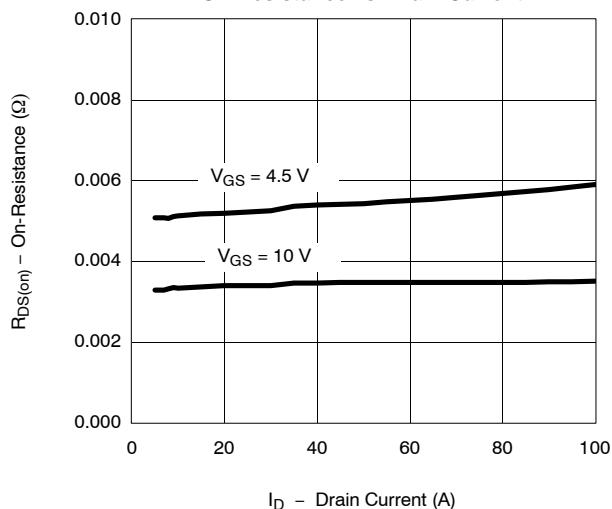
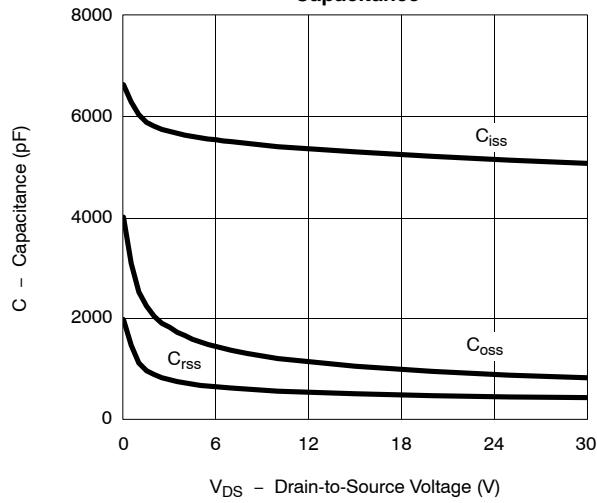
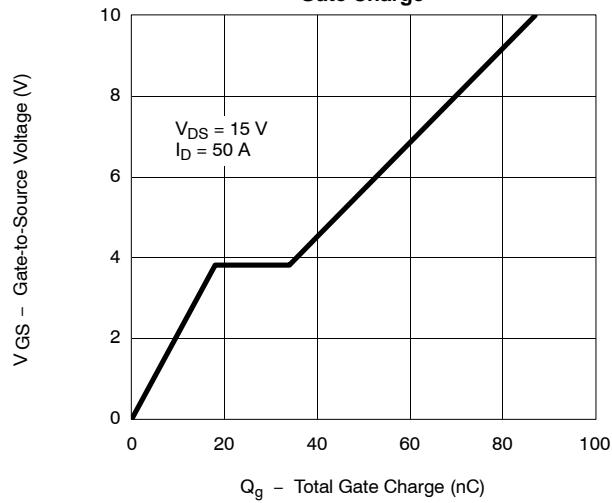
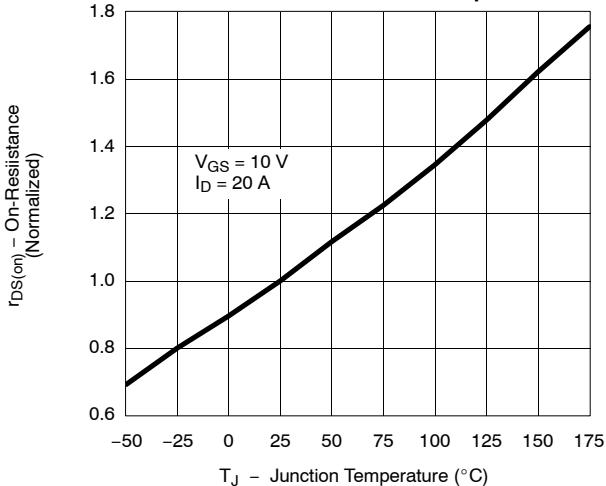
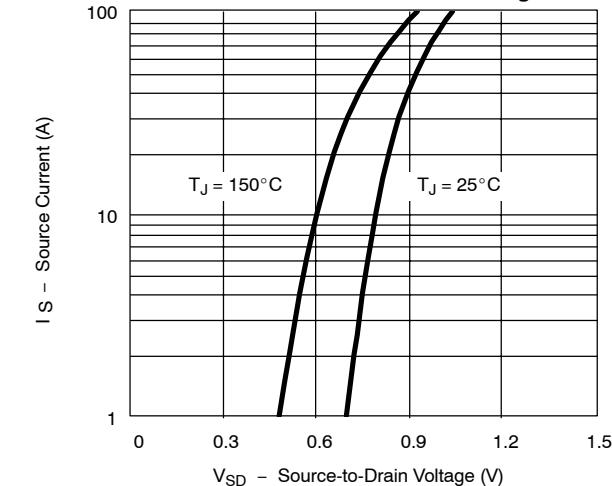
SPECIFICATIONS ($T_J = 25^\circ\text{C}$ UNLESS OTHERWISE NOTED)

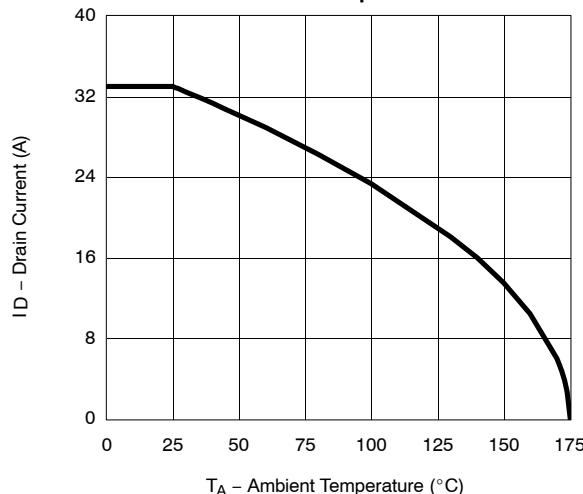
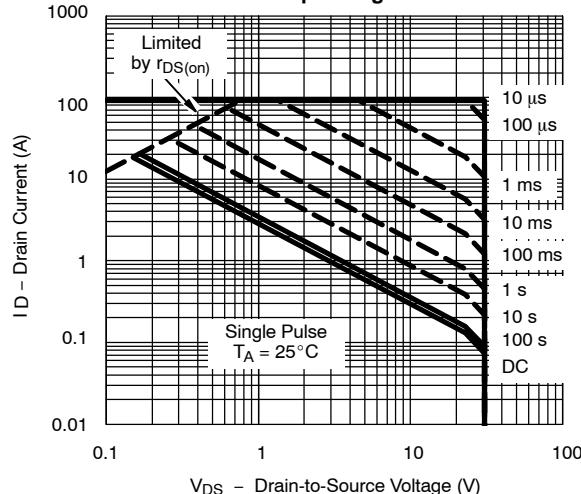
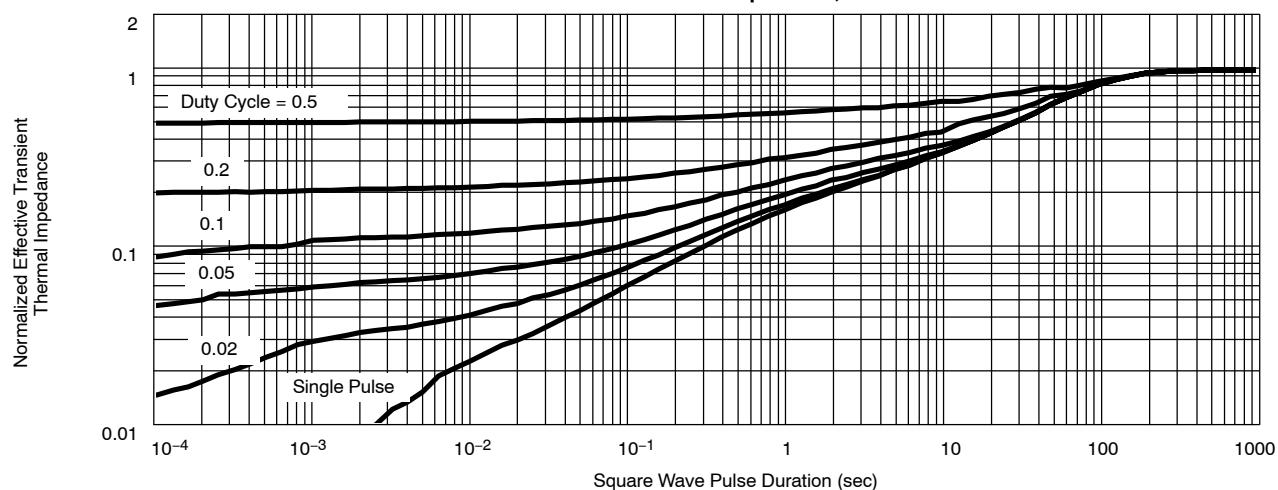
Parameter	Symbol	Test Condition	Min	Typ ^a	Max	Unit
Static						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	30			V
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}, I_D = 250 \mu\text{A}$	1.0		3.0	
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}$		1		μA
		$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}, T_J = 125^\circ\text{C}$		50		
On-State Drain Current ^b	$I_{D(\text{on})}$	$V_{DS} = 5 \text{ V}, V_{GS} = 10 \text{ V}$	50			A
Drain-Source On-State Resistance ^b	$r_{DS(\text{on})}$	$V_{GS} = 10 \text{ V}, I_D = 20 \text{ A}$		0.0035	0.0043	Ω
		$V_{GS} = 10 \text{ V}, I_D = 20 \text{ A}, T_J = 125^\circ\text{C}$			0.007	
		$V_{GS} = 4.5 \text{ V}, I_D = 20 \text{ A}$		0.0051	0.0065	
Forward Transconductance ^b	g_{fs}	$V_{DS} = 15 \text{ V}, I_D = 20 \text{ A}$	20			S
Dynamic^a						
Input Capacitance	C_{iss}	$V_{GS} = 0 \text{ V}, V_{DS} = 25 \text{ V}, f = 1 \text{ MHz}$		5100		pF
Output Capacitance	C_{oss}			860		
Reverse Transfer Capacitance	C_{rss}			430		
Gate Resistance	R_g	$f = 1 \text{ MHz}$	0.5	1.0	1.5	Ω
Total Gate Charge ^c	Q_g	$V_{DS} = 15 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 50 \text{ A}$		90	135	nC
Gate-Source Charge ^c	Q_{gs}			18		
Gate-Drain Charge ^c	Q_{gd}			16		
Turn-On Delay Time ^c	$t_{d(on)}$	$V_{DD} = 15 \text{ V}, R_L = 0.3 \Omega$ $I_D \approx 50 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 2.5 \Omega$		12	20	ns
Rise Time ^c	t_r			12	20	
Turn-Off Delay Time ^c	$t_{d(off)}$			40	60	
Fall Time ^c	t_f			10	15	
Source-Drain Diode Ratings and Characteristic ($T_C = 25^\circ\text{C}$)						
Pulsed Current	I_{SM}				100	A
Diode Forward Voltage ^b	V_{SD}	$I_F = 100 \text{ A}, V_{GS} = 0 \text{ V}$		1.2	1.5	V
Source-Drain Reverse Recovery Time	t_{rr}	$I_F = 50 \text{ A}, di/dt = 100 \text{ A}/\mu\text{s}$		40	80	ns

Notes

- a. Guaranteed by design, not subject to production testing.
- a. Pulse test; pulse width $\leq 300 \mu\text{s}$, duty cycle $\leq 2\%$.
- a. Independent of operating temperature.

TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)

TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)
Transconductance

On-Resistance vs. Drain Current

Capacitance

Gate Charge

On-Resistance vs. Junction Temperature

Source-Drain Diode Forward Voltage


THERMAL RATINGS
**Maximum Avalanche and Drain Current vs.
Ambient Temperature**

Safe Operating Area

Normalized Thermal Transient Impedance, Junction-to-Ambient

Normalized Thermal Transient Impedance, Junction-to-Case
