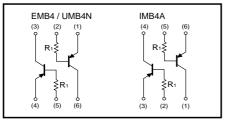
General purpose (dual digital transistors) EMB4/UMB4N/IMB4A

●Feature

1) Two DTA114T chips in a EMT or UMT or SMT package.

Equivalent circuits



Package, marking, and packaging specifications

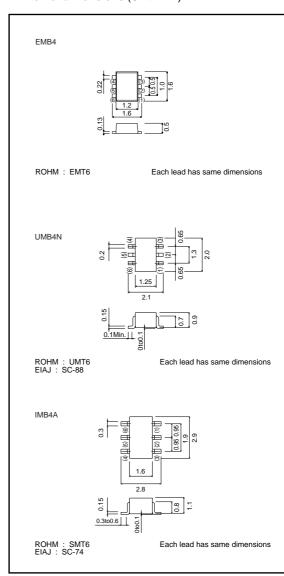
Туре	EMB4	UMB4N	IMB4A
Package	EMT6	UMT6	SMT6
Marking	B4	B4	B4
Code	T2R	TN	T110
Basic ordering unit (pieces)	8000	3000	3000

● Absolute maximum ratings (Ta=25°C)

Parameter		Symbol	Limits	Unit	
Collector-base voltage		Vсво	-50	V	
Collector-emitter voltage		Vceo	-50	V	
Emitter-base voltage		VEBO	-5	V	
Collector current		lc	-100	mA	
Power dissipation	EMB4 / UMB4N	Pd	150(TOTAL)	mW *1 *2	
	IMB4A	ı u	300(TOTAL)		
Junction temperature		Tj	150	°C	
Storage temperature		Tstg	-55 to +150	°C	

^{*1 120}mW per element must not be exceeded. *2 200mW per element must not be exceeded.

●External dimensions (Unit: mm)



●Electrical characteristics (Ta=25°C)

<u> </u>	<u> </u>					
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Collector-base breakdown voltage	ВУсво	-50	-	-	V	Ic=-50μA
Collector-emitter breakdown voltage	BVceo	-50	-	-	V	Ic=-1mA
Emitter-base breakdown voltage	BVEBO	-5	-	-	V	Iε=-50μA
Collector cutoff current	Ісво	-	-	-0.5	μΑ	Vcb=-50V
Emitter cutoff current	Ієво	-	-	-0.5	μΑ	V _{EB} =-4V
Collector-emitter saturation voltage	VcE(sat)	-	-	-0.3	V	Ic/I _B =-10mA/-1mA
DC current transfer ratio	hfe	100	250	600	-	Vce=-5V, Ic=-1mA
Transition frequency	fτ	-	250	-	MHz	Vce=-10V, Ie=5mA, f=100MHz *
Input resistance	R ₁	7	10	13	kΩ	_

^{*}Transition frequency of the device.

•Electrical characteristics curves

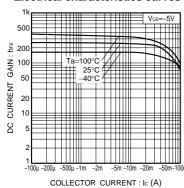


Fig.1 DC current gain vs. collector current

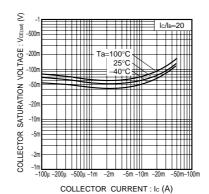


Fig.2 Collector-emitter saturation voltage vs. collector current

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