

SPB-5780W-1590G / SPB-5780BW-1590G

(RoHS Compliant)

SPB-5780LW-1590G / SPB-5780BLW-1590G

1590 nm TX / 1510 nm RX , 3.3V / 622 Mbps Digital Diagnostic Single-Fiber SFP Transceiver**FEATURES**

- | 1-Fiber Bi-Directional SFP Optical Transceiver
- | Simplex SC Connector: SPB-5780W-1590G
- | Simplex LC Connector: SPB-5780LW-1590G
- | **SFF-8472 Digital Diagnostic Function**
- | 1590 nm DFB LD Transmitter
- | 1510 nm Receiver
- | Distance Up to 80 km
- | AC/AC Coupling according to MSA
- | Single +3.3 V Power Supply
- | RoHS Compliant
- | 0 to 70°C Operating : SPB-5780W-1590G
- | -10 to 85°C Operating : SPB-5780BW-1590G
- | Class 1 Laser International Safety Standard IEC 60825 Compliant

APPLICATIONS

- | SONET/SDH Equipment Interconnect
- | Fibre Channel Links

DESCRIPTION

The SPB-5780W-1590G series is small form factor pluggable module for OC-12/STM-4 SONET/SDH and Fibre Channel single fiber communications by using 1590 nm transmitter and 1510 nm receiver. It is with the SFP 20-pin connector to allow hot plug capability. Digital diagnostic functions are available via an I²C series bus specified in the SFP MSA SFF-8472. The transmitter section uses a multiple quantum well 1590 nm laser and is a class 1 laser compliant according to International Safety Standard IEC 60825. The receiver section uses an integrated 1510 nm detector preamplifier (IDP) mounted in an optical header and a limiting post-amplifier IC.

LASER SAFETY

This single mode transceiver is a Class 1 laser product. It complies with IEC 60825 and FDA 21 CFR 1040.10 and 1040.11. The transceiver must be operated within the specified temperature and voltage limits. The optical ports of the module shall be terminated with an optical connector or with a dust plug.

ORDER INFORMATION

P/No.	Bit Rate (Mb/s)	Distance (km)	TX (nm)	RX (nm)	Package	Temp (°C)	TX Power (dBm)	RX Sens. (dBm)	RoHS Compliant
SPB-5780W-1590G	622	80	1590 DFB	1510	SC SFP with DMI	0 to 70	2 to -3	-28	Yes
SPB-5780BW-1590G	622	80	1590 DFB	1510	SC SFP with DMI	-10 to 85	2 to -3	-28	Yes
SPB-5780LW-1590G	622	80	1590 DFB	1510	LC SFP with DMI	0 to 70	2 to -3	-28	Yes
SPB-5780BLW-1590G	622	80	1590 DFB	1510	LC SFP with DMI	-10 to 85	2 to -3	-28	Yes

Absolute Maximum Ratings					
Parameter	Symbol	Min	Max	Units	Notes
Storage Temperature	Tstg	-40	85	°C	
Operating Case Temperature	Topr	0 -10	70 85	°C	SPB-5780W-1590G SPB-5780BW-1590G
Power Supply Voltage	Vcc	0	3.6	V	
Input Voltage	---	GND	Vcc	V	
Output Current	Iout	0	30	mA	

Recommended Operating Conditions					
Parameter	Symbol	Min	Typ	Max	Units / Notes
Power Supply Voltage	Vcc	3.1	3.3	3.5	V
Power Supply Current	I _{CC(TX+RX)}		200	300	mA
Operating Case Temperature	Topr	0 -10		70 85	°C / SPB-5780W-1590G °C / SPB-5780BW-1590G
Data Rate			622	700	Mb/s

Transmitter Specifications (0°C < Topr < 70°C, 3.1V < Vcc < 3.5V)						
Parameter	Symbol	Min	Typ	Max	Units	Notes
Optical						
Optical Transmit Power	Po	-3	---	2	dBm	1
Output Center Wavelength	λ	1580		1620	nm	
Output Spectrum Width	Δλ	---	---	1	nm	-20 dB width
Side Mode Suppression Ratio	SMSR	30			dB	
Extinction Ratio	E _R	10	---	---	dB	
Output Eye	Compliant with Bellcore GR-253-CORE and ITU recommendation G.957					
Optical Rise Time	t _r			1.2	ns	10% to 90% Values
Optical Fall Time	t _f			1.2	ns	10% to 90% Values
Relative Intensity Noise	RIN			-116	dB/Hz	
Total Jitter	TJ			0.55	ns	2
Electrical						
Data Input Current – Low	I _{IL}	-350			μA	
Data Input Current – High	I _{IH}			350	μA	
Differential Input Voltage	V _{IH} - V _{IL}	0.5		2.4	V	Peak-to-Peak
TX Disable Input Voltage – Low	T _{DIS, L}	0		0.5	V	3
TX Disable Input Voltage – High	T _{DIS, H}	2.0		Vcc	V	3
TX Disable Assert Time	T _{ASSERT}			10	μs	
TX Disable Deassert Time	T _{DEASSERT}			1	ms	
TX Fault Output Voltage -- Low	T _{FaultL}	0		0.5	V	4
TX Fault Output Voltage -- High	T _{FaultH}	2.0		Vcc+0.3	V	4

Notes: 1. Output power is power coupled into a 9/125 μm single mode fiber.

2. Measured with a 2²³-1 PRBS.

3. There is an internal 4.7K to 10K ohm pull-up resistor to VccTX.

4. Open collector compatible, 4.7K to 10K ohm pull-up to Vcc (Host Supply Voltage).

Optical

Electrical

7. Open collector compatible, 4.7K to 10K ohm pull-up to Vcc (Host Supply Voltage).

The diagram illustrates the internal architecture of an SFP module and its connections to an external SerDes IC and a Protocol IC. The SFP module contains an LD Driver, a POST Amp, and an EEPROM. The SerDes IC handles the high-speed data signals, while the Protocol IC manages the control and management signals.

SFP module components and pins:

- LD Driver:** Connected to VeeT (pins 1, 17, 20) and Tx_DISABLE (pin 3). It drives the TD+ (pin 18) and TD- (pin 19) signals.
- POST Amp:** Receives the LOS (pin 8) signal and drives the RD+ (pin 13) and RD- (pin 12) signals.
- EEPROM:** Contains MOD_DEF(0) (pin 6), MOD_DEF(1) (pin 5), and MOD_DEF(2) (pin 4) signals.
- Other pins:** VeeR (pins 9, 10, 11, 14), Tx_FAULT (pin 2), VccT (pin 16), VccR (pin 15), and LOS (pin 8).

External components and connections:

- Termination:** 4.7K to 10KΩ resistors are connected to Tx_DISABLE (pin 3), Tx_FAULT (pin 2), and the LOS (pin 8) signal.
- Impedance Matching:** 50Ω impedances (Z=50Ω) are used for the TD+ (pin 18), TD- (pin 19), RD+ (pin 13), and RD- (pin 12) signals.
- Power and Biasing:** Vcc 3.3V is connected to the VccT (pin 16) and VccR (pin 15) pins. Biasing networks with 10nF capacitors and 10μF inductors are used for the TD+ and TD- signals.
- Signal Conditioning:** A 100Ω resistor is connected to the RD+ (pin 13) signal.

Protocol IC: The Protocol IC is connected to the SerDes IC and the SFP module. It handles the LOS (pin 8) signal and the MOD_DEF(0) (pin 6), MOD_DEF(1) (pin 5), and MOD_DEF(2) (pin 4) signals.

CONNECTION DIAGRAM



PIN	Signal Name	Description	PIN	Signal Name	Description
1	TX GND	Transmitter Ground	11	RX GND	Receiver Ground
2	TX Fault	Transmitter Fault Indication	12	RX DATA OUT-	Inverse Receiver Data Out
3	TX Disable	Transmitter Disable (Module disables on high or open)	13	RX DATA OUT+	Receiver Data Out
4	MOD-DFE2	Modulation Definition 2 – Two wires serial ID Interface	14	RX GND	Receiver Ground
5	MOD-DEF1	Modulation Definition 1 – Two wires serial ID Interface	15	Vcc RX	Receiver Power – 3.3V±5%
6	MOD-DEF0	Modulation Definition 0 – Ground in Module	16	Vcc TX	Transmitter Power – 3.3V±5%
7	N/C	Not Connected	17	TX GND	Transmitter Ground
8	LOS	Loss of Signal	18	TX DATA IN+	Transmitter Data In
9	RX GND	Receiver Ground	19	TX DATA IN-	Inverse Transmitter Data In
10	RX GND	Receiver Ground	20	TX GND	Transmitter Ground

Module Definition

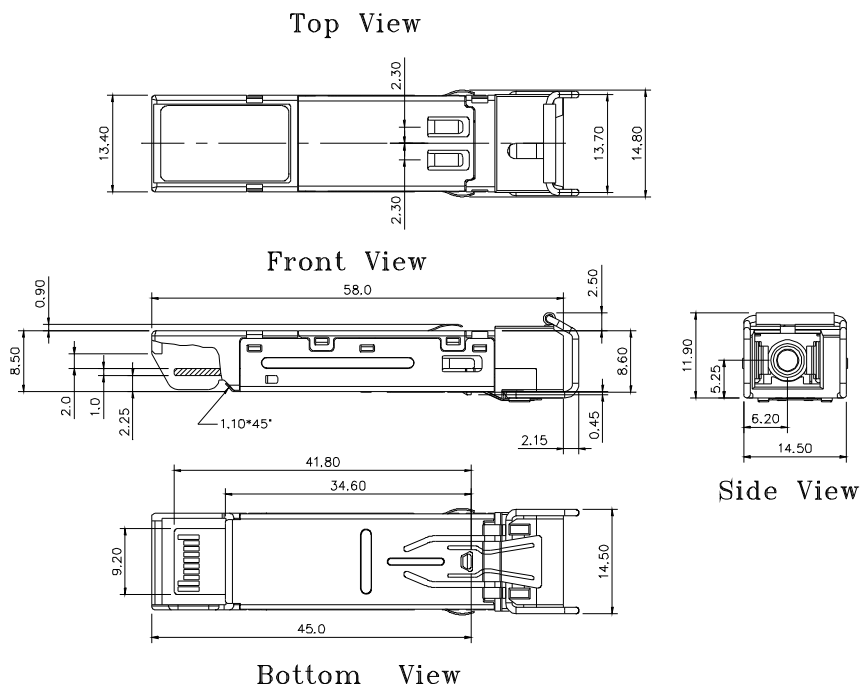
Module Definition	MOD-DEF2 PIN 4	MOD-DEF1 PIN 5	MOD-DEF0 PIN 6	Interpretation by Host
4	SDA	SCL	LV-TTL Low	Serial module definition protocol

Module Definition 4 specifies a serial definition protocol. For this definition, upon power up, MOD-DEF(1:2) appear as no connector (NC) and MOD-DEF(0) is TTL LOW. When the host system detects this condition, it activates the serial protocol. The protocol uses the 2-wire serial CMOS E²PROM protocol of the ATMEL AT24C01A/02/04 family of components.

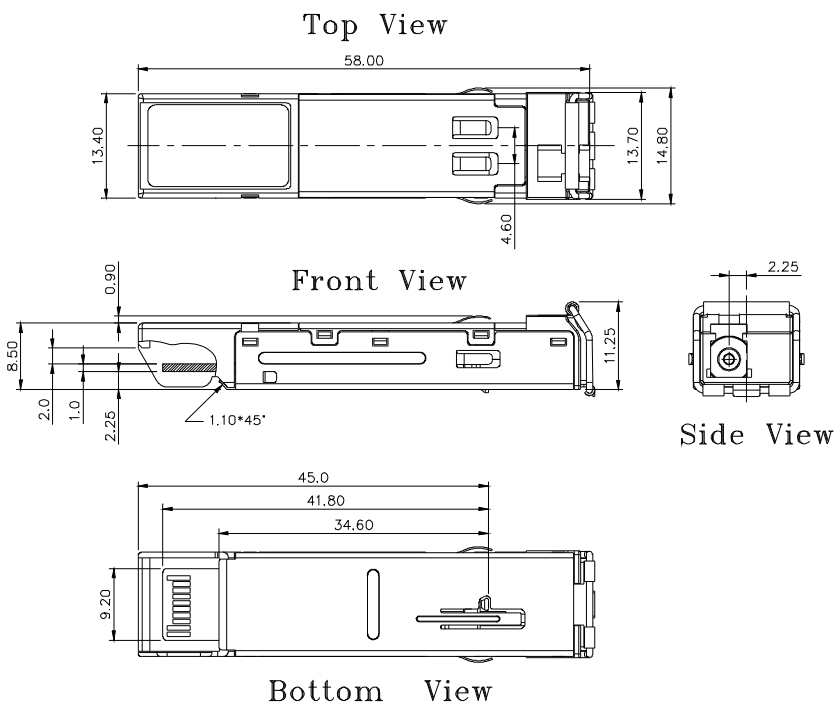
PACKAGE DIAGRAM

Units in mm

A) SC SFP



A) LC SFP



Note: Specifications subject to change without notice.