

NTE7167 Integrated Circuit Vertical Deflection Output Circuit

Description:

The NTE7167 is a vertical deflection output integrated circuit in a 7-Lead SIP type package designed for use in high-resolution television and CRT display systems that use a bus controller system signal processing IC. It can directly drive the deflection yoke (including the required DC component) from the bus controller system signal processing IC's sawtooth waveform output. Since the NTE7167 has a maximum deflection current of 2.2A_{P-P}, it is optimal for use in large aperture products, and is capable of driving 33 to 37 inch class monitors.

Features:

- Low Power Dissipation due to the Provision of a Built-In Pump Circuit
- Vertical Output Circuit
- On-Chip Thermal Protection Circuit
- Good Crossover Characteristics
- Supports DC Coupling

Recommended Operating Conditions: (T_A = +25°C unless otherwise specified)

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
Recommended Supply Voltage	V _{CC} 6		-	24	-	V
Operating Supply Voltage Range	V _{CC} 6 op		10	-	38	V
Recommended Deflection Output Current	I _{2P-P}		-	-	2.2	A_{P-P}

<u>Electgrical Characteristics:</u> $(T_A = +25^{\circ}C, V_{CC}6 = 24V \text{ unless otherwise specified})$

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
Pump Circuit Charge Saturation Voltage	V _{S7-1}	I ₇ = 20mA	-	-	1.8	V
Pump Circuit Discharge Saturation Voltage	V _{S6-7}	I ₇ = -1.1A	-	-	3.2	V
Deflection Output Saturation Voltage (Lower)	V _{S2-1}	I ₂ = 1.1A	-	-	1.5	V
Deflection Output Saturation Voltage (Upper)	V _{S3-2}	I ₂ = -1.1A	-	-	3.5	V
Idling Current	I _{DL}		35	-	70	mA
Midpoint Voltage	V_{MID}		11	12	13	V

