TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC74HC42AP,TC74HC42AF,TC74HC42AFN

#### BCD to Decimal Decoder

The TC74HC42A is a high speed CMOS BCD-to-DECIMAL DECODER fabricated with silicon gate C<sup>2</sup>MOS technology.

It achieves the high speed operation similar to equivalent LSTTL while maintaining the CMOS low power dissipation.

A BCD code applied to the four inputs (A-D) sets a low level at one of ten decoded outputs. A illegal BCD code such as eleven thru fifteen sets all outputs high. This device can be used as 3-to-8 LINE DECODER when input D is held low.

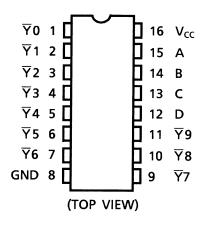
This device is useful for code conversion, address decoding, memory selection, multiplexing, or readout decoding.

All inputs are equipped with protection circuits against static discharge or transient excess voltage.

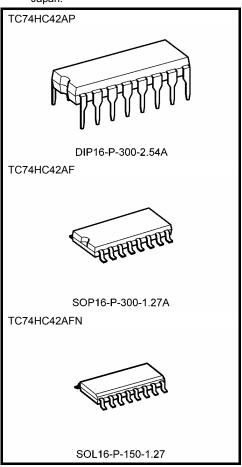
#### **Features**

- High speed:  $t_{pd} = 13 \text{ ns (typ.)}$  at  $V_{CC} = 5 \text{ V}$
- Low power dissipation:  $I_{CC} = 4 \mu A$  (max) at  $T_a = 25$ °C
- High noise immunity:  $V_{NIH} = V_{NIL} = 28\% V_{CC}$  (min)
- Output drive capability: 10 LSTTL loads
- Symmetrical output impedance: | I<sub>OH</sub> | = I<sub>OL</sub> = 4 mA (min)
- Balanced propagation delays:  $t_{pLH} \simeq t_{pHL}$
- Wide operating voltage range: V<sub>CC</sub> (opr) = 2~6 V
- Pin and function compatible with 74LS42

#### **Pin Assignment**



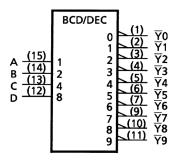
Note: xxxFN (JEDEC SOP) is not available in Japan.



Weight

DIP16-P-300-2.54A : 1.00 g (typ.) SOP16-P-300-1.27A : 0.18 g (typ.) SOL16-P-150-1.27 : 0.13 g (typ.)

# **IEC Logic Symbol**

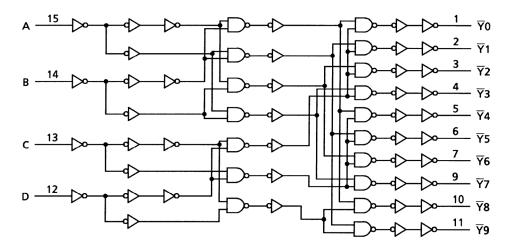


### **Truth Table**

Code		BCD	Inputs		Decimal Outputs									
No.	D	С	В	Α	₹0	<u>Y</u> 1	₹2	<del>Y</del> 3	<del>Y</del> 4	<del>Y</del> 5	<u>7</u> 6	<del>Y</del> 7	₹8	<del>Y</del> 9
0	L	L	L	L	L	Н	Н	Н	Н	Н	Н	Н	Н	Н
1	L	L	L	Н	Н	L	Н	Н	Н	Н	Н	Н	Н	Н
2	L	L	Н	L	Н	Н	L	Н	Н	Н	Н	Н	Н	Н
3	L	L	Н	Н	Н	Н	Н	L	Н	Н	Н	Н	Н	Н
4	L	Н	L	L	Н	Н	Н	Н	L	Н	Н	Н	Н	Н
5	L	Н	L	Н	Н	Н	Н	Н	Н	L	Н	Н	Н	Н
6	L	Н	Н	L	Н	Н	Н	Н	Н	Н	L	Н	Н	Н
7	L	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	L	Н	Н
8	Н	L	L	L	Н	Н	Н	Н	Н	Н	Н	Н	L	Н
9	Н	L	L	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	L
_	Н	Х	Н	Х	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н
_	Н	Н	Х	Х	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н

X: Don't care

# **System Diagram**



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#### **Absolute Maximum Ratings (Note 1)**

Characteristics	Symbol	Rating	Unit
Supply voltage range	V <sub>CC</sub>	-0.5~7	V
DC input voltage	V <sub>IN</sub>	-0.5~V <sub>CC</sub> + 0.5	V
DC output voltage	V <sub>OUT</sub>	-0.5~V <sub>CC</sub> + 0.5	٧
Input diode current	I <sub>IK</sub>	±20	mA
Output diode current	lok	±20	mA
DC output current	lout	±25	mA
DC V <sub>CC</sub> /ground current	Icc	±50	mA
Power dissipation	PD	500 (DIP) (Note 2)/180 (SOP)	mW
Storage temperature	T <sub>stg</sub>	-65~150	°C

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2: 500 mW in the range of Ta = -40 to  $65^{\circ}C$ . From Ta = 65 to  $85^{\circ}C$  a derating factor of -10 mW/°C shall be applied until 300 mW.

#### **Operating Ranges (Note)**

Characteristics	Symbol	Rating	Unit
Supply voltage	$V_{CC}$	2~6	V
Input voltage	V <sub>IN</sub>	0~V <sub>CC</sub>	V
Output voltage	V <sub>OUT</sub>	0~V <sub>CC</sub>	V
Operating temperature	T <sub>opr</sub>	-40~85	°C
		0~1000 (V <sub>CC</sub> = 2.0 V)	
Input rise and fall time	t <sub>r</sub> , t <sub>f</sub>	0~500 (V <sub>CC</sub> = 4.5 V)	ns
		0~400 (V <sub>CC</sub> = 6.0 V)	

Note: The operating ranges must be maintained to ensure the normal operation of the device.

Unused inputs must be tied to either VCC or GND.

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### **Electrical Characteristics**

#### **DC Characteristics**

0		Test Condition			-	Ta = 25°0	)	Ta = -4			
Characteristics	Symbol			V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	Unit	
				2.0	1.50	_	_	1.50	_		
High-level input voltage	V <sub>IH</sub>		_	4.5	3.15	_	_	3.15	_	V	
				6.0	4.20		_	4.20	_		
				2.0	_	_	0.50	_	0.50		
Low-level input voltage	V <sub>IL</sub>	_		4.5	_	_	1.35	_	1.35	V	
				6.0	_	_	1.80	_	1.80		
		V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub>		2.0	1.9	2.0	_	1.9	_		
	V <sub>OH</sub>		I <sub>OH</sub> = -20 μA	4.5	4.4	4.5	_	4.4	_		
High-level output voltage				6.0	5.9	6.0	_	5.9	_	V	
ŭ			$I_{OH} = -4 \text{ mA}$	4.5	4.18	4.31	_	4.13	_		
			$I_{OH} = -5.2 \text{ mA}$	6.0	5.68	5.80	_	5.63	_		
				2.0	_	0.0	0.1	_	0.1		
		V <sub>IN</sub>	$I_{OL} = 20 \mu A$	4.5	_	0.0	0.1	_	0.1		
Low-level output voltage	V <sub>OL</sub>	= V <sub>IH</sub> or		6.0	_	0.0	0.1	_	0.1	V	
		V <sub>IL</sub>	I <sub>OL</sub> = 4 mA	4.5	_	0.17	0.26	_	0.33		
			I <sub>OL</sub> = 5.2 mA	6.0	_	0.18	0.26	_	0.33		
Input leakage current	I <sub>IN</sub>	V <sub>IN</sub> = V <sub>CC</sub> or GND		6.0	_	_	±0.1	_	±1.0	μА	
Quiescent supply current	Icc	V <sub>IN</sub> = V <sub>CC</sub> or GND		6.0	_	_	4.0	_	40.0	μА	

### AC Characteristics ( $C_L = 15 \text{ pF}$ , $V_{CC} = 5 \text{ V}$ , $Ta = 25^{\circ}\text{C}$ , input: $t_r = t_f = 6 \text{ ns}$ )

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Output transition time	t <sub>TLH</sub>	_	_	4	8	ns
Capat transition time	t <sub>THL</sub>					110
Propagation delay time	t <sub>pLH</sub>	_	_	13	25	ns
Tropagation delay time	t <sub>pHL</sub>				20	113



# AC Characteristics (CL = 50 pF, input: $t_r = t_f = 6 \text{ ns}$ )

		Test Condition		-	Га = 25°C	;	Ta = -4		
Characteristics	Symbol		V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	Unit
	4		2.0	_	30	75	_	95	
Output transition time	t <sub>TLH</sub>	_	4.5	_	8	15	_	19	ns
			6.0	_	7	13	_	16	
	t <sub>pLH</sub>		2.0	_	48	145	_	180	
Propagation delay time		_	4.5	_	16	29	_	36	ns
	t <sub>pHL</sub>		6.0	_	14	25	_	31	
Input capacitance	C <sub>IN</sub>	_		_	5	10	_	10	pF
Power dissipation	C <sub>PD</sub>				68				pΕ
capacitance	(Note)	_			00		_	_	pF

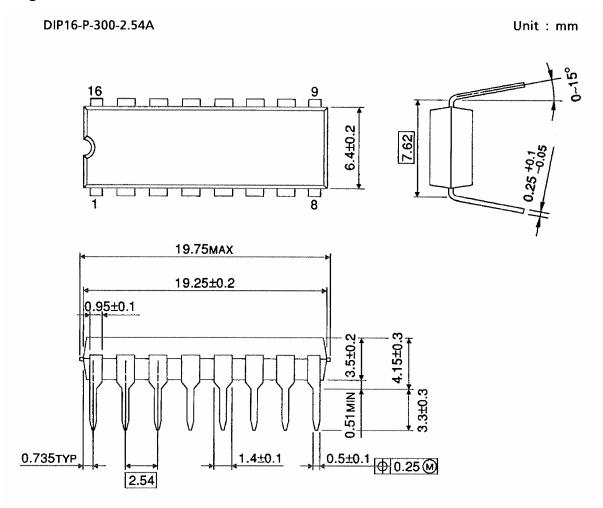
Note: C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

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Average operating current can be obtained by the equation:

$$I_{CC}$$
 (opr) =  $C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$ 

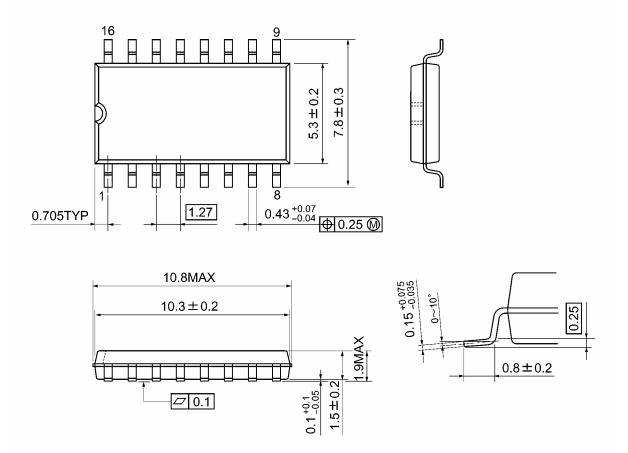
### **Package Dimensions**



Weight: 1.00 g (typ.)

### **Package Dimensions**

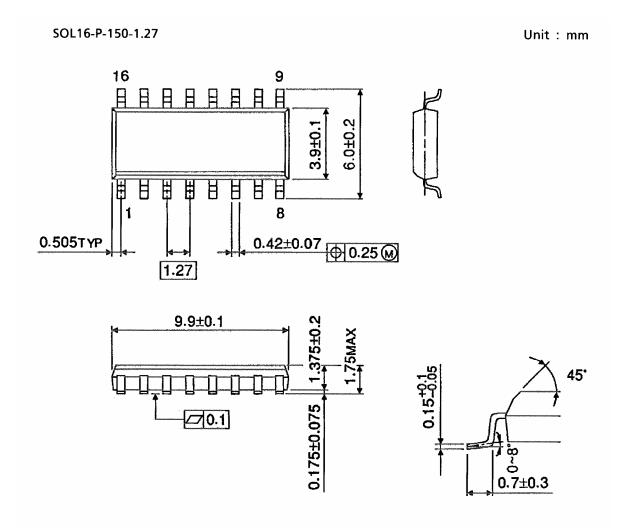
SOP16-P-300-1.27A Unit: mm



Weight: 0.18 g (typ.)



### **Package Dimensions (Note)**



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Note: This package is not available in Japan.

Weight: 0.13 g (typ.)

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20070701-EN GENERAL

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