TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7SET34F,TC7SET34FU

Non-Invert Buffer

Features

• High speed t_{pd} = 5.0 ns (typ.)

at $V_{CC} = 5 V$

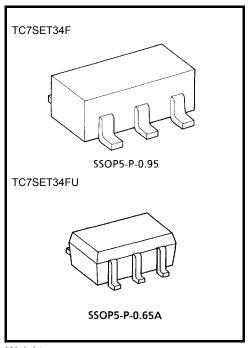
• Low power dissipation $I_{CC} = 2 \mu A \text{ (max)}$

at Ta = 25° C

• Compatible with TTL outputs...VIL = 0.8 V (max.)

VIH = 2.0 V (min.)

• 5.5V tolerant input.



Weight

SSOP5-P-0.95 : 0.016 g (typ.) SSOP5-P-0.65A : 0.006 g (typ.)

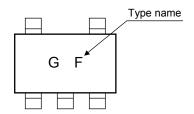
Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Supply voltage range	V _{CC}	-0.5~7.0	V
DC input voltage	V_{IN}	-0.5~7.0	V
DC output voltage	V _{OUT}	-0.5~V _{CC} + 0.5	V
Input diode current	l _{IK}	-20	mA
Output diode current	lok	±20	mA
DC output current	lout	±25	mA
DC V _{CC} /ground current	Icc	±50	mA
Power dissipation	P_{D}	200	mW
Storage temperature	T _{stg}	-65~150	°C
Lead temperature (10 s)	TL	260	°C

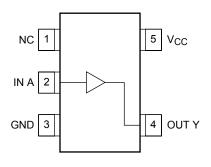
Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

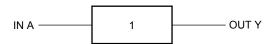
Marking



Pin Assignment (top view)



Logic Diagram



Truth Table

INPUT	OUTPUT
Α	Y
L	L
Н	Н

Operating Ranges

Characteristics	Symbol	Rating	Unit
Supply voltage	V_{CC}	4.5~5.5	V
Input voltage	V _{IN}	0~5.5	V
Output voltage	V _{OUT}	0~Vcc	V
Operating temperature	T _{opr}	-40~85	°C
Input rise and fall time	dt/dv	0~20	ns/V

DC Electrical Characteristics

Characteristics Symbol					Ta = 25°C			Ta = -40~85°C		
				V _{CC} (V)	Min	Тур.	Max	Min	Max	Unit
High-level input voltage	V _{IH}	_		4.5~ 5.5	2.0	_	_	2.0		٧
Low-level input voltage	V _{IL}	_		4.5~ 5.5	_	_	0.8	_	0.8	٧
High-level output voltage V _{OH}	\/	Maria Maria	I _{OH} = -50 μA	4.5	4.4	4.5		4.4	_	V
	VOH	$V_{IN} = V_{IH}$	I _{OH} = -8 mA	4.5	3.94	_	_	3.80	_	
Low-level output voltage	V _{IN} = V _{II}	$V_{IN} = V_{IH}$	$I_{OL} = 50 \mu A$	4.5	_	0.0	0.10	_	0.10	V
Low-level output voltage V _{OL}	VOL	or V _{IL}	I _{OL} = 8 mA	4.5	_	_	0.36	_	0.44	V
Input leakage current	I _{IN}	V _{IN} = 5.5 V or GND		0~ 5.5	_	_	±0.1	_	±1.0	μА
	Icc	V _{IN} = V _{CC} or GND		5.5	_	_	2.0	_	20.0	μΑ
Quiescent supply current	Ісст	Per Input	:V _{IN} = 3.4 V	5.5			1.35	_	1.50	mA
	1001	Other Input :V _{CC} or GND					1.50		1.50	111/5

AC Characteristics (input: $t_r = t_f = 3$ ns)

Characteristics	Symbol	Test Condition		Ta = 25°C			Ta = -40~85°C		- Unit	
			V _{CC} (V)	C _L (pF)	Min	Тур.	Max	Min	Max	Offic
Propagation delay time	t _{pLH} t _{pHL}		5.0 ± 0.5	15	_	5.0	7.0	1.0	8.0	- ns
				50	_	8.0	10.5	1.0	12.0	
Input capacitance	C _{IN}				_	4	10	_	10	pF
Power dissipation capacitance	C _{PD}			(Note)	_	17	_	_	_	pF

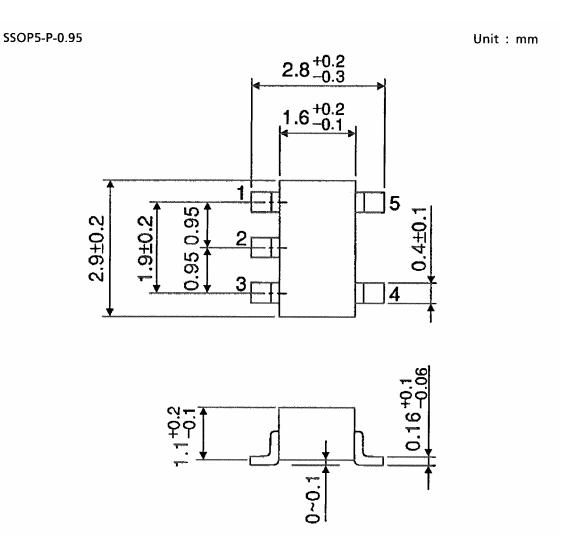
Note: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

$$I_{CC\;(opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$$



Package Dimensions



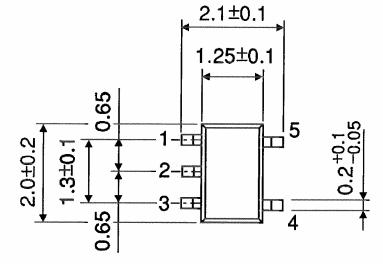
Weight: 0.016 g (typ.)

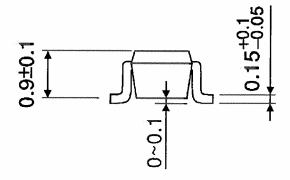
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Package Dimensions

TOSHIBA

SSOP5-P-0.65A Unit: mm





Weight: 0.006 g (typ.)

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20070701-EN GENERAL

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