



#### **Features**

- Miniature ceramic package
- Highly reliable with seam welding
- CMOS output
- Supply voltage V<sub>DD</sub>=3.3V
- Internal Bypass Capacitor
- Low Jitter
- ±25ppm available

#### Table 1

Star	ollity	Topr	Note		
Code	(ppm)	(°C)	Note		
0	± 50		Standard specifications		
S	± 30	-10 to +70 (Standard)	With only certain		
U	± 25	(Otaridard)	frequencies		
F	±100	-40 to +85	With only certain		
G	± 50	(Extend)	frequencies		

#### **How to Order**

 $\frac{\text{K50H-3C}}{\textcircled{1}} \ \frac{0}{\textcircled{2}} \ \frac{\text{S}}{\textcircled{3}} \ \frac{\text{E}}{\textcircled{4}} \ \frac{125.000}{\textcircled{5}}$ 

- ①Type(7×5 SMD, 3.3V)
- ②Frequency Stability Code(See Table1)
- 3Duty Ratio(S: 45% to 55% STD)
- 4 Enable/Disable Function(STD)
- ⑤Oscillation Frequency(Ex.: 125.000MHz)

Packaging(Tape & Reel 1,000pcs/reel)

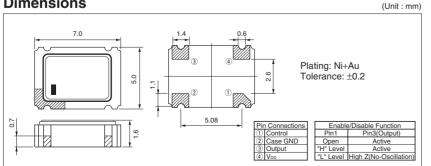
#### **Specifications**

Specifications						
Item	Symbol		Conditions	Min.	Max.	Units
Output Frequency Range	Fout			50	170	MHz
	Fsby	Overall conditions: initial tolerance, or	-25	+25	ppm	
Frequency Stability		power supply voltage	-30	+30		
		@25°C), shock and		-50	+50	
<b>Storage Temperature Range</b>	Тѕтс			-55	+125	
Operating Temperature Range	Topr	Standard		-10	+70	°C
Operating reinperature mange	TOPR	Extend(option)		-40	+85	
Max. Supply Voltage	_			-0.5	7.0	
Supply Voltage	V <sub>DD</sub>	Stability: ±50ppm, ±100p	pm(Ext Temp)	2.97	3.63	Volt
Supply Voltage	טט <b>ע</b>	Stability: ±25ppm, ±30pp	m, ±50ppm(Ext Temp)	3.14	3.46	
		50≤Fouт≤ 85MHz		_	30	
Current Consumption	IDD	85 <fouт≤100mhz< td=""><td>_</td><td>40</td><td rowspan="2">mA</td></fouт≤100mhz<>	_	40	mA	
(Maximum Loaded)		100 <fouт≤135mhz< td=""><td>_</td><td>50</td></fouт≤135mhz<>		_		50
	1		135 <fouт≤160mhz< td=""><td>60</td><td></td></fouт≤160mhz<>		60	
Duty Ratio(Symmetry)	Isт	Standby Function		_	10	μΑ
Duty Ratio(Symmetry)	SYM	@50% V <sub>DD</sub>		45	55	%
		50≤Fо∪т<100MHz	20% VDD to 80% VDD	_	3.5	
Rise/Fall Time	Tr/Tf	3021 001< 1001VII 12	10% VDD to 90% VDD	_	5.0	- nS
(10% Vpp to 90% Vpp Maximum Loaded)		100≤Fouт≤160MHz	20% VDD to 80% VDD	_	1.5	
			10% VDD to 90% VDD	_	2.0	
Output Voltage-"L"	Vol			_	10% V <sub>DD</sub>	Volt
Output Voltage-"H"	Vон			90% V <sub>DD</sub>	_	VOIL
Output Load	CL	CMOS		_	15	pF
Input Voltage Range	Vin			0	V <sub>DD</sub>	Volt
Input Voltage-"L"	VIL			_	30% V <sub>DD</sub>	Volt
Input Voltage-"H"	VIH			70% V <sub>DD</sub>	_	VOIL
Output Disable Time	_			_	5	mS
Output Enable Time	_			_	150	nS
Start-up Time	ST	@Minimum operating \	Voltage to be 0sec.	-	10	mS
Deterministic Jitter pk-pk	DJ	Measured with "Wavecre	et DTS-2070" VISL6 3 1	_	2	psec
1Sigma jitter	1sigma	Wiedsured With Wavecle	31 D 10 2013, VIOI 0.0.1	_	4	pacc

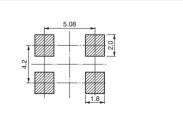
Note: Please contact us for inquires about extended operating temperature range, available frequencies and other conditions.

All electrical characteristics are defined at the maximum load and operating temperature range.

#### **Dimensions**



## Recommended Land Pattern (Unit:mm)



Note: K50H Series has Bypass Capacitor of  $0.01 \mu F$  between  $V_{\text{DD}}$  and GND.





#### **Features**

- Miniature ceramic package
- Highly reliable with seam welding
- CMOS output
- Supply voltage VDD=3.3V
- ±25ppm available

#### Table 1

Stability		Topr	Note		
Code	(ppm)	(°C)	Note		
0	± 50		Standard specifications		
S	± 30	-10 to $+70$ (Standard)	With only certain		
U	± 25	(Staridard)	frequencies		
F	±100	-40 to +85	With only certain		
G	± 50	(Extend)	frequencies		

#### **How to Order**

 $\frac{\text{K50-3C}}{\textcircled{1}} \ \, \frac{\textbf{0}}{\textcircled{2}} \cdot \frac{\textbf{S}}{\textcircled{3}} \ \, \frac{\textbf{E}}{\textcircled{4}} \ \, \frac{\textbf{25.0000}}{\textcircled{5}}$ 

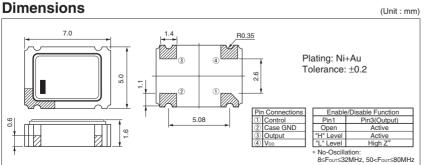
- ①Type(7×5 SMD, 3.3V)
- ②Frequency Stability Code(See Table1)
- 3Duty Ratio(S: 45% to 55% STD)
- 4Enable/Disable Function(STD)
- 5 Oscillation Frequency(Ex.: 25.0000MHz)

Packaging(Tape & Reel 1Kpcs/reel)

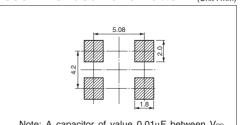
#### **Specifications**

Item	Symbol	Conditions	Min.	Max.	Units			
Output Frequency Range	Fouт		1.5	80	MHz			
Frequency Stability	Fsby	Overall conditions: initial tolerance, operating temperature range, rated power supply voltage change, load change, aging(1year @25°C), shock and vibration	-25 -30 -50	+25 +30 +50	ppm			
Storage Temperature Range	Тѕтс		-55	+125				
<u> </u>		Standard	-10	+70	°C			
Operating Temperature Range	Topr	Extend(option)	-40	+85	-			
Max. Supply Voltage	_	\ 1	-0.5	7.0				
Committee Walterna		Stability: ±50ppm, ±30ppm, ±100ppm(Ext Temp)	2.97	3.63	Volt			
Supply Voltage	$V_{DD}$	Stability: ±25ppm, ±50ppm(Ext Temp)	3.14	3.46	-			
		1.5≤Fouт≤20MHz	_	10				
Current Consumption	1	20 <fout≤40mhz< td=""><td>_</td><td>15</td><td> A</td></fout≤40mhz<>	_	15	A			
(Maximum Loaded)	IDD	40 <fout≤60mhz< td=""><td>_</td><td>20</td><td colspan="3">mA</td></fout≤60mhz<>	_	20	mA			
		60 <fout≤80mhz< td=""><td>_</td><td>30</td><td colspan="2"></td></fout≤80mhz<>	_	30				
Standby/Disable Current	Ist/Ide	8≤Fouт≤32MHz(Standby Function)	_	10	μΑ			
		32 <fouт≤50mhz(disable function)<="" td=""><td>15</td><td>mA</td></fouт≤50mhz(disable>		15	mA			
		50 <fouт≤80mhz(standby function)<="" td=""><td>_</td><td>10</td><td>μΑ</td></fouт≤80mhz(standby>	_	10	μΑ			
Duty Ratio(Symmetry)	SYM	@50% V <sub>DD</sub>	45	55	%			
Rise/Fall Time		8≤Fout≤26MHz	_	10				
(10% Vpb to 90% Vpb Maximum Loaded)	Tr/Tf	26 <fout≤45mhz< td=""><td>_</td><td>8</td><td colspan="2">nS</td></fout≤45mhz<>	_	8	nS			
(10% VDD to 90% VDD WAXIII alli Loaded)		45 <fouт≤80mhz< td=""><td>_</td><td>5</td><td colspan="3"></td></fouт≤80mhz<>	_	5				
Output Voltage-"L"	Vol	loL=8mA	_	10% V <sub>DD</sub>	Volt			
Output Voltage-"H"	Vон	Іон=–8mА	90% V <sub>DD</sub>	_				
Output Load	CL	CMOS	_	15	pF			
Input Voltage Range	Vin		0	VDD	Volt			
Input Voltage-"L"	VIL		_	30% V <sub>DD</sub>	Volt			
Input Voltage-"H"	VIH		70% V <sub>DD</sub>	-				
Output Disable Time	_		_	150	nS			
		8≤Fout≤32MHz	_	5	mS			
Output Enable Time	_	32 <fout≤50mhz< td=""><td>-</td><td>150</td><td>nS</td></fout≤50mhz<>	-	150	nS			
		50 <fout≤80mhz< td=""><td>-</td><td>5</td><td>mS</td></fout≤80mhz<>	-	5	mS			
Start-up Time	ST	@Minimum operating Voltage to be 0sec.	_	10	mS			

Note: Please contact us for inquires about extended operating temperature range, available frequencies and other conditions. All electrical characteristics are defined at the maximum load and operating temperature range



### Recommended Land Pattern (Unit:mm)



Note: A capacitor of value  $0.01 \mu F$  between  $V_{\text{DD}}$ and GND is recommended.





#### **Features**

- Miniature ceramic package
- Highly reliable with seam welding
- CMOS output
- Supply voltage VDD=5.0V
- ±25ppm available

#### Table 1

Stability		Topr	Note		
Code	(ppm)	(°C)	Note		
0	± 50		Standard specifications		
S	± 30	-10 to +70 (Standard)	With only certain		
U	± 25	(Staridard)	frequencies		
F	±100	-40 to +85	With only certain		
G	± 50	(Extend)	frequencies		

#### **How to Order**

K50-HC 0 - C S E 25.0000  $\overline{1}$   $\overline{2}$   $\overline{3}$   $\overline{4}$   $\overline{5}$ 

- ①Type(7×5 SMD, 5.0V)
- ②Frequency Stability Code(See Table1)
- **3CMOS Output**
- 4) Duty Ratio(S: 45% to 55% STD)
- ⑤Enable/Disable Function(STD)
- 6 Oscillation Frequency(Ex.: 25.0000MHz)

Packaging(Tape & Reel 1Kpcs/reel)

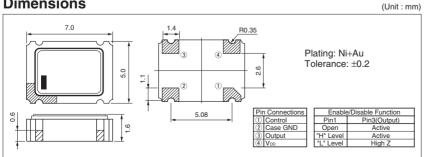
#### **Specifications**

Item	Symbol	Conditions	Min.	Max.	Units	
Output Frequency Range	Fout		1.5	68	MHz	
		Overall conditions:	-25	+25	ppm	
Frequency Stability	Fsby	initial tolerance, operating temperature range, rated power supply voltage change, load change, aging(1year @25°C).	-30	+30		
		shock and vibration	-50	+50		
Storage Temperature Range	Тѕтс		-55	+125		
Operating Temperature Range	Торв	Standard	-10	+70	°C	
operating reinperature mange	I OPR	Extend(option)	-40	+85		
Max. Supply Voltage	_		-0.5	7.0		
Supply Voltage	V <sub>DD</sub>	Stability: ±50ppm, ±30ppm, ±100ppm(Ext Temp)	4.5	5.5	Volt	
Supply voltage	<b>V</b> DD	Stability: ±25ppm, ±50ppm(Ext Temp)	4.75	5.25		
Current Consumption	Ірр	1.5≤Fouт≤20MHz	_	25	mA	
(Maximum Loaded)		20 <fouт≤40mhz< td=""><td>_</td><td>35</td></fouт≤40mhz<>	_	35		
(waximum Loaded)		40 <fouт≤68mhz< td=""><td>_</td><td>50</td></fouт≤68mhz<>	_	50		
Disable Current	IDE	@68.0000MHz	_	30	mA	
Duty Ratio(Symmetry)	SYM	@50% V <sub>DD</sub>	45	55	%	
Rise/Fall Time		8≤Fouт≤26MHz	_	10		
(10% Vpb to 90% Vpb Maximum Loaded)	Tr/Tf	26 <fout≤45mhz< td=""><td>8</td><td>nS</td></fout≤45mhz<>		8	nS	
(10% VDD to 90% VDD Waximum Loaded)		45 <fouт≤68mhz< td=""><td>_</td><td>5</td><td colspan="2">1</td></fouт≤68mhz<>	_	5	1	
Output Voltage-"L"	Vol	IoL=16mA	_	10% VDD	Volt	
Output Voltage-"H"	Vон	Iон=-16mA	90% V <sub>DD</sub>	_	VOIL	
Output Load(CMOS)	CL	8≤Four≤50MHz 50 <four≤68mhz< td=""><td>50</td><td rowspan="2">pF</td></four≤68mhz<>		50	pF	
Output Load(CMOS)	OL			15		
Input Voltage Range	VIN		0	V <sub>DD</sub>	Volt	
Input Voltage-"L"	VIL		_	0.8	Volt	
Input Voltage-"H"	VIH		2.2	_		
Output Disable Time	_		_	100	nS	
Output Enable Time	_		_	100	nS	
Start-up Time	ST	@Minimum operating Voltage to be 0sec.		10	mS	

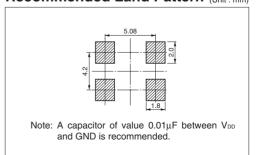
Note: Please contact us for inquires about extended operating temperature range, available frequencies and other conditions.

All electrical characteristics are defined at the maximum load and operating temperature range.

#### **Dimensions**



### Recommended Land Pattern (Unit: mm)







#### 1. Shock & Drop • Vibration

Do not inflict excessive shock and mechanical vibration that exceeds the norm, such as hitting or mistakenly dropping, when transporting and mounting on a board. There are cases when pieces of crystal break, and pieces that are used become damaged, and become inoperable. When a shock or vibration that exceeds the norm has been inflicted, make sure to check the characteristics.

#### 2. Cleaning

Since a crystal piece can be broken by resonance when a crystal device is cleaned by ultrasonic cleaning. Be careful when carrying out ultrasonic cleaning.

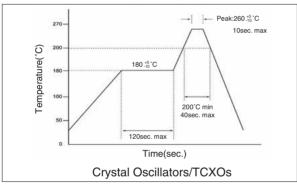
#### 3. Soldering conditions

To maintain the product reliability, please follow recommended conditions.

#### Standard soldering iron conditions

	Crystal Oscillators
Soldering iron	280°C ~ 340°C
Time	3+1/-0sec. max

#### **Reflow conditions (Example)**



Recommended reflow Conditions vary depending upon products. Please check with the respective specification for details.

#### 4. Mounting Precautions

#### **Leaded Devices**

The special glass, located where the lead of the retainer base comes out, is aligned with the coefficient of thermal expansion of the lead, If the glass is damaged and cracks appear, there may be cases in which performance deteriorates and it fails to operate.

Consequently, when making the device adhere closely and applying solder, align the gap of the hole of the board with the gap of the lead and insert without excessive force.

When making the device adhere closely to a through hole board and applying solder, be careful that the solder does not get into the metal part of the retainer base and cause a short. Putting in an insulation spacer is one more method of preventing a short circuit.

When the lead is mounted floating, fix it as far as possible so that contact with other parts and the breakage due to the fatigue, and the mechanical resonance of the lead will not occur.

When the lead is bent and used, do not bend the lead directly from the base, separate it 0.5mm or more and then bend it. When bending, before attaching to the board, fix the place where the lead comes out in advance and attach it after bending so that a crack does not occur in the glass part.

#### **Surface Mount Devices**

The lead of the device and the pattern of the board is soldered on the surface. Since extreme deformation of the board tears off the pattern, tears off the lead metal, cracks the solder and damages the sealed part of the device and there are cases in which performance deteriorates and operation fails, use it within the stipulated bending conditions. Due to the small cracks in the board resulting from mounting, please pay sufficient attention when attaching a device at the position where the warping of the board is great.

When using an automatic loading machine, as far as possible, select a type that has a small impact and use it while confirming that there is no damage.

Surface mount devices are NOT flow soldering compatible.

#### 5. Storage Condition

Since the long hour high temperature and low temperature storage, as well as the storage at high humidity are causes of deterioration in frequency accuracy and solderability.

Parts should be stored in temperature range of -5 to +40C°, humidity 40 to 60% RH, and avoid direct sunlight. Then use within 6 months.





#### 6. In order to use crystal oscillators

- (1)The minlature crystal oscillator for the clock utilizes a C-MOS IC and incorporates a protective circuit against static electricity. However, exercise care in the same manner as for a normal C-MOS IC.
- (2)Internal capacitor is not provided in the power supply section (+DC-GNC).

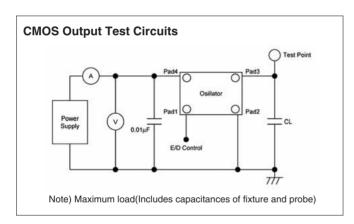
To serve as overimpressed voltage and overcurrent protective device, place a bypass capacitor (0.01  $\mu$ F) as near as possible to the (+DC–GND) terminal.

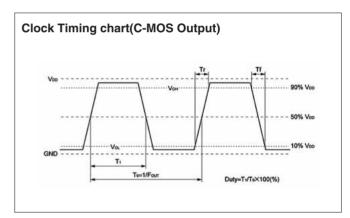
However, the capacity value is meant as a guideline.

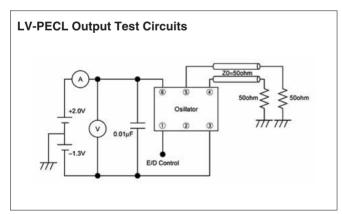
Depending on the capacitor type, frequency characteristics vary. Accordingly, use a capacitor that matches the frequency characteristics.

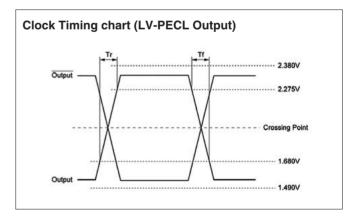
- (3)Applying reverse voltage could result in damage to internal parts. Take care not to connect terminals incorrectly.
- (4)Please do not use oscillators under unfavorable condition such as beyond specified range in catalog or specification sheet.
- (5)Please keep oscillators away from water, salt water or harmful gas.
- (6)K50H-3C / FSO series should be stored in humidity-controlled area after the package is unsealed, in temperature +25±5°C under humidity of 65%RH, and should be mounted on PCB within 7 days.
- (7)K50H-3C / FSO series has Bypass Capacitior between VDD and GND.

#### **Test Circuits and Clock Timing Chart**







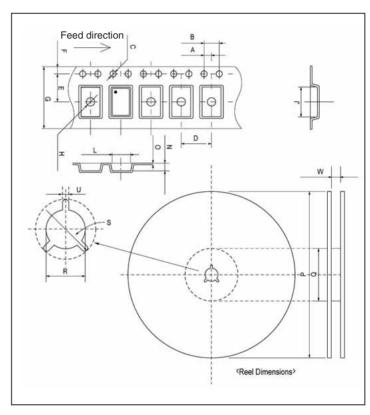




# Tape & Reel Specifications

# ■Crystal Oscillators

		KC2520	K25	K30	FXO-61F FXO-64F VC-FXO-65F					
	Α	2.0±0.1	2.0±0.1	2.0±0.1	2.0±0.1					
	В	4.0±0.1	4.0±0.1	4.0±0.1	4.0±0.1					
	С	φ1.5+0.1/ <del>-</del> 0	φ1.5+0.1/-0	φ1.5+0.1/ <del>-</del> 0	φ1.55±0.1					
	D	4.0±0.1	8.0±0.1	8.0±0.1	8.0±0.1					
	Е	3.5±0.05	3.5±0.05	5.5±0.1	5.5±0.1					
T	F	1.75±0.1	1.75±0.1	1.75±0.1	1.75±0.1					
P	G	8.0±0.2	8.0±0.2	12.0±0.2	12.0±0.3					
_	Н	φ1.1±0.1	φ1.55±0.1	φ1.55±0.1	φ1.55±0.1					
	J	2.7±0.1	3.5±0.05	5.5±0.1	5.4±0.1					
	L	2.2±0.1	2.8±0.05	3.7±0.1	3.6±0.1					
	N	1.25±0.1	1.1±0.05	1.4±0.1	1.7±0.1					
	0	0.2±0.05	0.3±0.05	0.3±0.05	0.3±0.05					
	Р	φ180+0/–3	φ180+0/–3	φ180+0/–3	φ254±2					
R	Q	φ60+1/-0	φ60+1/–0	φ60+1/–0	φ100±1					
F	R	φ13±0.2	φ13±0.2	φ13±0.2	φ13±0.5					
Ē	S	φ21±0.8	φ21±0.8	φ21±0.8	φ21±0.8					
	U	2.0±0.5	2.0±0.5	2.0±0.5	2.0±0.5					
	W	9.0±0.3	9.0±0.3	13.0±0.3	13.4+2/-0					
C	ty	2000	2000	1000	1000					



		FSO-1C/1D FSO-2B/2C/2D VC-FSO-1D	FXO-31F FXO-34F FXO-37F VC-FXO-35F VCXO-7050	K50H K50 K53 K50V-3P	KT21	(VC)TCXO-214C	(VC)TCXO-212C	(VC)TCXO-208C MFO-208F	KT18B	KT20A	(VC)TCXO-204C
	Α	2.0±0.1	2.0±0.1	2.0±0.1	2.0±0.1	2.0±0.1	2.0±0.1	2.0±0.1	2.0±0.1	2.0±0.1	2.0±0.1
	В	4.0±0.1	4.0±0.1	4.0±0.1	4.0±0.1	4.0±0.1	4.0±0.1	4.0±0.1	4.0±0.1	4.0±0.1	4.0±0.1
	С	φ1.55±0.1	φ1.55±0.1	φ1.5±0.1	φ1.5+0.1/–0	φ1.5+0.1/–0	φ1.5+0.1/-0	φ1.5+0.1/–0	φ1.5+0.1/–0	φ1.5+0.1/–0	φ1.5+0.1/–0
	D	8.0±0.1	8.0±0.1	8.0±0.1	4.0±0.1	4.0±0.1	4.0±0.1	8.0±0.1/4.0±0.1	8.0±0.1/4.0±0.1	8.0±0.1	4.0±0.1
_	Е	7.5±0.1	7.5±0.1	7.5±0.1	3.5±0.1	3.5±0.05	5.5±0.1	5.5±0.1	5.5±0.1	5.5±0.1	7.5±0.1
T A	F	1.75±0.1	1.75±0.1	1.75±0.1	1.75±0.1	1.75±0.1	1.75±0.1	1.75±0.1	1.75±0.1	1.75±0.1	1.75±0.1
P	G	16.0±0.3	16.0±0.3	16.0±0.2	8.0±0.3	8.0±0.2	8.0±0.3/12.0±0.3	12.0±0.3	12.0±0.3	12.0±0.3	16.0±0.3
-	Н	φ1.55	φ1.55	φ1.55±0.1	φ1.5+0.1/–0	φ1.0+0.2/-0	φ1.5+0.1/-0	φ1.5+0.1/–0	φ1.5+0.1/–0	φ1.5+0.1/–0	φ1.5+0.1/–0
	J	8.18	8.18	7.4±0.1	3.5±0.1	3.6±0.1	4.4±0.1	5.5±0.1	5.5±0.1	5.5±0.1	7.4±0.1
	L	5.56	5.56	5.4±0.1	2.8±0.1	2.9±0.1	2.9±0.1	3.7±0.1	3.7±0.1	3.7±0.1	5.4±0.1
	N	2.16±0.1	2.16±0.1	2.0±0.1	1.1±0.1	1.3±0.1	1.1±0.1/1.3±0.1	1.6±0.1/1.5±0.1	1.6±0.1/1.5±0.1	1.8±0.1	2.1±0.1
	0	0.3±0.05	0.3±0.05	0.3±0.05	0.25±0.05	0.3±0.05	0.2±0.05/0.3±0.05	0.3±0.05	0.3±0.05	0.3±0.05	0.3±0.05
	Р	\$330±2\$254±2	φ330±2φ254±2	φ180+0/-3	φ180±2	φ180+1/–1.5	φ254±1/φ180+1/-1.5	φ330±1	φ330±1	φ330±1	φ254±2
R	Q	φ100±1	φ100±1	φ60+1/-0	φ60	φ60+1/-0	φ100±1/φ60+1/–0	φ100±1/φ60	φ100±1φ60	φ100±1	φ100±1
E	R	φ13±0.3	φ13±0.3	φ13±0.2	φ13±0.2	φ13±0.2	φ13±0.2	φ13±0.2	φ13±0.2	φ13±0.2	φ13±0.2
L	S	φ21±0.8	φ21±0.8	φ21±0.8	φ21±0.8	φ21±0.8	φ21±0.8	φ21±0.8	φ21±0.8	φ21±0.8	φ21±0.8
	U	2.0±0.5	2.0±0.5	2.0±0.5	2.0±0.5	2.0±0.5	2.0±0.5	2.0±0.5	2.0±0.5	2.0±0.2	2.0±0.5
	W	16.4	17.5±0.5/16.4	17±0.2	9.0±0.1	9.0+0.3/-0	9.0±0.3/13.0+0.3-0	13.5±1	13.5±1	13.5±1	17.5±0.5
	Qty	3000/1000	3000/1000	1000	2000	2000	4000/2000	2000	4000	4000	1000



# ORDERING FORMAT FOR CRYSTAL OSCILLATORS

Please specify the following items when ordering crystal oscillators.

### I. Standard products shown in the catalogue:

Please specify package type and nominal frequency.

# II. Special-order products:

Please specify the following items in specification.

2.	. Frequency Stability		
	(1)Temperature ×10 <sup>-6</sup> MAX ~°C (On the basis	of +25°C	frequency)
	(2)Supply Voltage ×10 <sup>-6</sup> MAX VDC ± %	0/	
	(3)Load $\times$ 10 <sup>-6</sup> MAX $\Omega$ // pF $\pm$ % $\pm$ (4)Aging $\times$ 10 <sup>-6</sup> MAX./year	%	
3.	. Frequency Tuning Range		
	±×10 <sup>-6</sup> MIN		
4.	. Frequency Control Range		
	± ~± ×10 <sup>-6</sup> /+ V± V		
5.	. Output		
	(1)Voltage Vrms V <sub>P-P</sub> MIN. (2)Wave Form		
	Sine, Half sine wave, Harmonics 2nd dB, 3rd dB, Ot	hers	dB
	(3)Load Resistance $\underline{\hspace{1cm}}$ $\Omega$ //Capacitance $\underline{\hspace{1cm}}$ pF		
6.	5. Supply Voltage		
	(1)For circuit VDC± %, mA MAX.		
7.	. Environmental Condition		
	(1)Operating Temperature Range ~ °C (2)Vibration		
	Total Amplitude mm, Vibration Frequency Hz~	Hz	
	(3)Shock		
8.	. Dimensions, Pin Connection		
	· ————————————————————————————————————		
9.	. Application		