

HD74LS83A

4-bit Binary Full Adder (with Fast Carry)

REJ03D0420-0200 Rev.2.00 Feb.18.2005

This improved full adder performs the addition of two 4-bit binary numbers. The sum (Σ) outputs are provided for each bit and the resultant carry (C4) is obtained from the fourth bit. This adder features full internal look ahead across all four bit generating the carry term in ten nanoseconds typically. This provides the system designer with partial lookahead performance at the economy and reduced package count of a ripple-carry implementation.

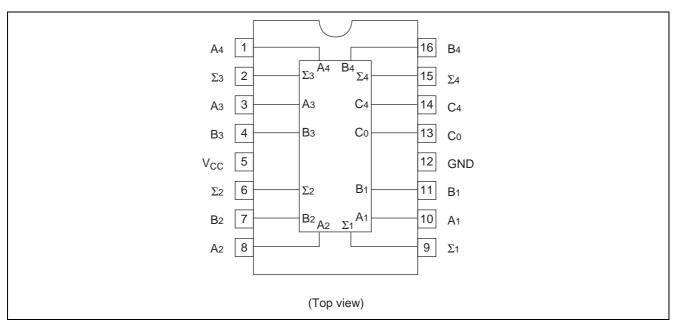
Features

• Ordering Information

Part Name	Package Type	Package Code (Previous Code)	Package Abbreviation	Taping Abbreviation (Quantity)		
HD74LS83AP	DILP-16 pin	PRDP0016AE-B (DP-16FV)	Р	_		

Note: Please consult the sales office for the above package availability.

Pin Arrangement



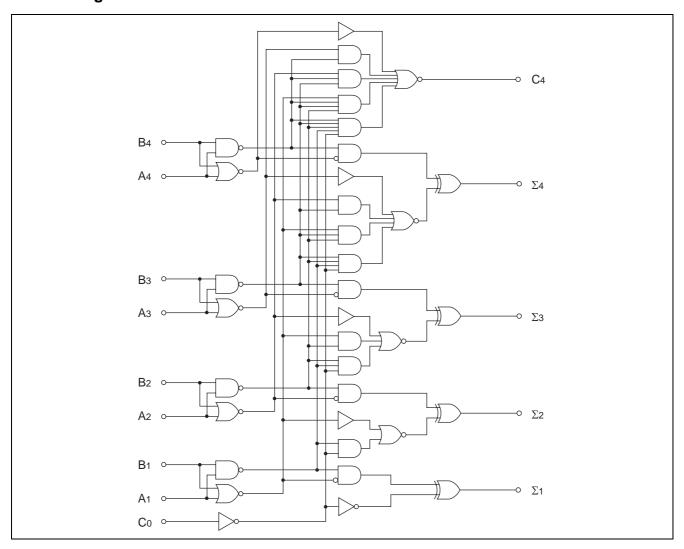
Function Table

				Output									
	Inp	out		When C ₀ =	L		When C ₀ = H						
					W	hen $C_2 = L$	When $C_2 = H$						
A ₁	B ₁	A ₂	B ₂	Σ ₁	Σ_2	C ₂	Σ ₁	Σ_2	C ₂				
A ₃	B ₃	A ₄	B ₄	Σ_3	Σ_4	C ₄	Σ_3	Σ_4	C ₄				
L	L	L	L	L	L	L	Н	L	L				
Н	L	L	L	Н	L	L	L	Н	L				
L	Н	L	L	Н	L	L	L	Н	L				
Н	Н	L	L	L	H L		Η	Н	L				
L	L	Н	L	L	H L		Η	Н	L				
Н	L	Н	L	Н	Н	L	L	L	Н				
L	Н	Н	L	Н	Н	L	L	L	Н				
Н	Н	Н	L	L	L	Н	Η	L	Н				
L	L	L	Н	L	Н	L	Н	Н	L				
Н	L	L	Н	Н	H L		L L		Н				
L	Н	L	Н	Н	Н	L	L	L	Н				
Н	Н	L	Н	L	L	Н	Н	L	Н				
L	L	Н	Н	L	Ь	Н	н н		Н				
Н	L	Н	Н	Н	L	H L		Н	Н				
L	Н	Н	Н	Н	L	H L		Н	Н				
Н	Н	Н	Н	L	Н	Н	Н	Н	Н				

H; high level, L; low level, X; irrelevant

Note: Input conditions at A_1 , B_1 , A_2 , B_2 , and C_0 are used to determine outputs Σ_1 and Σ_2 and the value of the internal carry C_2 . The value at C_2 , A_3 , B_3 , A_4 , and B_4 are than used to determine outputs Σ_3 , Σ_4 and C_4 .

Block Diagram



Absolute Maximum Ratings

Item	Symbol	Ratings	Unit
Supply voltage	V _{CC}	7	V
Input voltage	V _{IN}	7	V
Power dissipation	P _T	400	mW
Storage temperature	Tstg	-65 to +150	°C

Note: Voltage value, unless otherwise noted, are with respect to network ground terminal.

Recommended Operating Conditions

Item	Symbol	Min	Тур	Max	Unit
Supply voltage	V _{CC}	4.75	5.00	5.25	V
Output current	I _{OH}	_	_	-400	μΑ
Output current	I _{OL}	_	_	8	mA
Operating temperature	Topr	-20	25	75	°C

Electrical Characteristics

 $(Ta = -20 \text{ to } +75 \text{ }^{\circ}\text{C})$

I	tem	Symbol	min.	typ.*	max.	Unit	Condition				
ا میند بیان میا		V _{IH}	2.0	_	_	V					
Input voltage		V _{IL}	_	_	0.8	V					
		V _{OH}	2.7	_	_	V	$V_{CC} = 4.75 \text{ V}, V_{IH} = 2 \text{ V}, V_{IL} = 0.8 \text{ V},$				
Output v	0						$I_{OH} = -400 \mu A$				
Output vo	ollage	V	_	_	0.4	V	$I_{OL} = 4 \text{ mA}$ $V_{CC} = 4.75 \text{ V}, V_{IH} = 2 \text{ V},$				
		V_{OL}	_	_	0.5	V	$I_{OL} = 8 \text{ mA}$ $V_{IL} = 0.8 \text{ V}$				
	except C ₀		_	_	40		V 525V V 2.7V				
	C ₀	I _{IH}	_	_	20	μΑ	$V_{CC} = 5.25 \text{ V}, V_{I} = 2.7 \text{ V}$				
Input	except C ₀		_	_	-0.8	Л	V 525 V V 0.4 V				
current	C ₀	I _{IL}	_	_	-0.4	mA	$V_{CC} = 5.25 \text{ V}, V_{I} = 0.4 \text{ V}$				
	except C ₀		_	_	0.2	Л	V 5.25 V V 7.V				
	C ₀	I _I	_	_	0.1	mA	$V_{CC} = 5.25 \text{ V}, V_{I} = 7 \text{ V}$				
Short-circuit output current		los	-20	_	-100	mA	V _{CC} = 5.25 V				
Supply current			_	22	39		All inputs = 0 V				
		I _{CC}	_	19	34	mA	B input = 0.8 V , Other inputs 4.5 V $V_{CC} = 5.25 \text{ V}$				
			_	19	34		All inputs = 4.5 V				
Input clar	np voltage	V_{IR}	_	_	-1.5	V	$V_{CC} = 4.75 \text{ V}, I_{IN} = -18 \text{ mA}$				

Note: ${}^*V_{CC} = 5 \text{ V}, \text{ Ta} = 25 {}^\circ\text{C}$

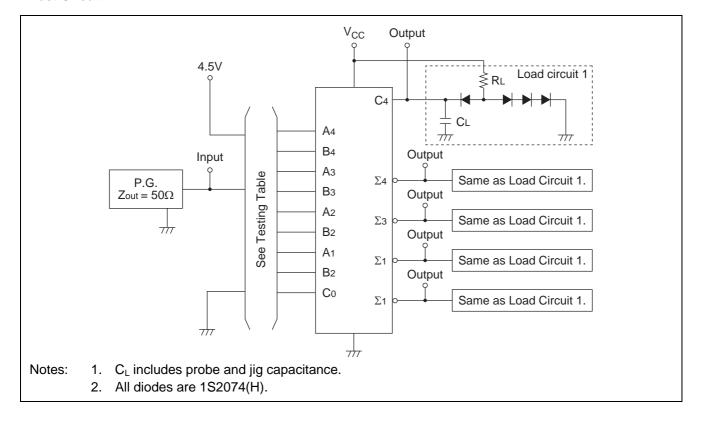
Switching Characteristics

 $(V_{CC} = 5 \text{ V}, \text{Ta} = 25^{\circ}\text{C})$

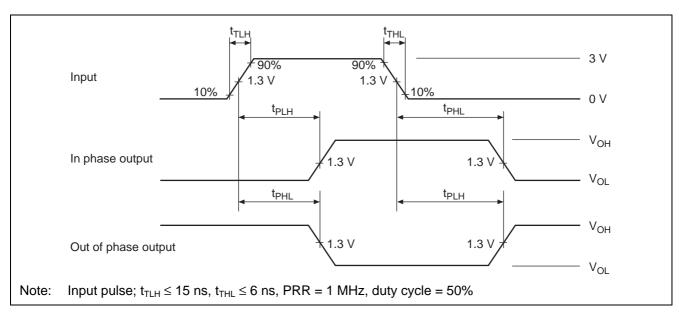
Item	Symbol	Inputs	Outputs	min.	typ.	max.	Unit	Condition		
	t _{PLH}	Co	Σ_1		16	24				
	t _{PHL}	0	41		15	24				
	t _{PLH}	A _i , B _i	Σ_1	_	15	24	- ns			
Propagation delay time	t _{PHL}	Ai, Di	41	_	15	24		$C_L = 15 \text{ pF}, R_L = 2 \text{ k}\Omega$		
Fropagation delay time	t _{PLH}	Co	C ₄	_	11	17				
	t _{PHL}	00		_	15	22				
	t _{PLH}	A _i , B _i	_	_	11	17				
	t _{PHL}	Λ _i , D _i	C ₄	_	12	17				

Testing Method

Test Circuit



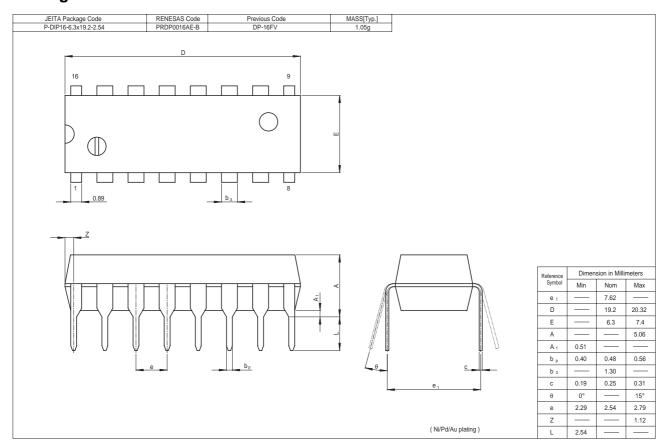
Waveform



Testing Table

Item	From input					Input						Output			
iteiii	to output	B ₄	A_4	B ₃	A_3	B ₂	A ₂	B ₁	A ₁	C ₀	C ₄	Σ_4	Σ_3	Σ_2	Σ_1
	0 7 0	GND	GND	GND	GND	GND	GND	GND	GND	IN	_	_	_	_	OUT
	$C_O \rightarrow \Sigma_i \text{ or } C_4$	GND	GND	GND	4.5 v	GND	4.5 v	GND	4.5 v	IN	OUT	OUT	OUT	OUT	OUT
		GND	GND	GND	GND	GND	GND	GND	IN	GND				_	OUT
		GIND	GIND	GIND	GND	שאט	GIND	IN	GND	GND					001
		GND	GND	GND	GND	GND	IN	GND	GND	GND				OUT	
		GIND	GIND	GIND	GND	IN	GND	GIND	GIND	GND			_	001	
		GND	GND	GND	IN	GND GND	GND	GND	GND			OUT			
				IN	GND		GIVD	GIND	GIND	GIND			001		
t_{PLH}		GND	IN	GND	GND	GND	GND	GND	GND	GND	_	OUT	_	_	_
t_{PHL}	A _i or B _i	IN	GND												
	$ ightarrow \Sigma_i$ or C_4	GND	D GND	D GND	GND	GND	GND	4.5 v	IN	GND				OUT	OUT
		GIVD	GIND					IN	4.5 v	5				0	001
		GND	GND	GND	GND	4.5 v	IN	GND	GND	GND			OUT	OUT	
		GIND	GIND	GIND	5	IN	4.5 v	GIND	GIVD	5			001	0	
		GND	GND	4.5 v	IN	GND	GND	GND	GND	GND		OUT	OUT		
		GND	GIND	IN	4.5 v	שאוט	GIVD	GND	GND	GIND					
		4.5 v	IN	GND	GND	GND	GND	GND	GND	GND	OUT	OUT			
		IN	4.5 v	GIND	GND	GIND	GIVD	GIND	GIVD	GND	001	001			

Package Dimensions



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