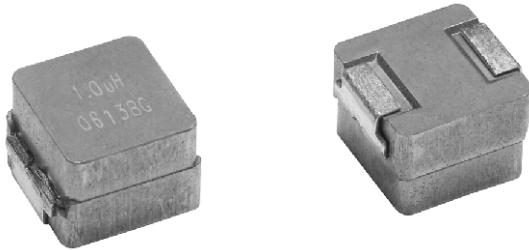


Low Profile, High Current Inductor



Manufactured under one or more of the following:
US Patents; 6,198,375/6,204,744/6,449,829/6,460,244.
 Several foreign patents, and other patents pending.

STANDARD ELECTRICAL SPECIFICATIONS				
Lo INDUCTANCE $\mu\text{H} \pm 20\%$ at 100 kHz 0.25 V, 0 A	DCR $\text{m}\Omega$ TYPICAL 25 °C	DCR $\text{m}\Omega$ MAX 25 °C	HEAT RATING CURRENT DC AMPS ³ TYPICAL	SATURATION CURRENT DC AMPS ⁴ TYPICAL
0.56	3.4	3.6	20	12
0.68	4.2	4.5	18	11.5
0.82	4.6	4.9	16.5	13
1.0	5.6	6.5	13	15
1.5	8.6	9.0	12	12
2.2	13.0	13.6	10	10
3.3	19.9	20.9	8	8
4.7	28.9	30.3	6.5	7
5.6	32.7	34.4	6	7
6.8	42.5	44.6	5.5	5.5
8.2	43.5	45.6	5.5	5.5
10.0	67.9	71.3	4.5	4.5

NOTES:

- All test data is referenced to 25 °C ambient.
- Operating Temperature Range - 55 °C to + 125 °C
- DC current (A) that will cause an approximate ΔT of 40 °C.
- DC current (A) that will cause L_o to drop approximately 20 %
- The part temperature (ambient + temp rise) should not exceed 125 °C under worst case operating conditions. Circuit design, component placement, PWB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application.

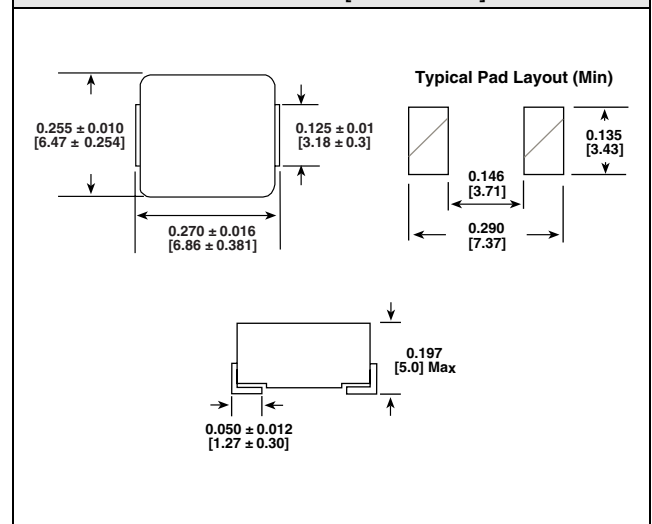
FEATURES

- Shielded construction
- Frequency range below 1 MHz
- Lowest DCR/ μH , in this package size
- Powdered iron composition provides soft saturation
- Handles high transient current spikes without hard saturation
- Ultra low buzz noise, due to composite construction
- 100 % lead (Pb)-free and RoHS compliant


RoHS
 COMPLIANT

APPLICATIONS

- Notebook/Desktop/Server applications
- High current POL converters
- Low profile, high current power supplies
- Battery powered devices
- DC/DC converters in distributed power systems
- DC/DC converter for Field Programmable Gate Array (FPGA)

DIMENSIONS in inches [millimeters]

DESCRIPTION

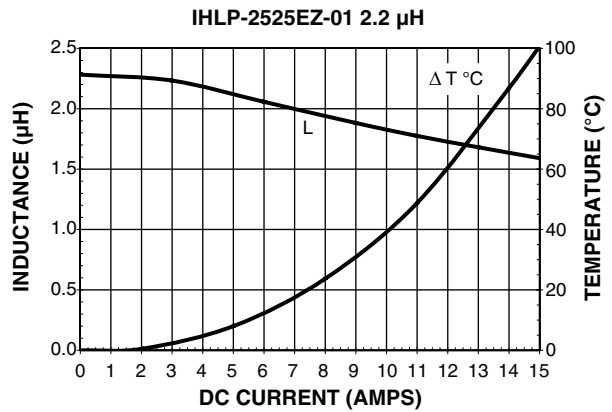
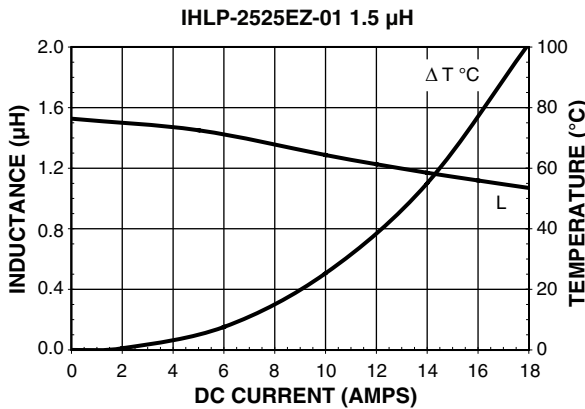
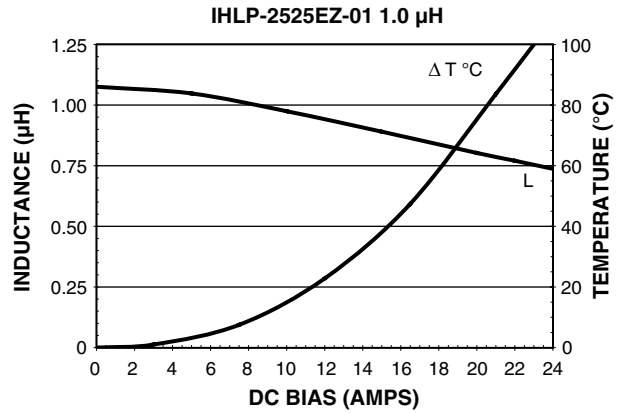
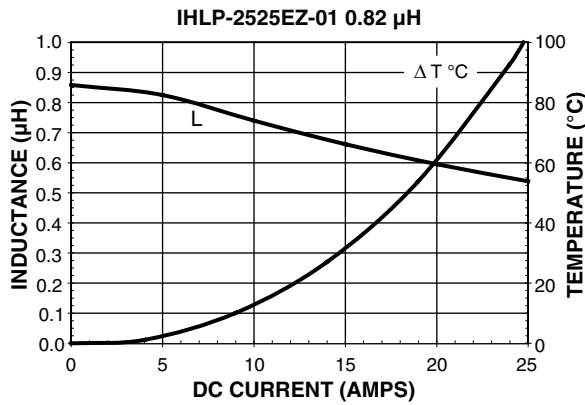
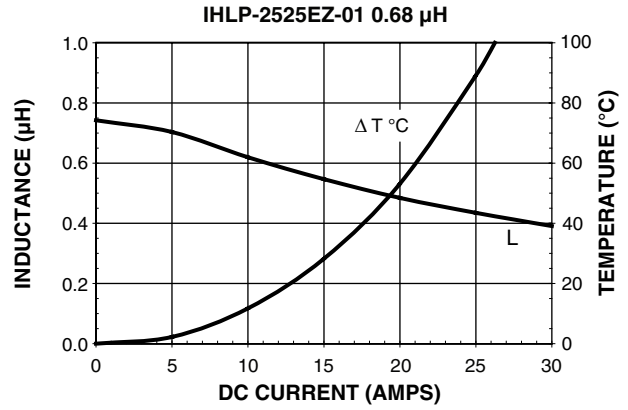
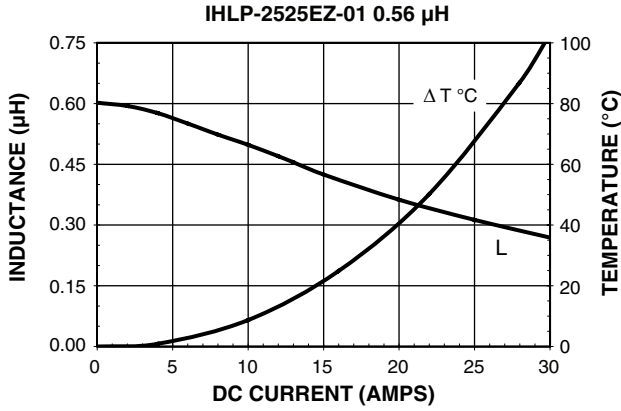
IHLP-2525EZ-01 MODEL	1.0 μH INDUCTANCE VALUE	$\pm 20\%$ INDUCTANCE TOLERANCE	ER PACKAGE CODE	e3 JEDEC LEAD (Pb)-FREE STANDARD
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GLOBAL PART NUMBER

I	H	L	P	2	5	2	5	E	Z	E	R	1	R	0	M	0	1
MODEL				SIZE					PACKAGE CODE		INDUCTANCE VALUE		INDUCTANCE TOLERANCE		SERIES		

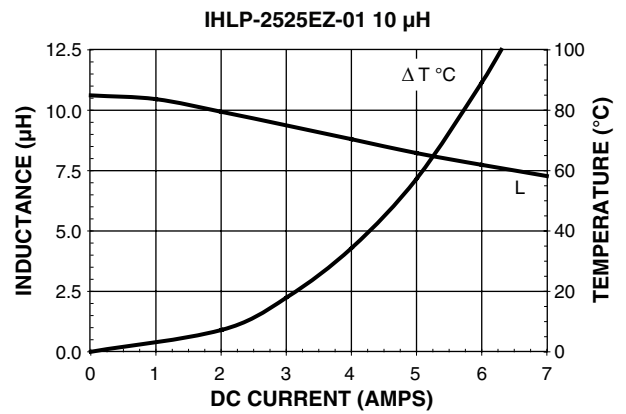
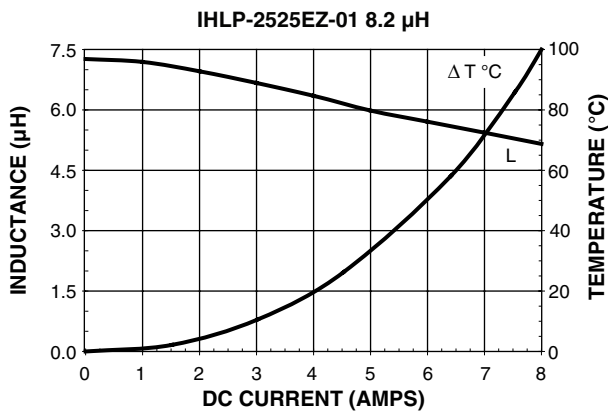
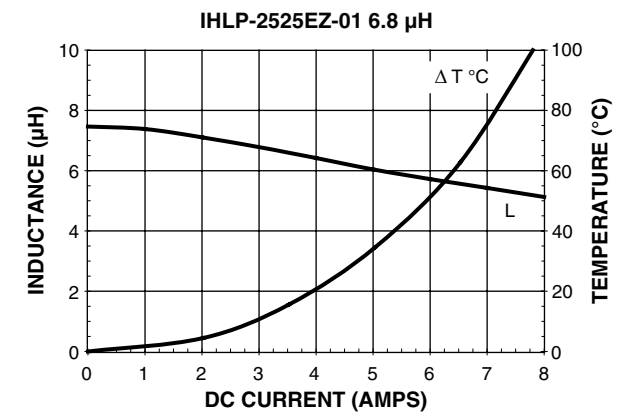
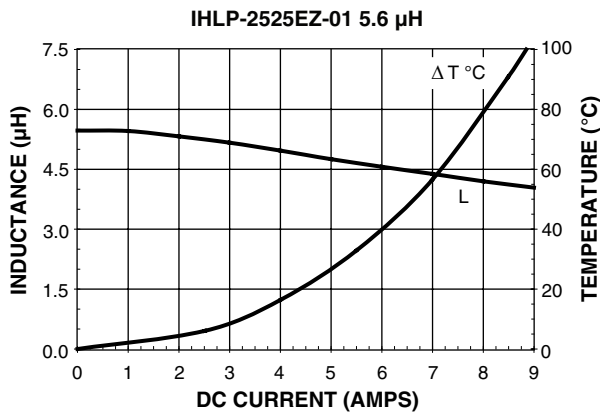
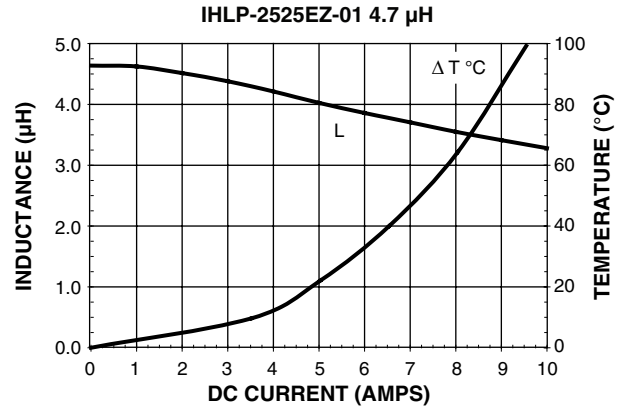
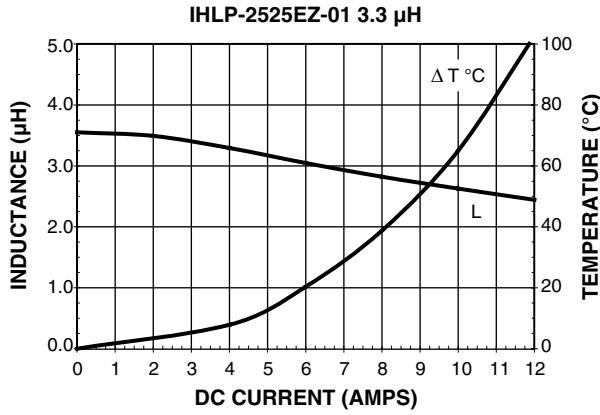


PERFORMANCE GRAPHS





PERFORMANCE GRAPHS





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