

64 Mbit SDR I/F Consumer FCRAM™

Consumer Embedded Application Specific Memory

MB81ES641645A-07

■ FEATURES

- SDRAM Interface
- 1 M word × 16 bit × 4 bank organization
- 1.8 V Low Power Supply ($V_{DD} = V_{DDQ}$)
- 4K Refresh Cycle every 64ms
- Auto- and Self-Refresh
- Burst Read / Burst Write and Burst Read / Single Write Operation Capability
- Programmable Partial Array Self Refresh (PASR)
- Programmable Driver Strength (DS)
- Deep Power Down Mode
- CKE Power Down Mode

■ MAIN SPECIFICATIONS

Part Number		MB81ES641645A-07
Organization		1 M Word × 16 bit × 4 bank
Supply Voltage ($V_{DD} = V_{DDQ}$)		1.7 V to 1.95 V
Clock Frequency (Max.)	CL=2	81 MHz
	CL=3	135 MHz
Clock Period (t_{CK}) (Min.)	CL=2	12.3 ns
	CL=3	7.4 ns
Access Time from Clock (t_{AC}) (Max.)	CL=2	9 ns
	CL=3	6.5 ns
Operating Current (Max.)		T.B.D.
Standby Current (Power Down Mode) (Max.)		T.B.D.
Self Refresh Current (Max.)		T.B.D.

Note: FCRAM is a trademark of Fujitsu Limited, Japan.