

1.2 A, Step-Up/Down/Inverting Switching Regulators

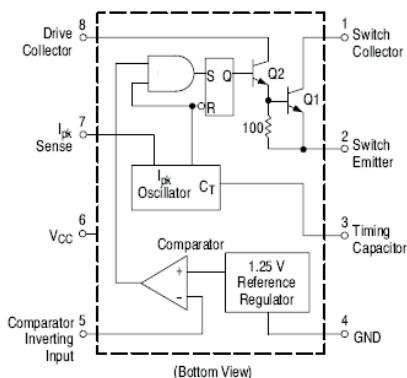
MC34063



SOP8

■ Features

- Low Standby Current
- Current Limiting
- Output Switch Current to 1.2 A
- Output Voltage Adjustable
- Frequency Operation to 100 kHz
- Precision 2% Reference



■ Absolute Maximum Ratings $T_a = 25^\circ\text{C}$

Parameter	Symbol	Rating	Unit
Power Supply Voltage	V_{CC}	40	V
Comparator Input Voltage Range	V_{IR}	-0.3 to +40	V
Switch Collector Voltage	$V_{C(\text{switch})}$	40	V
Switch Emitter Voltage ($V_{Pin\ 1} = 40\text{ V}$)	$V_{E(\text{switch})}$	40	V
Switch Collector to Emitter Voltage	$V_{CE(\text{switch})}$	40	V
Driver Collector Voltage	$V_{C(\text{driver})}$	40	V
Driver Collector Current *	$I_{C(\text{driver})}$	100	mA
Switch Current	I_{SW}	1.2	A
Power Dissipation and Thermal Characteristics			
$T_a = 25^\circ\text{C}$	P_D	625	mW
Thermal Resistance	$R_{\theta JA}$	160	°C/W
Operating Junction Temperature	T_J	150	°C
Operating Ambient Temperature Range	T_A	0 to +70	°C
Storage Temperature Range	T_{stg}	-65 to +150	°C

* Maximum package power dissipation limits must be observed.

MC34063■ Electrical Characteristics $T_A = 25^\circ\text{C}$

Parameter	Symbol	Testconditons	Min	Typ	Max	Unit
Frequency	f_{osc}	$V_{Pin\ 5} = 0\text{ V}, C_T = 1.0\text{ nF}, T_A = 25^\circ\text{C}$	24	33	42	kHz
Charge Current	I_{chg}	$V_{CC} = 5.0\text{ V to }40\text{ V}, T_A = 25^\circ\text{C}$	24	35	42	$\mu\text{ A}$
Discharge Current	I_{dischq}	$V_{CC} = 5.0\text{ V to }40\text{ V}, T_A = 25^\circ\text{C}$	140	220	260	$\mu\text{ A}$
Discharge to Charge Current Ratio	I_{dischq}/I_{chg}	Pin 7 to V_{CC} , $T_A = 25^\circ\text{C}$	5.2	6.5	7.5	
Current Limit Sense Voltage	$V_{ipk(sense)}$	$I_{chg} = I_{dischq}, T_A = 25^\circ\text{C}$	250	300	350	mV
Saturation Voltage, Darlington Connection	$V_{CE(sat)}$	$I_{SW} = 0.8\text{ A}, P_{ins\ 1,8}\text{ connected}$		1.0	1.3	V
Saturation Voltage (Forced $\beta \approx 20$)	$V_{CE(sat)}$	$I_{SW} = 0.8\text{ A}, R_{Pin\ 8} = 82\ \Omega$ to V_{CC} ,		0.45	0.7	V
DC Current Gain	h_{FE}	$I_{SW} = 0.8\text{ A}, V_{CE} = 5.0\text{ V}, T_A = 25^\circ\text{C}$	50	75		
Collector Off-State Current	$I_{C(off)}$	$V_{CE} = 40\text{ V}$		0.01	100	$\mu\text{ A}$
Threshold Voltage $T_A = 25^\circ\text{C}$ $T_A = 0^\circ\text{C to }70^\circ\text{C}$	V_{th}		1.225 1.21	1.25	1.275 1.29	V
Threshold Voltage Line Regulation	R_{gline}	$V_{CC} = 3.0\text{ V to }40\text{ V}$		1.4	5.0	mV
Input Bias Current	I_{IB}	$V_{in} = 0\text{ V}$		-20	-400	nA
Supply Current $V_{Pin\ 5} > V_{th}$, Pin 2 = GND, remaining pins open	I_{CC}	$V_{CC} = 5.0\text{ V to }40\text{ V}, C_T = 1.0\text{ nF},$ Pin 7 = V_{CC} ,			4.0	mA