

Silicon PNP Power Transistors

2SA1535 2SA1535A

DESCRIPTION

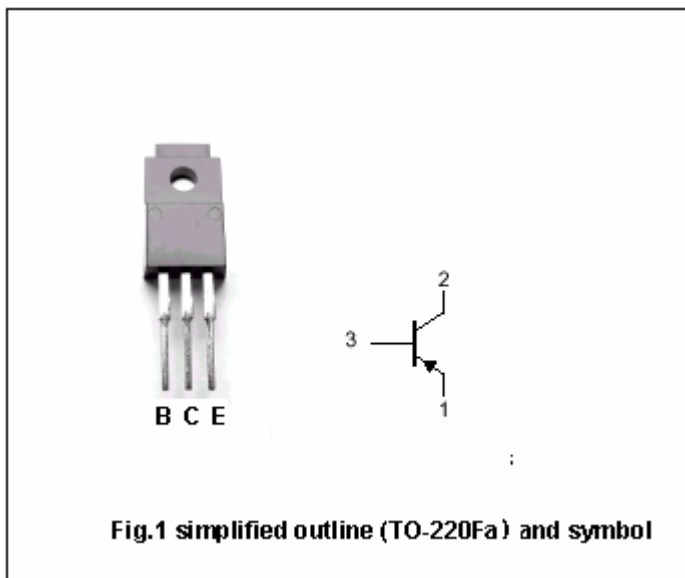
- With TO-220Fa package
- Complement to type 2SC3944/3944A
- Optimum for the driver-stage of a 60W to 100W output amplifier

APPLICATIONS

- For low-frequency driver and high power amplification

PINNING

PIN	DESCRIPTION
1	Base
2	Collector
3	Emitter



Absolute maximum ratings(Ta=25°C)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V _{CBO}	Collector-base voltage	2SA1535	-150	V
		2SA1535A	-180	
V _{CEO}	Collector-emitter voltage	2SA1535	-150	V
		2SA1535A	-180	
V _{EBO}	Emitter-base voltage	Open collector	-5	V
I _C	Collector current		-1.0	A
I _{CM}	Collector current-peak		-1.5	A
P _C	Collector power dissipation	T _a =25°C	2.0	W
		T _C =25°C	15	
T _j	Junction temperature		150	°C
T _{stg}	Storage temperature		-55~150	°C

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CHARACTERISTICS

T_j=25 °C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-emitter breakdown voltage	2SA1535	I _C =-1mA; I _B =0	-150		V
		2SA1535A	I _C =-0.1mA; I _B =0	-180		
V _{(BR)EBO}	Emitter-base breakdown voltage	I _E =-10μA; I _C =0	-5			V
V _{CEsat}	Collector-emitter saturation voltage	I _C =-0.5 A; I _B =-50m A			-2.0	V
V _{BEsat}	Base-emitter saturation voltage	I _C =-0.5 A; I _B =-50m A			-2.0	V
I _{CBO}	Collector cut-off current	V _{CB} =-150V; I _E =0			-10	μA
h _{FE-1}	DC current gain	I _C =-150mA ; V _{CE} =-10V	90		330	
h _{FE-2}	DC current gain	I _C =-500mA ; V _{CE} =-5V	50			
f _T	Transition frequency	I _C =-50mA ; V _{CB} =-10V		200		MHz
C _{OB}	Output capacitance	I _E =0; V _{CB} =-10V; f=1MHz		30		pF

◆ h_{FE-1} classifications

Q	R	S
90-155	130-220	185-330

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PACKAGE OUTLINE

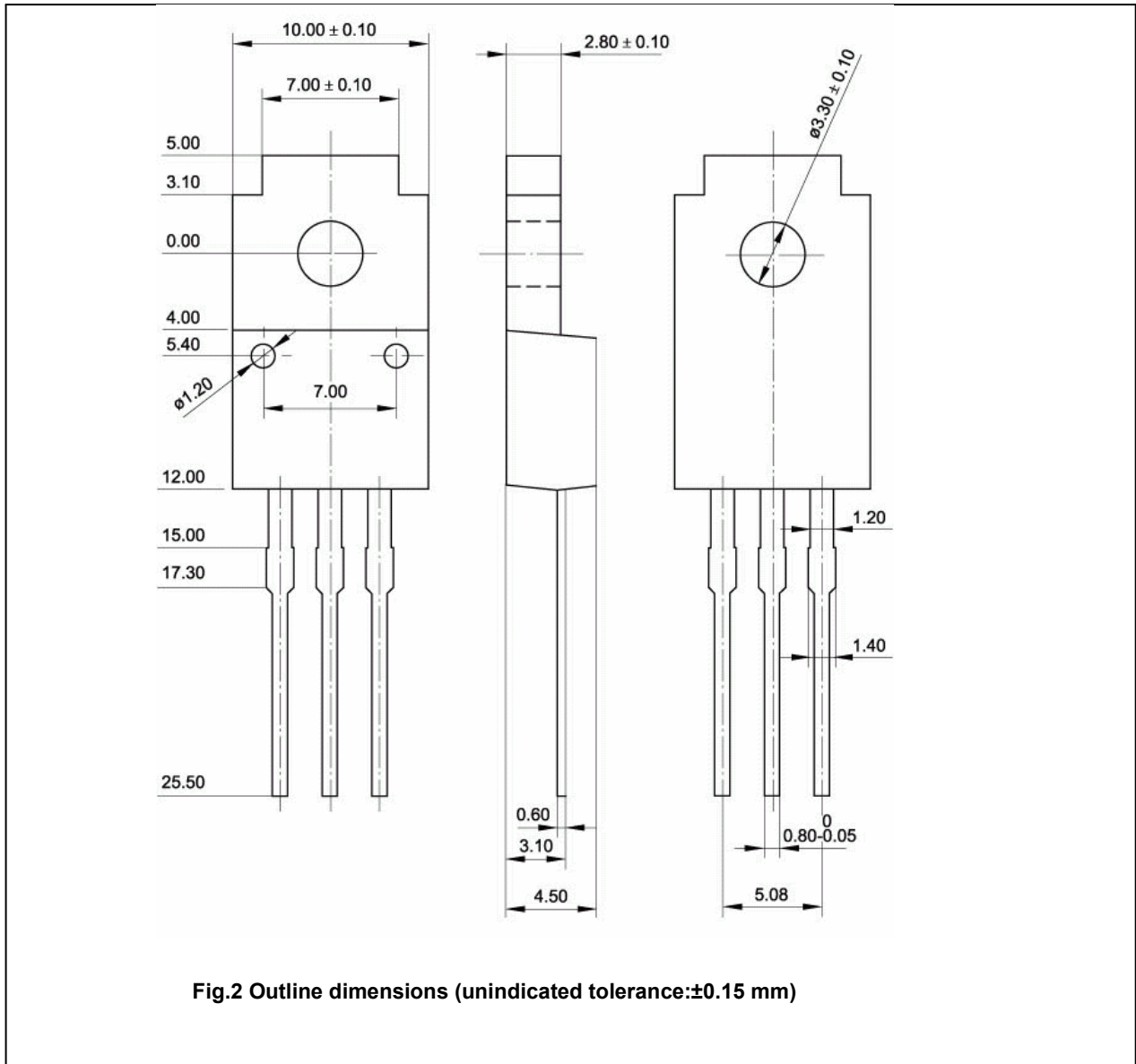


Fig.2 Outline dimensions (unindicated tolerance: ± 0.15 mm)

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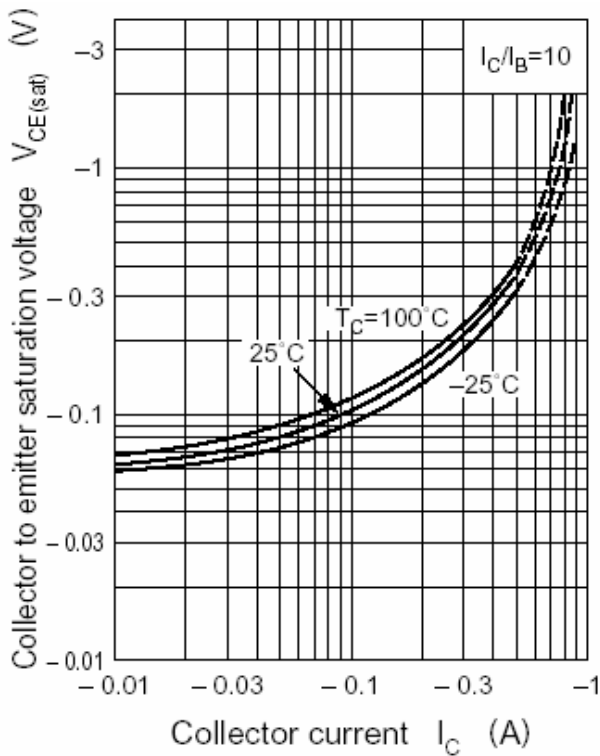


Fig.3 Collector-Emitter Saturation Voltage

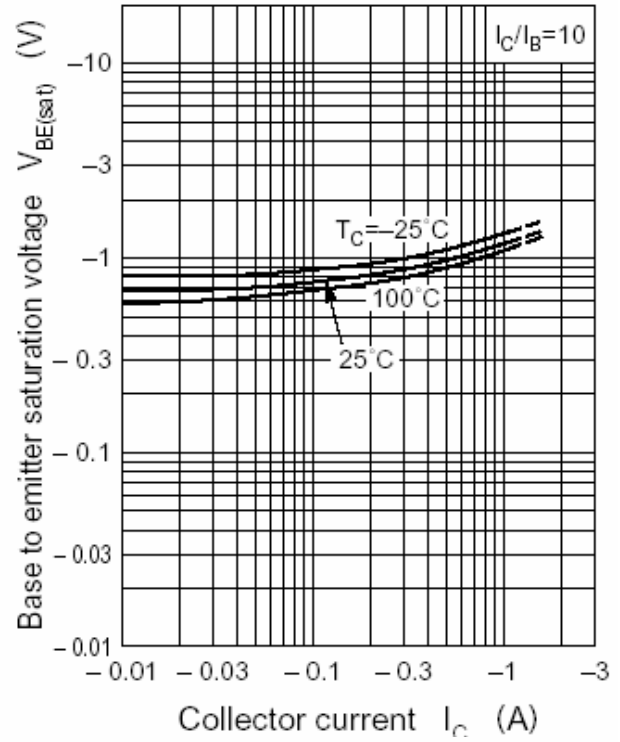


Fig.4 Base-Emitter Saturation Voltage

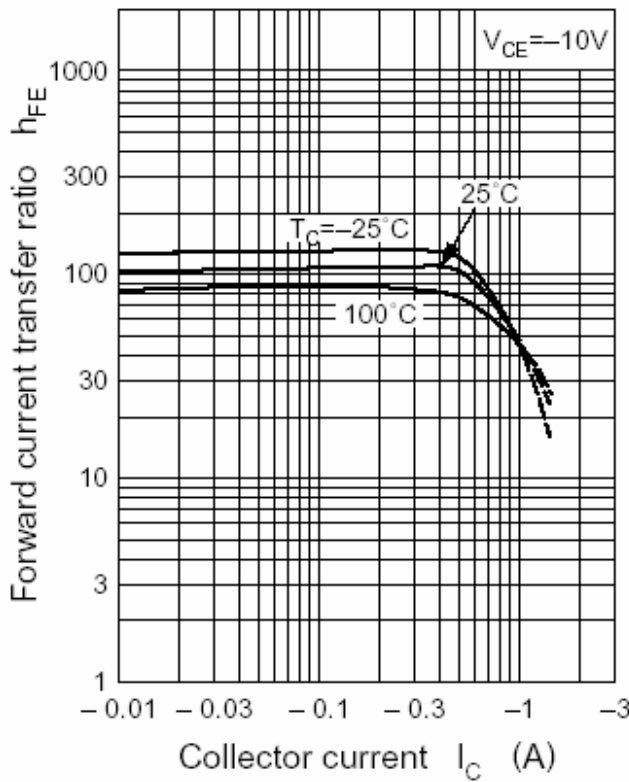


Fig.5 DC current Gain

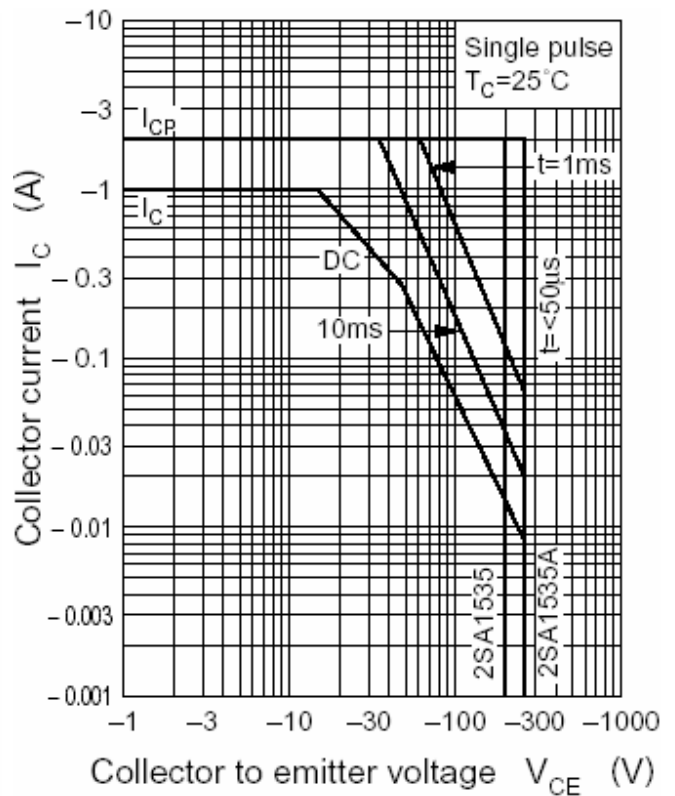


Fig.6 Safe Operating Area