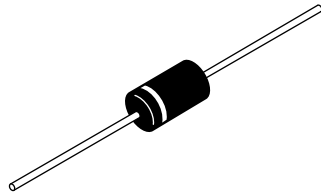


Schottky Rectifier, 3.3 A


C-16


FEATURES

- Low profile, axial leaded outline
- High frequency operation
- Very low forward voltage drop
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Guard ring for enhanced ruggedness and long term reliability
- Lead (Pb)-free plating
- Designed and qualified for industrial level


RoHS
COMPLIANT

PRODUCT SUMMARY

$I_{F(AV)}$	3.3 A
V_R	50/60 V

DESCRIPTION

The 31DQ.. axial leaded Schottky rectifier has been optimized for very low forward voltage drop, with moderate leakage. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection

MAJOR RATINGS AND CHARACTERISTICS

SYMBOL	CHARACTERISTICS	VALUES	UNITS
$I_{F(AV)}$	Rectangular waveform	3.3	A
V_{RRM}		50/60	V
I_{FSM}	$t_p = 5 \mu s$ sine	340	A
V_F	3 Apk, $T_J = 25^\circ C$	0.62	V
T_J		- 40 to 150	$^\circ C$

VOLTAGE RATINGS

PARAMETER	SYMBOL	31DQ05	31DQ06	UNITS
Maximum DC reverse voltage	V_R	50	60	V
Maximum working peak reverse voltage	V_{RWM}			

ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current See fig. 4	I _{F(AV)}	50 % duty cycle at T _L = 105 °C, rectangular waveform		3.3	A
Maximum peak one cycle non-repetitive surge current See fig. 6	I _{FSM}	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated V _{RRM} applied	340	
		10 ms sine or 6 ms rect. pulse		55	
Non-repetitive avalanche energy	E _{AS}	T _J = 25 °C, I _{AS} = 1 A, L = 10 mH		5.0	mJ
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μs Frequency limited by T _J maximum V _A = 1.5 x V _R typical		1.0	A

ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop See fig. 1	V _{FM} ⁽¹⁾	3 A	T _J = 25 °C	0.62	V
		6 A		0.78	
		3 A	T _J = 125 °C	0.54	
		6 A		0.65	
Maximum reverse leakage current See fig. 4	I _{RM} ⁽¹⁾	T _J = 25 °C	V _R = Rated V _R	2	mA
		T _J = 125 °C		15	
Typical junction capacitance	C _T	V _R = 5 V _{DC} (test signal range 100 kHz to 1 MHz) 25 °C		160	pF
Typical series inductance	L _S	Measured lead to lead 5 mm from package body		9.0	nH
Maximum voltage rate of charge	dV/dt	Rated V _R		10 000	V/μs

Note(1) Pulse width < 300 μ s, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	$T_J^{(1)}, T_{Stg}$		- 40 to 150	$^{\circ}\text{C}$
Maximum thermal resistance, junction to ambient	R_{thJA}	DC operation Without cooling fin	80	$^{\circ}\text{C}/\text{W}$
Typical thermal resistance, junction to lead	R_{thJL}	DC operation	15	
Approximate weight			1.2	g
			0.042	oz.
Marking device		Case style C-16	31DQ05	
			31DQ06	

Note(1) $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$ thermal runaway condition for a diode on its own heatsink

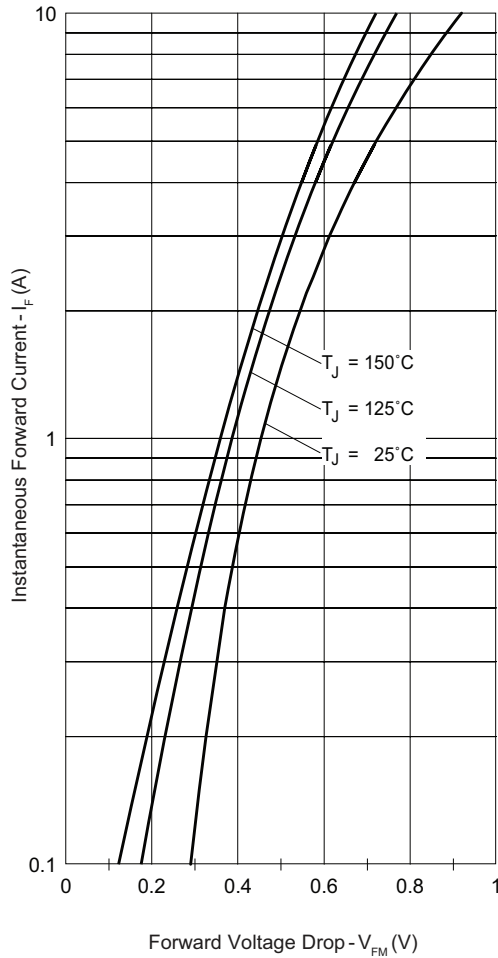


Fig. 1 - Maximum Forward Voltage Drop Characteristics

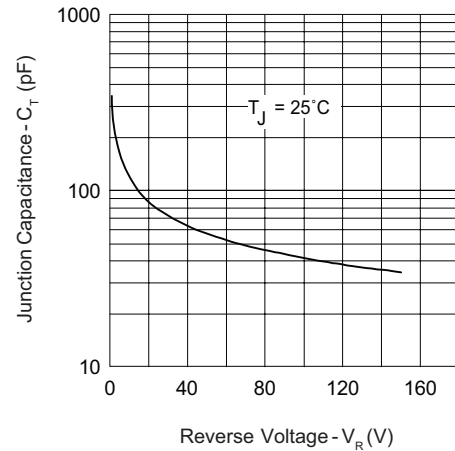


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

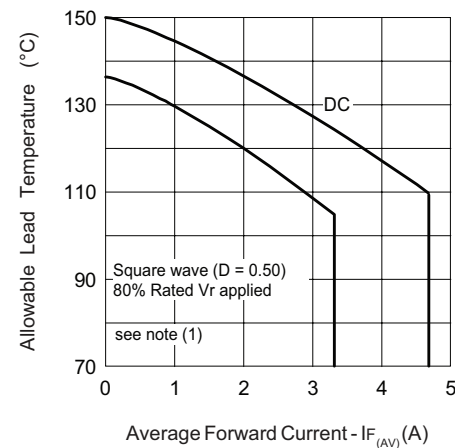


Fig. 4 - Maximum Allowable Lead Temperature vs. Average Forward Current

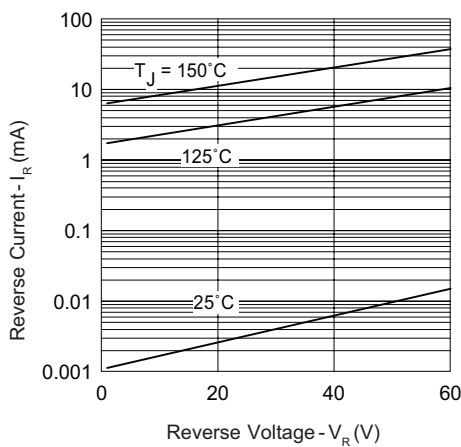


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

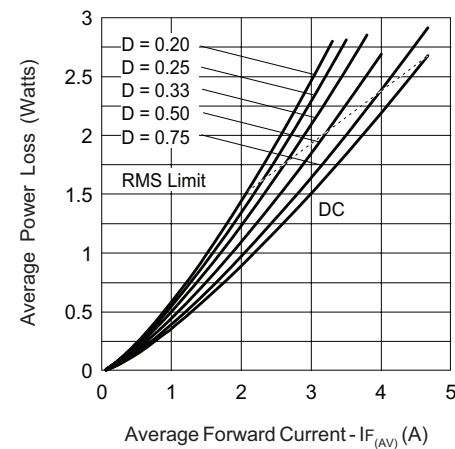


Fig. 5 - Forward Power Loss Characteristics

Note

(1) Formula used: $T_C = T_J - (P_d + P_{dREV}) \times R_{thJC}$

P_d = Forward power loss = $I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 6); P_{dREV} = Inverse power loss = $V_{R1} \times I_R (1 - D)$; I_R at $V_{R1} = 80\%$ rated V_R

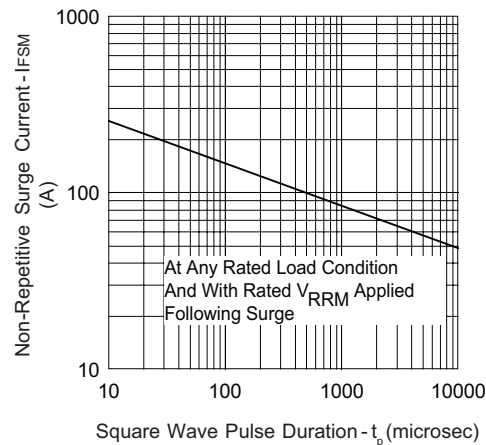


Fig. 6 - Maximum Non-Repetitive Surge Current

ORDERING INFORMATION TABLE

Device code	31	D	Q	06	TR
	1	2	3	4	5
1	- 31 = 3.1 A (axial and small packages - current is x 10)				
2	- D = DO-201 package				
3	- Q = Schottky Q.. series				
4	- 06 = Voltage ratings				
5	- • TR = Tape and reel package (1200 pcs) • None = Box package (500 pcs)				

05 = 50 V
06 = 60 V

LINKS TO RELATED DOCUMENTS	
Dimensions	http://www.vishay.com/doc?95242
Part marking information	http://www.vishay.com/doc?95304
Packaging information	http://www.vishay.com/doc?95309



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