



DC COMPONENTS CO., LTD.

DISCRETE SEMICONDUCTORS

2N3055

TECHNICAL SPECIFICATIONS OF NPN EPITAXIAL PLANAR TRANSISTOR

Description

Designed for power switching circuits, series and shunt regulators, output stages and high fidelity amplifiers.

Pinning

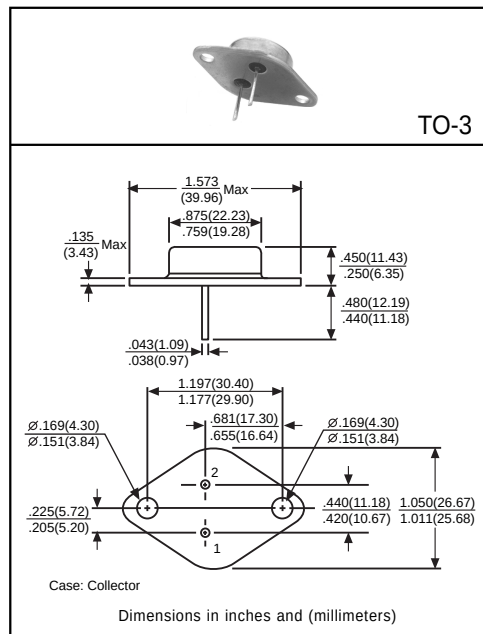
1 = Base

2 = Emitter

Case = Collector

Absolute Maximum Ratings($T_A=25^{\circ}\text{C}$)

Characteristic	Symbol	Rating	Unit
Collector-Base Voltage	V_{CB0}	100	V
Collector-Emitter Voltage	V_{CE0}	60	V
	V_{CEV}	70	V
Emitter-Base Voltage	V_{EB0}	7	V
Collector Current	I_C	15	A
Base Current	I_B	7	A
Total Power Dissipation($T_C=25^{\circ}\text{C}$)	P_D	115	W
Junction Temperature	T_J	+200	$^{\circ}\text{C}$
Storage Temperature	T_{STG}	-65 to +200	$^{\circ}\text{C}$



Electrical Characteristics

(Ratings at 25°C ambient temperature unless otherwise specified)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Conditions
Collector-Emitter Sustaining Voltage	$V_{CE0(sus)}$	60	-	-	V	$I_C=0.2A, I_B=0$
	$V_{CER(sus)}$	70	-	-	V	$I_C=0.2A, R_{BE}=100\Omega$
Collector Cutoff Current	I_{CE0}	-	-	0.7	mA	$V_{CE}=30V, I_B=0$
	I_{CEX}	-	-	1	mA	$V_{CE}=100V, V_{BE(off)}=1.5V$
		-	-	5	mA	$V_{CE}=100V, V_{BE(off)}=1.5V, T_C=150^{\circ}\text{C}$
Emitter Cutoff Current	I_{EBO}	-	-	5	mA	$V_{BE}=7V, I_C=0$
Collector-Emitter Saturation Voltage ⁽¹⁾	$V_{CE(sat)1}$	-	-	1.1	V	$I_C=4A, I_B=0.4A$
	$V_{CE(sat)2}$	-	-	3	V	$I_C=10A, I_B=3.3A$
Base-Emitter On Voltage ⁽¹⁾	$V_{BE(on)}$	-	-	1.5	V	$I_C=4A, V_{CE}=4V$
DC Current Gain ⁽¹⁾	h_{FE1}	20	-	70	-	$I_C=4A, V_{CE}=4V$
	h_{FE2}	5	-	-	-	$I_C=10A, V_{CE}=4V$
Second Breakdown Collector with Base Forward Bias	$I_{S/b}$	2.87	-	-	A	$V_{CE}=40V, t=1.0s, \text{Non-repetitive}$
Current Gain - Bandwidth Product	f_T	2.5	-	-	MHz	$I_C=0.5A, V_{CE}=10V, f=1MHz$
Small-Signal Current Gain	h_{fe}	15	-	120	-	$I_C=10A, V_{CE}=4V, f=1KHz$
Small-Signal Current Gain Cutoff Frequency	f_{hfe}	10	-	-	KHz	$I_C=1A, V_{CE}=4V, f=1KHz$

(1)Pulse Test: Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 2\%$