# FCSP240LTR

## **Vishay High Power Products**

# FlipKY<sup>®</sup>, 1.5 A **Chip Scale Package Schottky Barrier Rectifier**



- Ultra low V<sub>F</sub> per footprint area
- Low leakage
- · Low thermal resistance
- One-fifth footprint of SMA
- · Available tested on tape and reel
- · Reverse polarity protection
- · Current steering
- Freewheeling
- Flyback
- Oring

#### DESCRIPTION

Vishay's FlipKY® product family utilizes wafer level chip scale packaging to deliver Schottky diodes with the lowest V<sub>F</sub> to PCB footprint area in industry. The four bump 1.5 x 1.5 mm devices can deliver up to 1.5 A and occupy only 2.3 mm<sup>2</sup> of board space. The anode and cathode connections are made through solder bump pads on one side of the silicon enabling designers to strategically place the diodes on the PCB. This design not only minimizes board space but also reduces thermal resistance and inductance, which can improve overall circuit efficiency.

Typical applications include hand-held, portable equipment such as cell phones, MP3 players, bluetooth, GPS, PDAs, and portable hard disk drives where space savings and performance are crucial.

MAJOR RATINGS AND CHARACTERISTICS				
SYMBOL	CHARACTERISTICS	MAX.	UNITS	
V <sub>RRM</sub>		40	V	
I <sub>F(AV)</sub>	Rectangular waveform	1.5	٨	
I <sub>FSM</sub>		250	A	
V <sub>F</sub>	1.5 Apk, T <sub>J</sub> = 125 °C	0.42	V	
TJ		- 55 to 150	°C	

VOLTAGE RATINGS				
PARAMETER	SYMBOL	FCSP240LTR	UNITS	
Maximum DC reverse voltage	V <sub>R</sub>	- 40	V	
Maximum working peak reverse voltage	V <sub>RWM</sub>		v	

PRODUCT SUMMARY		
I <sub>F(AV)</sub>	1.5 A	
V <sub>R</sub>	40 V	

**FlipKY<sup>®</sup>** 

- Super low profile (0.6 mm)

### APPLICATIONS



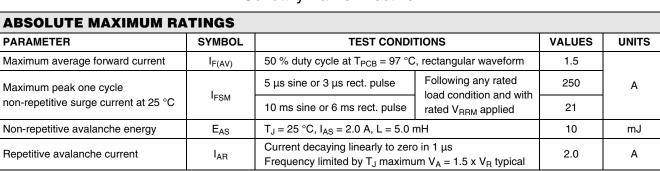




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<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise noted)						
PARAMETER	SYMBOL	TEST CONDITIONS		TYP.	MAX.	UNITS
Maximum forward voltage drop See fig. 1	V <sub>FM</sub> <sup>(1)</sup>	1.5 A	T <sub>.1</sub> = 25 °C	0.45	0.49	V
		3 A	1j=25 C	0.55	0.60	
		1.5 A	T 105 %C	0.37	0.42	
		3 A	Τ <sub>J</sub> = 125 °C	0.51	0.57	
	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C	V <sub>R</sub> = Rated V <sub>R</sub>	15	80	μA mA
			V <sub>R</sub> = 20 V	3.5	20	
			V <sub>R</sub> = 10 V	2	10	
Maximum reverse leakage current			V <sub>R</sub> = 5 V	1.5	5	
See fig. 2		T <sub>J</sub> = 125 °C	V <sub>R</sub> = Rated V <sub>R</sub>	9	20	
			V <sub>R</sub> = 20 V	3.5	8	
			V <sub>R</sub> = 10 V	2.5	6	
			V <sub>R</sub> = 5 V	2	5	
Maximum junction capacitance	CT	$V_{R} = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C - 160		160	pF	
Maximum voltage rate of charge dV/dt Rated V <sub>R</sub>		-	10 000	V/µs		

Note

 $^{(1)}\,$  Pulse width < 300  $\mu s,$  duty cycle < 2 %

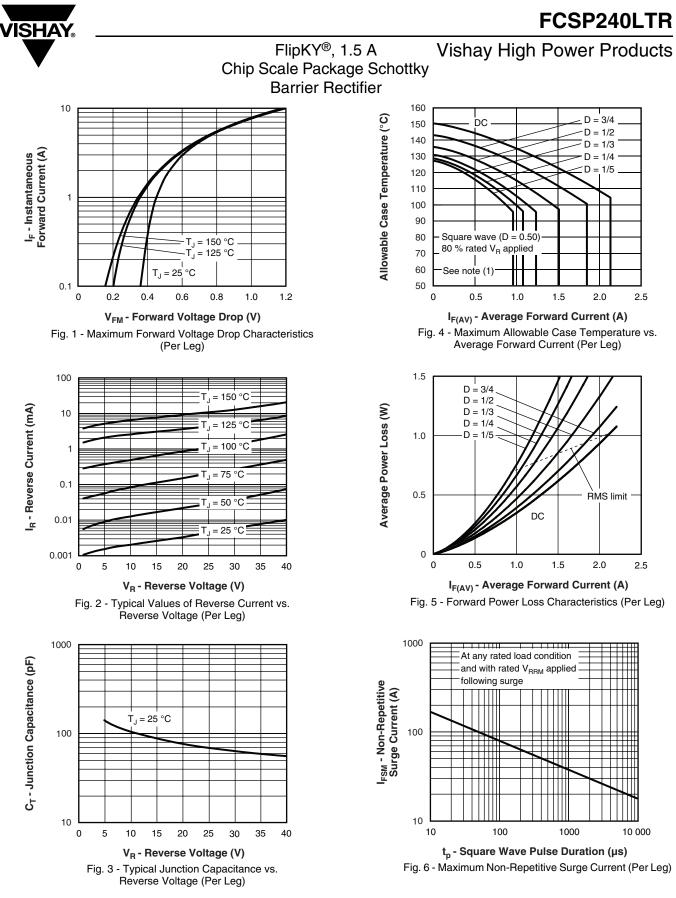
THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature range	T <sub>J</sub> <sup>(1)</sup> , T <sub>Stg</sub>		- 55 to 150	°C	
Typical thermal resistance, junction to PCB RthJL <sup>(2)</sup>		DC operation	40	°C/W	
Maximum thermal resistance, junction to ambient R <sub>thJA</sub>			62	C/ VV	

#### Notes

(1)

 $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$  thermal runaway condition for a diode on its own heatsink

(2) Mounted 1" square PCB



#### Note

<sup>(1)</sup> Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$ ;

 $Pd = Forward power loss = I_{F(AV)} \times V_{FM} at (I_{F(AV)}/D) (see fig. 6); Pd_{REV} = Inverse power loss = V_{R1} \times I_R (1 - D); I_R at 80 \% V_R applied$ 

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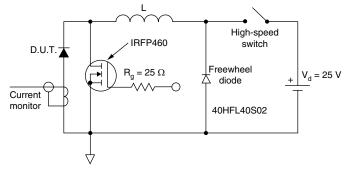


Fig. 7 - Unclamped Inductive Test Circuit

LINKS TO RELATED DOCUMENTS		
Dimensions	http://www.vishay.com/doc?95282	
Part marking information	http://www.vishay.com/doc?95281	
Packaging information	http://www.vishay.com/doc?95062	



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