

STB12NM50ND STD12NM50ND

N-channel 500 V, 0.29 Ω, 11 A, FDmesh[™] II Power MOSFET (with fast diode), D²PAK, DPAK

Preliminary Data

Features

Туре	V _{DSS} (@Tjmax)	R _{DS(on)} max	I _D
STB12NM50ND	550 V	0.38 Ω	11 A
STD12NM50ND	550 V	0.38 Ω	11 A

- The worldwide best R_{DS(on)}* area amongst the fast recovery diode devices
- 100% avalanche tested
- Low input capacitance and gate charge
- Low gate input resistance
- Extremely high dv/dt and avalanche capabilities

Application

Switching applications

Description

The FDmesh[™] II series belongs to the second generation of MDmesh[™] technology. This revolutionary Power MOSFET associates a new vertical structure to the company's strip layout and associates all advantages of reduced onresistance and fast switching with an intrinsic fastrecovery body diode.Strongly recommended for bridge topologies, in ZVS phase-shift converters.



Figure 1. Internal schematic diagram



Table 1. Device summary

Orc	ler codes	Marking	Marking Package Packagin	
STB	12NM50ND	12NM50ND	D ² PAK	Tape and reel
STD	12NM50ND	12NM50ND	DPAK	Tape and reel

Contents

1	Electrical ratings
2	Electrical characteristics
3	Test circuits
4	Package mechanical data7
5	Packaging mechanical data 10
6	Revision history



1 Electrical ratings

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Table 2.	Absolute maximum ratings

Symbol	Parameter	Value	Unit
V _{DS}	Drain-source voltage (V _{GS} =0)	500	V
V _{GS}	Gate-source voltage	± 25	V
۱ _D	Drain current (continuous) at $T_C = 25 \ ^{\circ}C$	11	Α
۱ _D	Drain current (continuous) at $T_C = 100 \ ^{\circ}C$	6.9	Α
I _{DM} ⁽¹⁾	Drain current (pulsed)	44	A
P _{TOT}	Total dissipation at T_{C} = 25 °C	100	W
dv/dt ⁽²⁾	Peak diode recovery voltage slope	40	V/ns
T _{stg}	Storage temperature	-55 to 150	°C
Тj	Operating junction temperature	150	°C

1. Pulse width limited by safe operating area

2. $I_{SD} \leq 11$ A, di/dt ≤ 600 A/µs, $V_{DD} = 80\%$ $V_{(BR)DSS}$

Table 3. Thermal data

Symbol	Parameter	D ² PAK	DPAK	Unit
Rthj-case	Thermal resistance junction-case max	1.25		°C/W
Rthj-pcb	Thermal resistance junction-pcb max	30 50		°C/W
Τ _Ι	Maximum lead temperature for soldering purposes	300		°C

Table 4. Avalanche characteristics

Symbol	Parameter	Max value	Unit
I _{AS}	Avalanche current, repetitive or not-repetitive (pulse width limited by Tj max)	5	A
E _{AS}	Single pulse avalanche energy (starting Tj = 25 °C, $I_D = I_{AS}$, $V_{DD} = 50$ V)	350	mJ



2 Electrical characteristics

(T_{CASE}=25°C unless otherwise specified)

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
V _{(BR)DSS}	Drain-source breakdown voltage	$I_D = 1 \text{ mA}, V_{GS} = 0$	500			V
dv/dt ⁽¹⁾	Drain-source voltage slope	V _{DD} = 400 V,I _D = 11 A, V _{GS} = 10 V		44		V/ns
I _{DSS}	Zero gate voltage drain current ($V_{GS} = 0$)	V _{DS} = Max rating, V _{DS} = Max rating,@125 °C			1 100	μΑ μΑ
I _{GSS}	Gate body leakage current (V _{DS} = 0)	V _{GS} = ±20 V			100	nA
V _{GS(th)}	Gate threshold voltage	$V_{DS} = V_{GS}$, $I_D = 250 \ \mu A$	3	4	5	V
R _{DS(on)}	Static drain-source on resistance	V _{GS} = 10 V, I _D = 5.5 A		0.29	0.38	Ω

Table 5. On/off states

1. Value measured at turn off under inductive load

Table 0.	Dynamic					
Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
g _{fs} ⁽¹⁾	Forward transconductance	V _{DS} =15 V, I _D = 5.5 A		8		S
C _{iss} C _{oss} C _{rss}	Input capacitance Output capacitance Reverse transfer capacitance	V _{DS} = 50 V, f =1 MHz, V _{GS} = 0		TBD TBD TBD		pF pF pF
C _{oss eq.} ⁽²⁾	Equivalent output capacitance	$V_{GS} = 0, V_{DS} = 0 \text{ to } 400 \text{ V}$		TBD		pF
Rg	Gate input resistance	f=1 MHz Gate DC Bias=0 Test signal level=20 mV open drain		TBD		Ω
Q _g Q _{gs} Q _{gd}	Total gate charge Gate-source charge Gate-drain charge	V_{DD} = 400 V, I _D = 11 A V_{GS} = 10 V <i>(see Figure 3)</i>		TBD TBD TBD		nC nC nC

Table 6. Dynamic

1. Pulsed: pulse duration = 300 μ s, duty cycle 1.5%

2. $C_{oss~eq.}$ is defined as a constant equivalent capacitance giving the same charging time as C_{oss} when V_{DS} increases from 0 to 80% V_{DSS}

Symbol	Parameter	Test conditions	Min	Тур	Max	Unit
t _{d(on)} t _r t _{d(off)} t _f	Turn-on delay time Rise time Turn-off delay time Fall time	$V_{DD} = 250 \text{ V}, \text{ I}_{D} = 5.5 \text{ A},$ $R_{G} = 4.7 \Omega, V_{GS} = 10 \text{ V}$ (see Figure 2)		TBD TBD TBD TBD		ns ns ns ns

Table 7.Switching times

Table 8. Source drain diode

Symbol	Parameter	Test conditions	Min	Тур	Max	Unit
I _{SD} I _{SDM} ⁽¹⁾	Source-drain current Source-drain current (pulsed)				11 44	A A
V _{SD} ⁽²⁾	Forward on voltage	I _{SD} = 11 A, V _{GS} =0			TBD	V
t _{rr} Q _{rr} I _{RRM}	Reverse recovery time Reverse recovery charge Reverse recovery current	$I_{SD} = 11 \text{ A, di/dt} = 100 \text{ A/}\mu\text{s,}$ $V_{DD} = 100 \text{ V}$ (see Figure 4)		TBD TBD TBD		ns nC A
t _{rr} Q _{rr} I _{RRM}	Reverse recovery time Reverse recovery charge Reverse recovery current	V _{DD} = 100 V di/dt =100 A/µs, I _{SD} = 11 A Tj = 150 °C <i>(see Figure 4)</i>		TBD TBD TBD		ns nC A

1. Pulse width limited by safe operating area

2. Pulsed: pulse duration = $300 \ \mu$ s, duty cycle 1.5%



3 Test circuits

Figure 2. Switching times test circuit for resistive load





Gate charge test circuit

Figure 4. Test circuit for inductive load switching and diode recovery times





Figure 5. Unclamped Inductive load test circuit



57



Figure 3.



6/13

4 Package mechanical data

In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a lead-free second level interconnect. The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: *www.st.com*



Dim	mm			inch		
	Min	Тур	Max	Min	Тур	Мах
A	4.40		4.60	0.173		0.181
A1	0.03		0.23	0.001		0.009
b	0.70		0.93	0.027		0.037
b2	1.14		1.70	0.045		0.067
С	0.45		0.60	0.017		0.024
c2	1.23		1.36	0.048		0.053
D	8.95		9.35	0.352		0.368
D1	7.50			0.295		
E	10		10.40	0.394		0.409
E1	8.50			0.334		
е		2.54			0.1	
e1	4.88		5.28	0.192		0.208
Н	15		15.85	0.590		0.624
J1	2.49		2.69	0.099		0.106
L	2.29		2.79	0.090		0.110
L1	1.27		1.40	0.05		0.055
L2	1.30		1.75	0.051		0.069
R		0.4			0.016	
V2	0°		8°	0°		8°



	TO-252 (DPAK) mechanical data				
DIM.	mm.				
	min.	typ	max.		
A	2.20		2.40		
A1	0.90		1.10		
A2	0.03		0.23		
b	0.64		0.90		
b4	5.20		5.40		
С	0.45		0.60		
c2	0.48		0.60		
D	6.00		6.20		
D1		5.10			
E	6.40		6.60		
E1		4.70			
e		2.28			
e1	4.40		4.60		
Н	9.35		10.10		
L	1				
L1		2.80			
L2		0.80			
L4	0.60		1		
R		0.20			
V2	0 °		8 °		





57

5 Packaging mechanical data



TAPE AND REEL SHIPMENT



* on sales type

DPAK FOOTPRINT





TAPE AND REEL SHIPMENT



6 Revision history

Table 9.Document revision history

Date	Revision	Changes
23-Sep-2008	1	First release



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