

### Specifications

| Parameter                                       | Unit                       | Minimum | Typical | Maximum |
|---|----------------------------|---------|---------|---------|
| Center Frequency                                | MHz                        | 89.85   | 90      | 90.15   |
| Insertion Loss                                  | dB                         | -       | 33      | 35      |
| 1.5dB Bandwidth                                 | MHz                        | 24      | 24.14   | -       |
| 3 dB Bandwidth                                  | MHz                        | -       | 24.24   | -       |
| 35 dB Bandwidth                                 | MHz                        | -       | 24.75   | 24.8    |
| 40 dB Bandwidth                                 | MHz                        | -       | 24.88   | -       |
| 50 dB Bandwidth                                 | MHz                        | -       | 25.14   | -       |
| Passband Variation                              | dB                         | -       | 1.1     | 1.5     |
| Absolute Delay                                  | usec                       | -       | 3.8     | -       |
| Phase Linearity( $f_0 \pm 11\text{MHz}$ )       | deg                        | -       | 6       | -       |
| Group Delay Variation( $f_0 \pm 11\text{MHz}$ ) | nsec                       | -       | 70      | -       |
| Ultimate Rejection                              | dB                         | 50      | 51      | -       |
| Material Temperature coefficient                | KHz/°C                     | -8.46   |         |         |
| Ambient Temperature                             | °C                         | 25      |         |         |
| Package Size                                    | DIP3512 (35.0x12.8x4.7mm3) |         |         |         |

#### Notes:


1. All specifications are based on the test circuit shown
2. In production, devices will be tested at room temperature to a guardbanded specification to ensure electrical compliance over temperature
3. Electrical margin has been built into the design to account for the variations due to temperature drift and manufacturing tolerances
4. This is the optimum impedance in order to achieve the performance show

### Matching Configuration

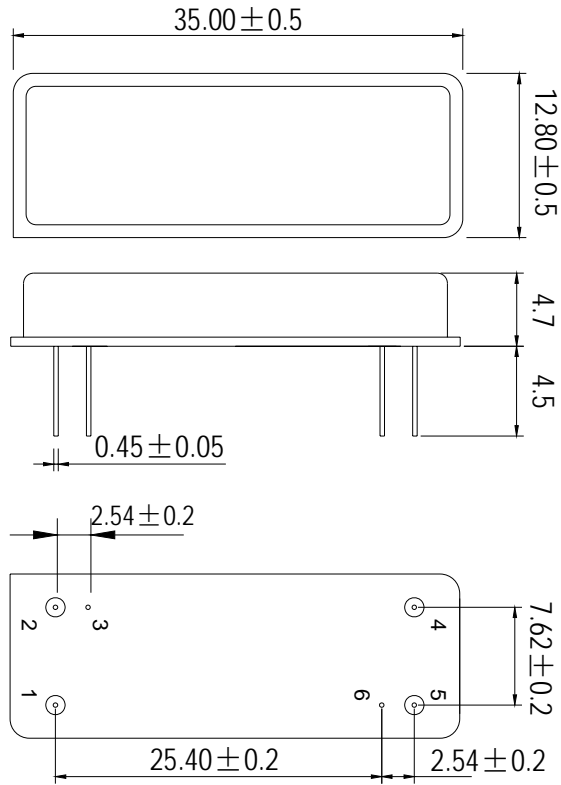


**Source/Load Impedance=50 ohm**

Notes - Component values may change depending on board layout.

|  |   |             |           |      |
|--|---|-------------|-----------|------|
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|  |   | Rev. Date   | 2006-8-30 |      |
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*Package Dimension*



|        |            |
|--------|------------|
| Input  | 1          |
| Output | 5          |
| Ground | 2, 3, 4, 6 |

**Package: DIP3512**

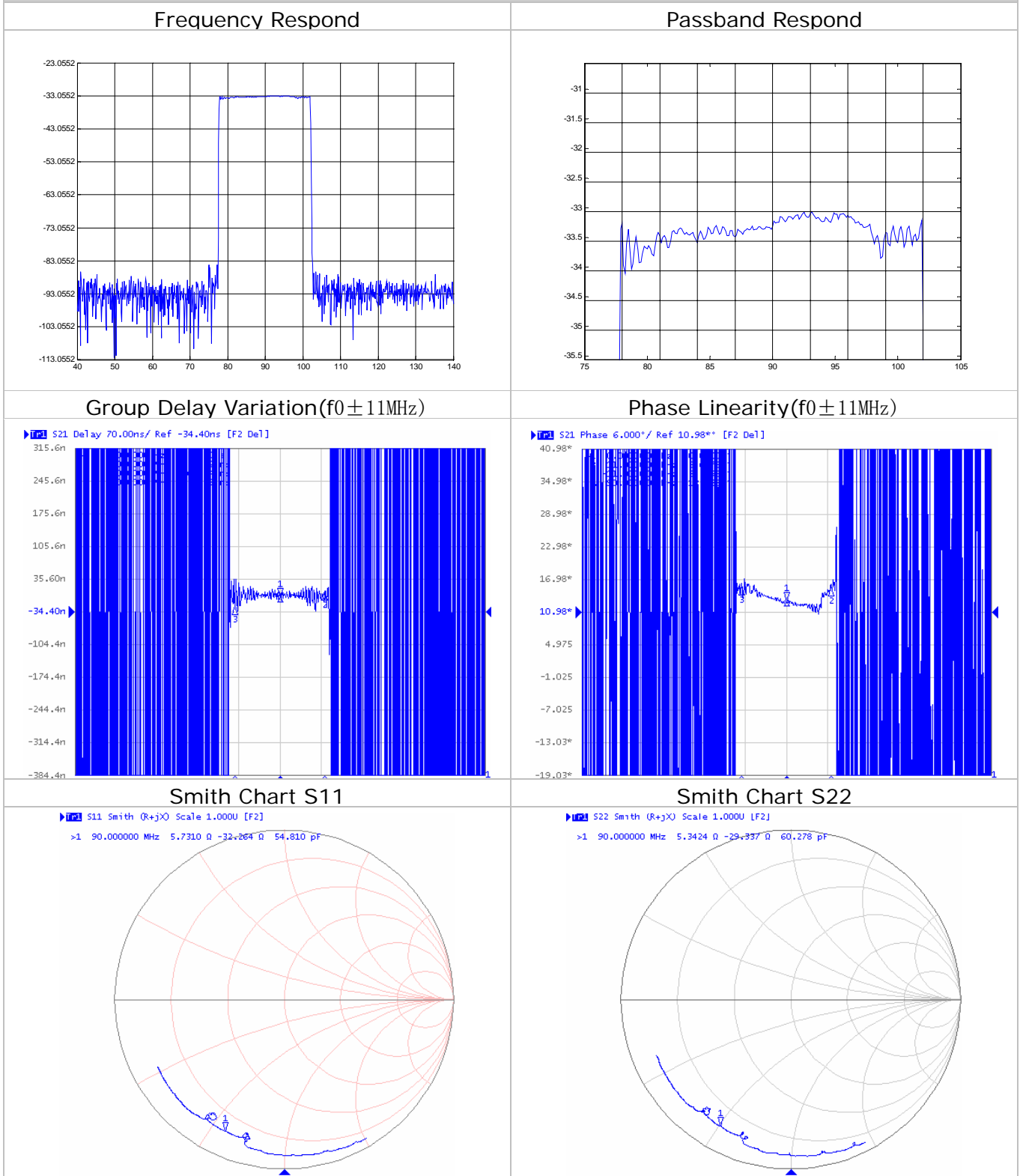
**Unit: mm**



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Typical Performance



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