



**CHENMKO ENTERPRISE CO.,LTD**

*Lead free devices*

**SURFACE MOUNT  
SWITCHING DIODE**  
VOLTAGE 80 Volts CURRENT 0.1 Ampere

**CHN222PT**

#### APPLICATION

- \* Ultra high speed switching

#### FEATURE

- \* Small surface mounting type. (SC-75/SOT-416)
- \* High speed. (TRR=1.5nSec Typ.)
- \* Suitable for high packing density.
- \* Maximum total power dissipation is 150mW.
- \* Peak forward current is 300mA.

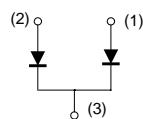
#### CONSTRUCTION

- \* Silicon epitaxial planar

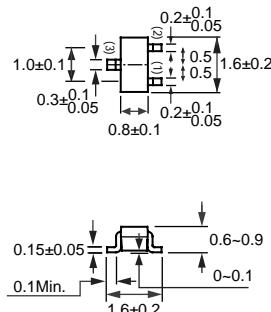
#### MARKING

- \* A4

#### CIRCUIT



**SC-75/SOT-416**



Dimensions in millimeters

**SC-75/SOT-416**

**MAXIMUM RATINGS** ( At TA = 25°C unless otherwise noted )

RATINGS	SYMBOL	CHN222PT	UNITS
Maximum Recurrent Peak Reverse Voltage	V <sub>RRM</sub>	80	Volts
Maximum RMS Voltage	V <sub>RMS</sub>	56	Volts
Maximum DC Blocking Voltage	V <sub>D</sub>	80	Volts
Maximum Average Forward Rectified Current	I <sub>O</sub>	0.1	Amps
Peak Forward Surge Current at 1uSec.	I <sub>FSM</sub>	4.0	Amps
Typical Junction Capacitance between Terminal (Note 1)	C <sub>J</sub>	3.5	pF
Maximum Reverse Recovery Time (Note 2)	T <sub>RR</sub>	4.0	nSec
Maximum Operating Temperature Range	T <sub>J</sub>	+150	°C
Storage Temperature Range	T <sub>STG</sub>	-55 to +150	°C

**ELECTRICAL CHARACTERISTICS** ( At TA = 25°C unless otherwise noted )

CHARACTERISTICS	SYMBOL	CHN222PT	UNITS
Maximum Instantaneous Forward Voltage at I <sub>F</sub> = 100mA	V <sub>F</sub>	1.20	Volts
Maximum Average Reverse Current at V <sub>R</sub> = 70V	I <sub>R</sub>	0.1	uAmps

NOTES : 1. Measured at 1.0 MHz and applied reverse voltage of 6.0 volts.  
2. Measured at applied forward current of 5mA and reverse voltage of 6.0 volts.  
3. ESD sensitive product handling required.

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## RATING CHARACTERISTIC CURVES ( CHN222PT )

FIG. 1 - TYPICAL FORWARD CURRENT DERATING CURVE

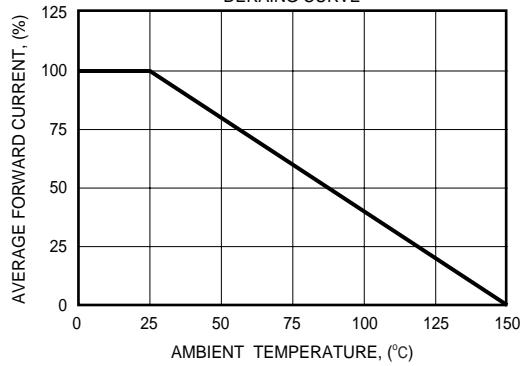


FIG. 2 - FORWARD CHARACTERISTICS

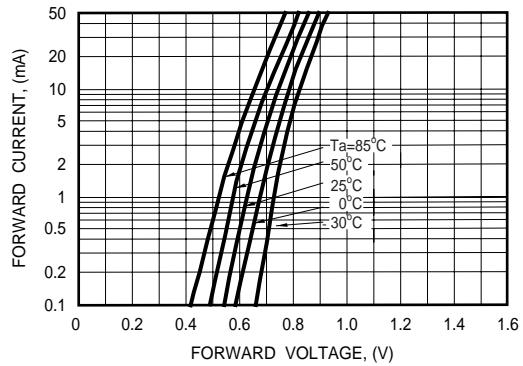


FIG. 3 - TYPICAL JUNCTION CAPACITANCE

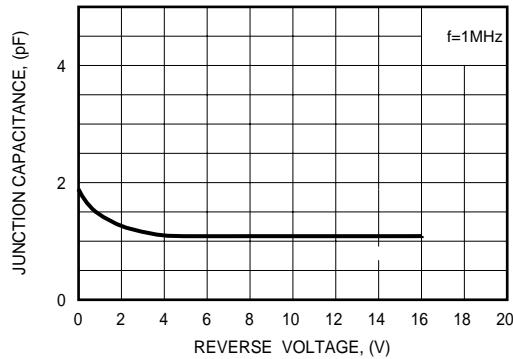


FIG. 4 - REVERSE CHARACTERISTICS

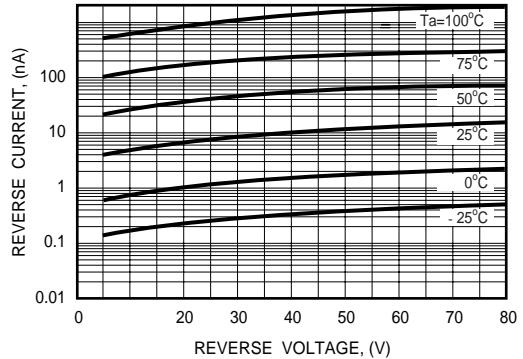


FIG. 5 - REVERSE RECOVERY TIME

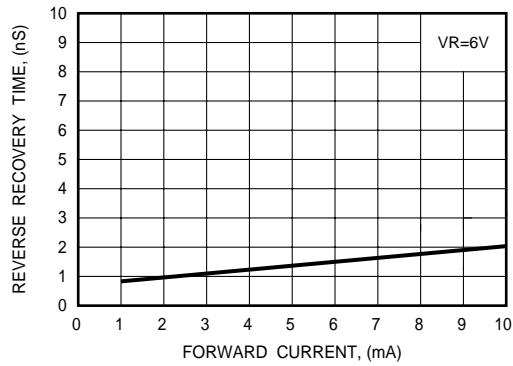


FIG. 6 - REVERSE RECOVERY TIME MEASUREMENT CIRCUIT

