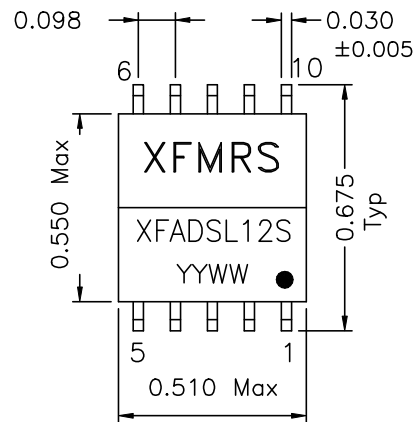
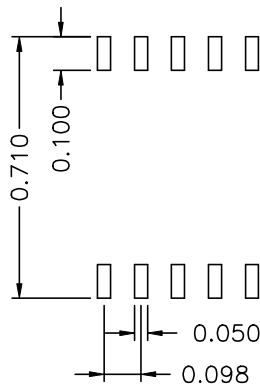


1. Dimensions:



CO-PLANARITY: 0.004 Max

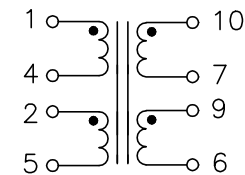


Suggested PCB Layout

Note: Designed to meet UL1950 Supplementary Insulation requirements for a working voltage up to 250V.

2.Schematic:

Line Side Chip Side



3.Electrical Specifications: @25°C

Isolation: 2000Vrms

PIN10-6 OCL: 430uH±10% @10KHz 100mV

(CONNECT PIN7-9)

URNS RATIO: (10-6):(1-5)=1CS:1CS ±2%

DCR: 1-4,2-5 0.45 Ohms Max

DCR: 10-7,9-6 0.35 Ohms Max

Leakage L: 1-5 10uH Max @ 100KHz, 0.1V

(Short Pins 2-4,7-9,10-6)

Long. Bal.: 25KHz-1.1MHz 40dB Min

THD: -80dB @100KHz

Insertion Loss: 0.5dB Max @772KHz

DOC. REV B/1

XFMRS Inc UNLESS OTHERWISE SPECIFIED TOLERANCES: .xxx ±0.010 Dimensions in INCH	Title: ADSL TRANSFORMER			
	P/N: XFADSL12S		REV. B	
	DWN.	廖玉坤	Jun-08-01	
	CHK.	李清儿	Jun-08-01	
SHEET 1 OF 1	APP.	Joe Huff	Jun-08-01	