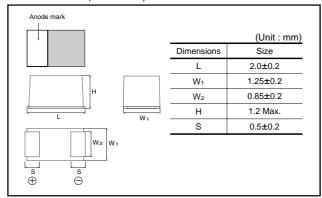
Chip tantalum capacitors TCT Series P Case

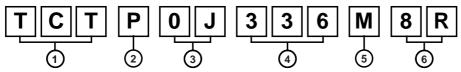
●Features (P)

- 1) Vital for all hybrid integrated circuits board application.
- 2) Wide capacitance range.
- 3) Screening by thermal shock.

●Dimensions (Unit: mm)



●Part No. Explanation



- 1 Series name
- 2 Case style
- (3) Rated voltage

Rated voltage (V)	25	4	6.3	10	16	20	25
CODE	0E	0G	0J	1A	1C	1D	1E

(4) Nominal capacitance

Nominal capacitance in pF in 3 digits: 2 significant figures followed by the figure representing the number of 0's.

(5) Capacitance tolerance

M: ±20%

6 Taping

8 : Tape width

R : Positive electrode on the side opposite to sprocket hole

Rated table

			Rat	ed voltag	ge (V)		
(μF)	2.5	4	6.3	10	16	20	25
	0E	0G	0J	1A	1C	1D	1E
2.2 (225)						ı	<i>New</i> P
3.3 (335)							*P
4.7 (475)					*P		
6.8 (685)							
10 (106)					Р		
15 (156)				Р			
22 (226)			Р	Р			
33 (336)		Р	Р	<i>New</i> P			
47 (476)		Р	Р				
68 (686)		Wew P	*P				
100 (107)	Wew P	Wew P					
150 (157)	*P	*P					
220 (227)	*P						

Remark) Case size codes (P) in the above show products line-up.

* Under development

New Product

Marking

The indications listed below should be given on the surface of a capacitor.

- (1) Polarity : The polarity should be shown by □ bar. (on the anode side)
 (2) Rated DC voltage : Due to the small size of P case, a voltage code is used as shown below.
- (3) Visual typical example

				,									
(1)	VC	lta	ge	code	(2)) ca	apa	cita	an	се	C	bc	е

Voltage Code	Rated DC Voltage (V)
е	2.5
g	4
j	6.3
Α	10
С	16
D	20
E	25

Capacitance Code	Nominal Capacitance (μF)					
A	1.0					
E	1.5					
J	2.2					
N	3.3					
S	4.7					
W	6.8					
а	10					
е	15					
j	22					
n	33					
s	47					
W	68					
ā	100					
ē	150					
j	220					

[P case] note 1) $\frac{j}{(1)}$ $\frac{n}{(2)}$



note 2) voltage code and capacitance code are variable with parts number

Characteristics

Itei	n					P	erfori	mano	се		Test conditions (based on JIS C 5101–1 and JIS C 5101-					
Operating Temp		-5	5°C	C to -	+125	°C					Volta	age r	reduction when t	temperature ex	ceeds +85°C	
Maximum operat temperature with derating	ing no voltage	+8	5°C													
Rated voltage (VDC)	2.5	4	6.3	10	16	20	25			at 85	5°C				
Category voltag	je (VDC)	1.6	2.5	5 4	6.3	10	13	16			at 125°C					
Surge voltage (VDC)	3.2	5.2	2 8	13	20	26	33			at 85°C					
DC Leakage cu	rrent	Sh	OWI	n in '	' Stai	nda	ard lis	st "			As p	er 4.	9 JIS C 5101-1 5.1 JIS C 5101- Rated voltage f			
Capacitance tol	Shall be satisfied allowance range. ±20%					nge.	As p Mea Mea	As per 4.7 JIS C 5101-1 As per 4.5.2 JIS C 5101-3 Measuring frequency : 120±12Hz Measuring voltage : 0.5Vrms +1.5 to 2V.DC Measuring circuit : DC Equivalent series circuit								
Tangent of loss (Df, tan δ)	angle				atisfie I list '		the v	oltaç	ge	n	As per 4.8 JIS C 5101-1 As per 4.5.3 JIS C 5101-3 Measuring frequency : 120±12Hz Measuring voltage : 0.5Vrms +1.5 to 2V.DC Measuring circuit : DC Equivalent series circuit					
Impedance					atisfie I list '		the v	oltag	ge	n	As per 4.10 JIS C 5101-1 As per 4.5.4 JIS C 5101-3 Measuring frequency: 100±10kHz Measuring voltage : 0.5Vrms or less Measuring circuit : DC Equivalent series circuit				eries circuit	
Resistance to Soldering heat Appearance			There should be no significant abnormality. The indications should be clear.						As p	er 4.	.14 JIS C 5101- .6 JIS C 5101-3	1				
	L.C.	Less than initial limit						Dip in the solder bath Solder temp : 260±5°C								
	ΔC / C	Wi	ithir	1 ±20)% o	f in	itial v	alue)		Duration : 5±0.5s Repetition : 1					
	Df (tan δ)	Less than 200% of initial limit						mit		After the specimens, leave it at room temperature for over 24h and then measure the sample.						
Temperature cycle	Appearance						no siç			abnormality.	As p	er 4.	.16 JIS C 5101- .10 JIS C 5101-3			
	L.C.	Le	ss t	than	2009	% c	of init	ial lir	mit				n : 5 cycles : steps 1 to 4) wi	ithout discontin	uation.	
	ΔC / C	Wi	ithir	า ±20)% o	f in	itial v	alue	,]``		Temp.	Time		
	Df (tan δ)	Le	ss t	than	2009	%0	f initi	al lin	nit		1	1	-55±3°C	30±3min.		
												2	Room temp.	3min. or less		
												3	125±2°C	30±3min.		
											٨٠٠٠	4	Room temp.	3min. or less	mporature for	
											After the specimens, leave it at room temperature for over 24h and then measure the sample.					
Moisture resistance	Appearance	There should be no significant abnormality. The indications should be clear.						As per 4.22 JIS C 5101-1 As per 4.12 JIS C 5101-3								
	L.C.	Le	ss t	than	2009	% c	of init	ial lir	mit				ving the sample			
	ΔC / C	Wi	ithir	1 ±20)% o	f in	itial \	alue)		condition that the temperature and humidity are 60±2°C and 90 to 95% RH, respectively, for 500±12h leave it at room					
	Less than 200% of initial limit						temperature for over 24h and then measure the sample.									

Tantalum capacitors

Iten	ņ	Performance	Test conditions (based on JIS C 5101–1 and JIS C 5101–3				
Temperature	Temp.	–55°C	As per 4.29 JIS C 5101-1				
Stability	ΔC / C	Within 0/–15% of initial value	As per 4.13 JIS C 5101-3				
	Df (tan δ)	Shall be satisfied the voltage on " Standard list "					
	L.C.	-					
	Temp.	+85°C					
	ΔC / C	Within +15/0% of initial value					
	Df (tan δ)	Shall be satisfied the voltage on " Standard list "					
	L.C.	5μA or 0.1CV whichever is greater					
	Temp.	+125°C					
	ΔC / C	Within +20/0% of initial value					
	Df (tan δ)	Shall be satisfied the voltage on " Standard list "					
	L.C.	6.3μA or 0.125CV whichever is greater					
Surge voltage	Appearance	There should be no significant abnormality.	As per 4.26JIS C 5101-1				
	L.C.	Less than 200% of initial limit	As per 4.14JIS C 5101-3 Apply the specified surge voltage every 5±0.5 min.				
	ΔC / C	Within ±20% of initial value	for 30±5 s. each time in the atmospheric condition of 85±2°C. Repeat this procedure 1,000 times.				
	Df (tan δ)	Less than 200% of initial limit	After the specimens, leave it at room temperature for over 24h and then measure the sample.				
Loading at High temperature	Appearance	There should be no significant abnormality.	As per 4.23 JIS C 5101-1 As per 4.15 JIS C 5101-3				
r ligir terriperature	L.C.	Less than 200% of initial limit	After applying the rated voltage for 1000+36/0 h without				
	ΔC / C	Within ±20% of initial value	discontinuation via the serial resistance of 3Ω or less at a temperature of 85±2°C, leave the sample at room				
	Df (tan δ)	Less than 200% of initial limit	temperature / humidity for over 24h and measure the value.				
Terminal	Capacitance	The measured value should be stable.	As per 4.35 JIS C 5101-1				
strength	Appearance	There should be no significant abnormality.	As per 4.9 JIS C 5101-3 A force is applied to the terminal until it bends to 1mm an by a prescribed tool maintain the condition for 5s. (See the figure below) (Unit:mm) F (Apply force) R230 thickness=1.6mm				

Tantalum capacitors

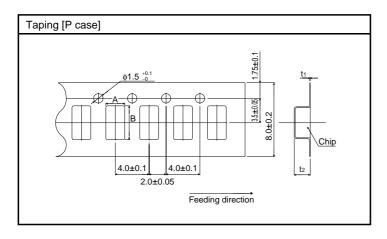
It	em	Performance	Test conditions (JIS C 5101-1 and JIS C 5101-3)			
Adhesiven	ess	The terminal should not come off.	As per 4.34 JIS C 5101-1 As per 4.8 JIS C 5101-3 Apply force of 5N in the two directions shown in the figure below for 10±1s after mounting the terminal on a circuit board.			
Dimensions		Refer to "External dimensions"	Measure using a caliper of JIS B 7507 Class 2 or higher grade.			
Resistance to solvents		The indication should be clear	As per 4.32 JIS C 5101-1 As per 4.18 JIS C 5101-3 Dip in the isopropyl alcohol for 30±5s, at room temperature.			
Solderability		3/4 or more surface area of the solder coated terminal dipped in the soldering bath should be covered with the new solder.	As per 4.15.2 JIS C 5101-1 As per 4.7 JIS C 5101-3 Dip speed=25±2.5mm / s Pre-treatment(accelerated aging): Leave the sample on the boiling distilled water for 1 h. Solder temp.: 245±5°C Duration : 3±0.5s Solder : M705 Flux : Rosin 25% IPA 75%			
Vibration Capacitano Appearano		Measure value should not fluctuate during the measurement.	As per 4.17 JIS C 5101-1 Frequency: 10 to 55 to 10Hz/min. Amplitude: 1.5mm Time: 2h each in X and Y directions Mounting: The terminal is soldered on a print circuit board			
		There should be no significant abnormality.				

• Standard products list, TCT series P case

Part No.	Rated voltage 85°C	Category voltage 125°C	Surge voltage 85°C	Cap. 120Hz	Tolerance	Leakage current 25°C		Df 120Hz (%)		Impedance 100kHz
	(V)	(V)	(V)	(μF)	(%)	1WV.60s (μA)	–55°C	25°C 85°C	125°C	(Ω)
TCT P 0E 107M8R	2.5	1.6	3.2	100	±20	12.5	60	30	40	4.0
TCT P 0G 107M8R	4	2.5	5.2	100	±20	20	60	30	40	4.0
TCT P 0G 336M8R	4	2.5	5.2	33	±20	1.3	30	20	30	4.0
TCT P 0G 476M8R	4	2.5	5.2	47	±20	1.9	30	20	30	4.0
TCT P 0G 686M8R	4	2.5	5.2	68	±20	13.6	60	30	40	4.0
TCT P 0J 226M8R	6.3	4	8	22	±20	1.4	30	20	30	5.0
TCT P 0J 336M8R	6.3	4	8	33	±20	2.1	30	20	30	4.0
TCT P 0J 476M8R	6.3	4	8	47	±20	14.8	60	30	40	4.0
TCT P 1A 156M8R	10	6.3	13	15	±20	1.5	30	20	30	6.0
TCT P 1A 226M8R	10	6.3	13	22	±20	2.2	30	20	30	5.0
TCT P 1A 336M8R	10	6.3	13	33	±20	16.5	60	30	40	4.0
TCT P 1C 106M8R	16	10	20	10	±20	1.6	30	20	30	6.0
TCT P 1E 225M8R	25	16	33	2.2	±20	0.55	30	20	30	8.0

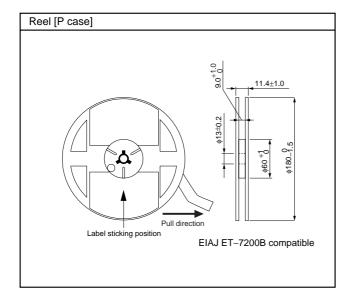
Packaging specifications

Case code	A±0.1	B±0.1	t1± 0.05	$t_2\!\pm\!0.1$
Р	1.55	2.3	0.25	1.5



Packaging style

Case code	Packaging	Packag	ging style	Symbol	Basic ordering units
P case	Taping	plastic taping	∮180mm Reel	R	3,000pcs



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