

SANYO Semiconductors DATA SHEET



BI-CMOS LSI For mobile phone 2.5V Constant-Voltage Power Supply IC

Overview

The LV59025M is a constant-voltage power supply IC for mobile phone. It is the best for the constant-voltage power supply of the battery machine used.

Features

- 2.5V output
- Output current of 1A obtainable (VIN1, VIN2 \geq 3.5V)
- Low current consumption
- MFP8 (200mil) package, ensuring easy mounting design
- With ON/OFF-switch

Specifications Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Maximum power supply	V _{IN} 1	V _{IN} 1 pin	6.2	V
	V _{IN} 2	V _{IN} 2 pin	6.2	V
Allowable power dissipation	Pd max	Mounted on a specified board.*	1.45	W
Operating Temperature	Topr		-30 to +85	°C
Storage Temperature	Tstg		-40 to +125	°C

* Specified board: 50mm \times 50mm \times 1.6mm, glass epoxy both sides

Recommended Operating Ranges at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
power supply	V _{IN} 1	V _{IN} 1 pin	2.6 to 6	V
	V _{IN} 2	V _{IN} 2 pin	2.6 to 6	V
Output current	IO		0 to 1	А

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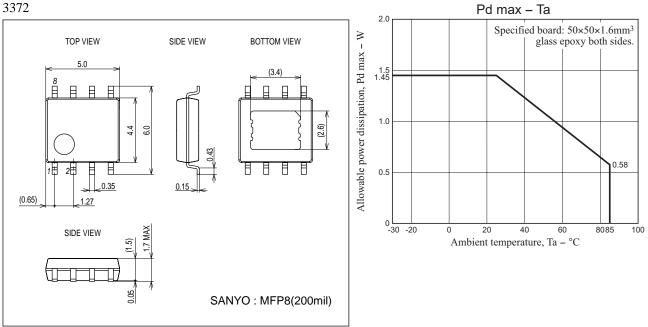
LV59025M

D		O an dition o			Ratings		
Parameter	Symbol Conditions			min	typ	max	Unit
Current drain	IVIN	CTL = 4.3V, I _O = 0mA			110	160	μA
Standby current	ISTBY	CTL = Low				1	μA
Output		•			·		
Output voltage	VO	I _O = 10mA		2.45	2.50	2.55	V
Dropout voltage	Vdrop1_1	I _O = 1A				1.0	V
	Vdrop1_2	I _O = 0.3A				0.4	V
Load Regulation	V _{LD}	$I_{O} = 5mA$ to 1A			10	50	mV
Line Regulation	V _{LN}	$V_{IN}1 = V_{IN}2 = 2.6V$ to 6V, $I_O = 10$ mA			10	50	mV
Voltage temperature coefficient	ΔVT	Ta = -30 to +85°C, I _O = 10mA	*		±100		ppm/°C
Ripple Rejection	V _{RL}	I _O = 10mA, VRpp=1V, f _{RR} = 1kHz	*		65		dB
Output Noise Voltage	VON	20Hz < f < 20kHz	*		150		μVrms
CTL pin		•			•		
High level voltage	VCTLH			1.5		5	V
Low level voltage	VCTLL			0		0.3	V
Input current	ICTL	V _{CTL} = 6V				8.5	μΑ

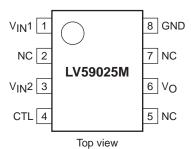
* Design guarantee

Package Dimensions

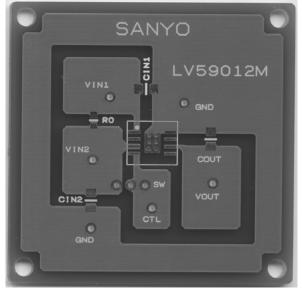
unit : mm (typ) 3372



Pin Assignment

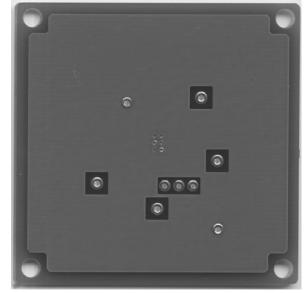


Specified Board (Top side)

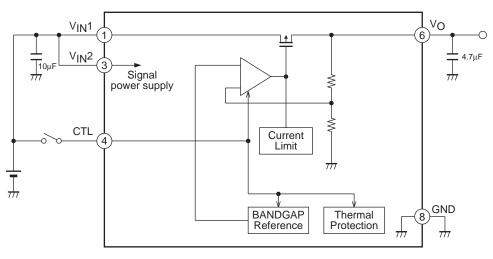


Note: The substrate is common with LV59012M.

Specified Board (Bottom side)

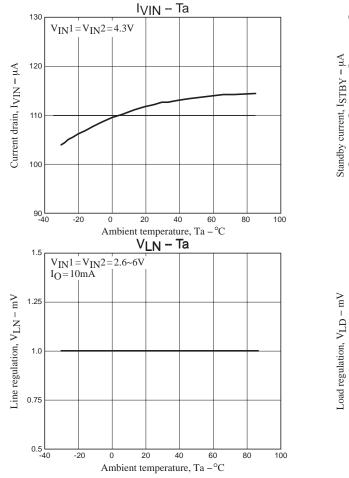


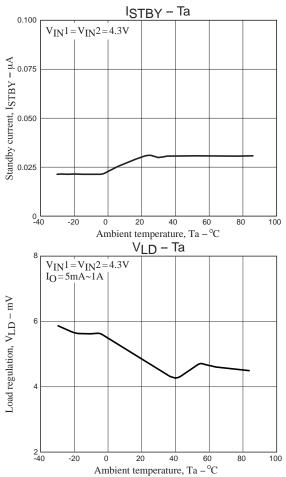
Block Diagram

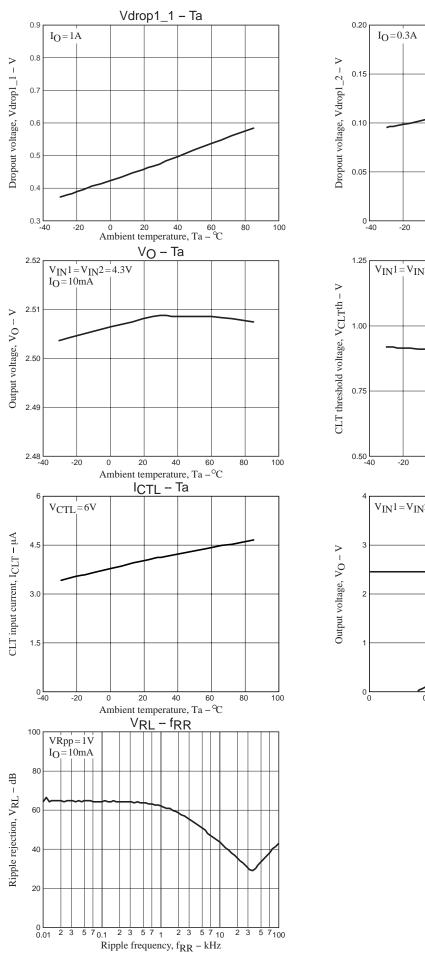


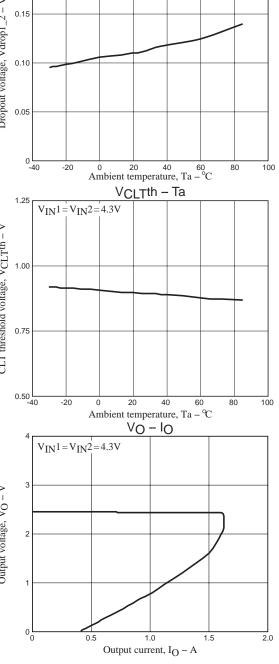
 $\label{eq:Pins 2,5,7 NC} Pins 2,5,7 \ NC \\ Connect and use V_{IN1} and V_{IN2}.$

Pin Function					
Pin No.	Pin name	Function	Equivalent circuit		
1	V _{IN} 1	Power system supply pin.			
6	Vo	Output voltage pin.			
2	NC	No contact.			
3	V _{IN} 2	Signal system power supply pin.	VIN ² (3)		
4	CTL	ON/OFF control pin.	$CTL (4) \xrightarrow{10k\Omega} F$ $STL (4) \xrightarrow{10k\Omega} F$ $STL (4) \xrightarrow{10k\Omega} F$		
5	NC	No contact.			
7	NC	No contact.			
8	GND	Ground pin.			

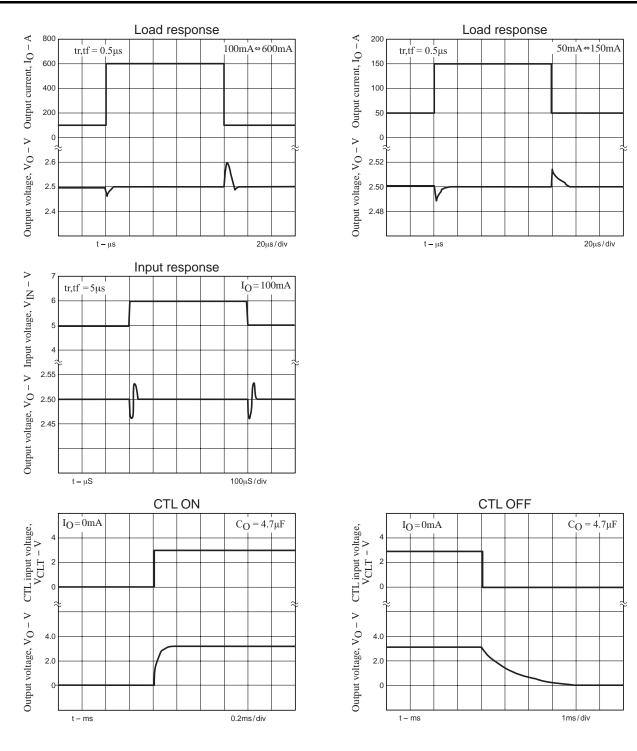








Vdrop1_2 - Ta



Radiation Pad

- Radiation pad is high impedance and connected with a substrate of IC.
- Use radiation pad by GND or opening.

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