



CHENMKO ENTERPRISE CO.,LTD

Lead free devices

**SURFACE MOUNT
PNP Digital Silicon Transistor**

VOLTAGE 50 Volts CURRENT 30 mAmpere

CHDTA144WMPT

APPLICATION

- * Switching circuit, Inverter, Interface circuit, Driver circuit.

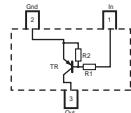
FEATURE

- * Small surface mounting type. (SOT-723)
- * High current gain.
- * Suitable for high packing density.
- * Low collector-emitter saturation.
- * High saturation current capability.
- * Internal isolated PNP transistors in one package.
- * Built in bias resistor($R_1=47\text{k}\Omega$, Typ.)

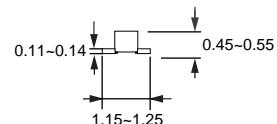
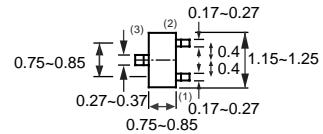
CONSTRUCTION

- * One PNP transistors and bias of thin-film resistors in one package.

CIRCUIT



SOT-723



Dimensions in millimeters

SOT-723

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CC}	Supply voltage		–	-50	V
V _{IN}	Input voltage		-40	+10	V
I _O	DC Output current		–	-30	mA
I _{C(MAX.)}			–	-100	
P _{TOT}	Total power dissipation	T _{amb} ≤ 25 °C, Note 1	–	150	mW
T _{TG}	Storage temperature		-55	+150	°C
T _J	Junction temperature		–	150	°C
R _{θJS}	Thermal resistance	junction - soldering point	–	140	°C/W

Note

- Transistor mounted on an FR4 printed-circuit board.

RATING CHARACTERISTIC (CHDTA144WMPT)

CHARACTERISTICS

$T_{amb} = 25^{\circ}\text{C}$ unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
$V_{I(off)}$	Input off voltage	$I_o=-100\mu\text{A}; V_{cc}=-5.0\text{V}$	-0.8	—	—	V
$V_{I(on)}$	Input on voltage	$I_o=-2\text{mA}; V_o=-0.3\text{V}$	—	—	-4.0	V
$V_{O(on)}$	Output voltage	$I_o=-10\text{mA}; I_i=-0.5\text{mA}$	—	-0.1	-0.3	V
I_i	Input current	$V_i=-5\text{V}$	—	—	-0.16	mA
$I_{C(off)}$	Output current	$V_i=0\text{V}; V_{cc}=-50\text{V}$	—	—	-0.5	μA
h_{FE}	DC current gain	$I_o=-5\text{mA}; V_o=-5.0\text{V}$	56	—	—	—
R_1	Input resistor	—	32.9	47	61.1	$\text{k}\Omega$
R_2/R_1	Resistor ratio	—	0.37	0.47	0.57	—
f_T	Transition frequency	$I_E=5\text{mA}, V_{CE}=-10.0\text{V}$ $f=100\text{MHz}$	—	250	—	MHz

Note

1. Pulse test: $t_p \leq 300\mu\text{s}; \delta \leq 0.02$.