

DESCRIPTION

The SSF4606 uses advanced trench technology MOSFET to provide excellent $R_{\text{DS(ON)}}$ and low gate charge. The complementary MOSFET may be used in power inverters, and other applications.

GENERAL FEATURES

N-Channel

$$\begin{split} V_{DS} &= 30 \text{V,I}_D = 6.9 \text{A} \\ R_{DS(ON)} &< 42 \text{m} \Omega \text{ @ V}_{GS} = 4.5 \text{V} \\ R_{DS(ON)} &< 28 \text{m} \Omega \text{ @ V}_{GS} = 10 \text{V} \end{split}$$

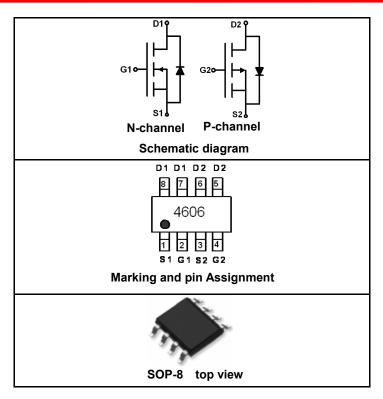
●P-Channel

 $V_{DS} = -30V, I_{D} = -6A$

 $R_{DS(ON)} < 58 m\Omega$ @ V_{GS} =-4.5V

 $R_{DS(ON)} < 35m\Omega$ @ V_{GS} =-10V

- High Power and current handing capability
- Lead free product is acquired
- Surface Mount Package



PACKAGE MARKING AND ORDERING INFORMATION

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
4606	SSF4606	SOP-8	Ø330mm	12mm	3000 units

ABSOLUTE MAXIMUM RATINGS(TA=25 ℃ unless otherwise noted)

Parameter Drain-Source Voltage		Symbol	N-Channel	P-Channel	Unit	
		V _{DS}	30	-30	V	
Gate-Source Voltage	V_{GS}	±20	±20	V		
Outing a Paris Out of	T _A =25℃	- I _D	6.9	-6	А	
Continuous Drain Current	T _A =70°C		6.0	-5.0		
Pulsed Drain Current (Note 1)		I _{DM}	30	-30	Α	
Mayimum Dayyar Dissination	T _A =25℃	Б	2.0	2.0	10/	
Maximum Power Dissipation	T _A =70°C	P _D	1.44	1.44	W	
Operating Junction and Storage Temperature Range		T_{J} , T_{STG}	-55 To 150	-55 To 150	$^{\circ}$	

THERMAL CHARACTERISTICS

Thermal Resistance.Junction-to-Ambient (Note2)	P	N-Ch	62.5	°C/W
Thermal Resistance, sunction-to-Ambient (Note2)	$\kappa_{ heta ext{JA}}$	P-Ch	62.5	CIVV

ELECTRICAL CHARACTERISTICS (TA=25°C unless otherwise noted)

Parameter	Symbol	Condition		Min	Тур	Max	Unit
OFF CHARACTERISTICS			_	•			



		V _{GS} =0V I _D =250μA	N-Ch	30				
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =-250μA	P-Ch	-30			V	
		V _{DS} =24V,V _{GS} =0V	N-Ch	-30		1		
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-24V,V _{GS} =0V	P-Ch			-1	μΑ	
		VDS24V,VGS-0V	N-Ch			±100		
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V,V _{DS} =0V	P-Ch			±100	nA	
ON CHARACTERISTICS (Note 3)			P-CII			±100		
Cit Cin au to Latino Mos (Mosto C)		$V_{DS}=V_{GS},I_{D}=250\mu A$	N-Ch	1	1.9	3		
Gate Threshold Voltage	$V_{GS(th)}$	V _{DS} =V _{GS} ,I _D =-250μA	P-Ch	-1.2	-2	-2.4	V	
		V _{GS} =10V, I _D =6.9A	N-Ch		22.5	28		
		V _{GS} =-10V, I _D =-6.0A	P-Ch		28	35	-	
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =4.5V, I _D =5A	N-Ch		34.5	42	mΩ	
		V _{GS} =-4.5V, I _D =-5A	P-Ch		44	58	-	
		V _{DS} =5V,I _D =6.9A	N-Ch	10	15.4			
Forward Transconductance	9 FS	V _{DS} =-5V,I _D =-6A	P-Ch		13		S	
DYNAMIC PARAMETERS								
Input Capacitance			N-Ch	680				
	C _{lss}	N-Ch	P-Ch		900		pF	
Output Capacitance		VGS=0V, VDS=15V, f=1MHz	N-Ch		100			
	C _{oss}	P-Ch	P-Ch		200			
	C _{rss}	VGS=0V, VDS=-15V, f=1MHz	N-Ch		77			
Reverse Transfer Capacitance			P-Ch		120			
SWITCHING CHARACTERISTICS (No	ote 4)	1						
			N-Ch		4.6			
Turn-on Delay Time	$t_{d(on)}$	N-Ch	P-Ch		7.7		nS	
- D		V_{DD} =15V, R _L =2.2Ω V_{GEN} =10V,R _{GEN} =3Ω	N-Ch		4.1		_	
Turn-on Rise Time	t _r		P-Ch		5.7		nS	
T 0%D T		P-Ch	N-Ch		20.6		- nS	
Turn-Off Delay Time	$t_{d(off)}$	V_{DD} =-15V, R_L =2.7 Ω	P-Ch		20			
		V_{GEN} =-10V, R_{GEN} =3 Ω	N-Ch		5.2		nS	
Turn-Off Fall Time	t _f		P-Ch		9.5			
			N-Ch		7		nC	
Total Gate Charge	Q_g	N-Ch	P-Ch		9.6			
		V_{DS} =15V, I_{D} =6.9A, V_{GS} =4.5V	N-Ch		1.8			
Gate-Source Charge	Q_gs		P-Ch		2.5		nC	
		P-Ch V _{DS} =-15V,I _D =-6A,						
Gate-Drain Charge	$Q_{\sf gd}$	V _{GS} =-4.5V	N-Ch		3.2		nC	
	- '54		P-Ch		4.5			



DRAIN-SOURCE DIODE CHARACTERISTICS								
Diode Forward Voltage (Note 3)	\/	V _{GS} =0V,I _S =1A	N-Ch		0.76	1	٧	
	V _{SD}	V _{GS} =0V,I _S =-1A	P-Ch		-0.77	-1	٧	

NOTES:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2. Surface Mounted on FR4 Board, t ≤ 10 sec.
- 3. Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2%.
- 4. Guaranteed by design, not subject to production testing.

N-Channel THERMAL CHARACTERISTICS

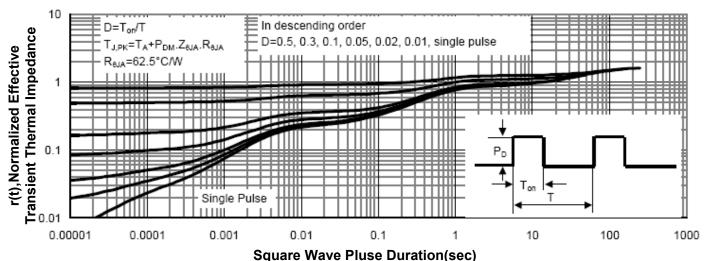


Figure 1: Normalized Maximum Transient Thermal Impedance

P-Channel THERMAL CHARACTERISTICS

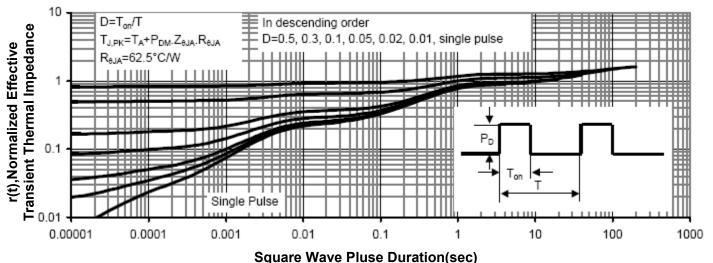
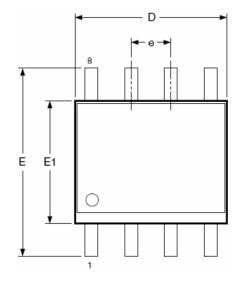


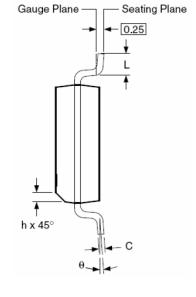
Figure 2: Normalized Maximum Transient Thermal Impedance

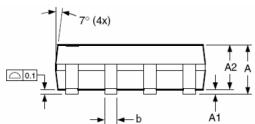


SOP-8 PACKAGE INFORMATION

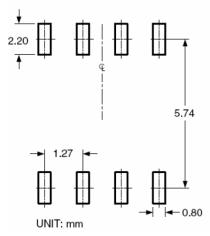
Dimensions in Millimeters (UNIT:mm)







RECOMMENDED LAND PATTERN



Dimensions in millimeters							
Symbols	Min.	Nom.	Max.				
Α	1.35	1.65	1.75				
A1	0.10	_	0.25				
A2	1.25	1.50	1.65				
b	0.31	_	0.51				
С	0.17	_	0.25				
D	4.80	4.90	5.00				
E1	3.80	3.90	4.00				
е	-	1.27 BSC					
E	5.80	6.00	6.20				
h	0.25	_	0.50				
L	0.40		1.27				
θ	0°	_	8°				

Dimensions in inches								
Symbols	Min.	Nom.	Max.					
Α	0.053	0.065	0.069					
A1	0.004	_	0.010					
A2	0.049	0.059	0.065					
b	0.012	_	0.020					
С	0.007	_	0.010					
D	0.189	0.193	0.197					
E1	0.150	0.154	0.157					
Ф	0	.050 BS	С					
Е	0.228	0.236	0.244					
h	0.010	_	0.020					
L	0.016	_	0.050					
θ	0 °	_	8°					
θ	0 °	_	8°					

NOTES:

- 1. All dimensions are in millimeters.
- Dimensions are inclusive of plating
 Package body sizes exclude mold flash and gate burrs. Mold flash at the non-lead sides should be less than 6 mils.
- 4. Dimension L is measured in gauge plane.
- 5. Controlling dimension is millimeter, converted inch dimensions are not necessarily exact.



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