

# Single N-channel MOSFET with schottky diode

ELM16706EA-S

## General description

ELM16706EA-S uses advanced trench technology to provide excellent  $R_{ds(on)}$  and low gate charge.

## Features

- $V_{ds}=30V$
- $I_d=3.3A$  ( $V_{gs}=10V$ )
- $R_{ds(on)} < 65m\Omega$  ( $V_{gs}=10V$ )
- $R_{ds(on)} < 75m\Omega$  ( $V_{gs}=4.5V$ )
- $R_{ds(on)} < 160m\Omega$  ( $V_{gs}=2.5V$ )
- Schottky diode
- $V_{ds}(V)=20V$
- $I_f=1A$
- $V_f < 0.5V@0.5A$

## Maximum absolute ratings

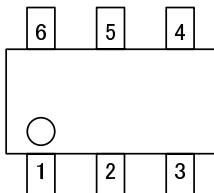
Parameter	Symbol	MOSFET	Schottky	Unit	Note
Drain-source voltage	$V_{ds}$	30		V	
Gate-source voltage	$V_{gs}$	$\pm 12$		V	
Continuous drain current	$I_d$	$T_a=25^\circ C$	3.3	A	1
		$T_a=70^\circ C$	2.6		
Pulsed drain current	$I_{dm}$	10		A	2
Schottky reverse voltage	$V_{ka}$		20	V	
Continuous forward current	$I_f$	$T_a=25^\circ C$	2	A	1
		$T_a=70^\circ C$	1		
Pulsed forward current	$I_{fm}$		10	A	2
Power dissipation	$P_d$	$T_a=25^\circ C$	1.15	W	
		$T_a=70^\circ C$	0.70		
Junction and storage temperature range	$T_j, T_{stg}$	-55 to 150	-55 to 150	$^\circ C$	

## Thermal characteristics

Parameter (MOSFET)	Symbol	Typ.	Max.	Unit	Note
Maximum junction-to-ambient	$R\theta_{ja}$	80.3	110.0	$^\circ C/W$	1
Maximum junction-to-ambient		Steady-state	117.0	150.0	
Maximum junction-to-lead	$R\theta_{jl}$	43.0	80.0	$^\circ C/W$	3
Parameter (Schottky)	Symbol	Typ.	Max.	Unit	Note
Maximum junction-to-ambient	$R\theta_{ja}$	109.4	135.0	$^\circ C/W$	1
Maximum junction-to-ambient		Steady-state	136.5	175.0	
Maximum junction-to-lead	$R\theta_{jl}$	58.5	80.0	$^\circ C/W$	3

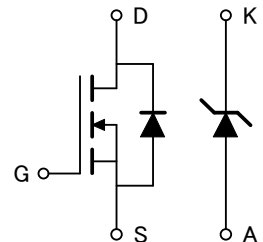
## Pin configuration

SOT-26 (TOP VIEW)



Pin No.	Pin name
1	ANODE
2	SOURCE
3	GATE
4	DRAIN
5	No Connection
6	CATHODE

## Circuit



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### Electrical characteristics

T<sub>a</sub>=25°C

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
<b>STATIC PARAMETERS</b>						
Drain-source breakdown voltage	BV <sub>dss</sub>	I <sub>d</sub> =250 μA, V <sub>gs</sub> =0V	30			V
Zero gate voltage drain current	I <sub>dss</sub>	V <sub>d</sub> =24V			1	μA
		V <sub>gs</sub> =0V			5	
Gate-body leakage current	I <sub>gss</sub>	V <sub>d</sub> =0V, V <sub>gs</sub> =±12V			100	nA
Gate threshold voltage	V <sub>gs(th)</sub>	V <sub>d</sub> =V <sub>gs</sub> , I <sub>d</sub> =250 μA	1.0	1.4	1.8	V
On state drain current	I <sub>d(on)</sub>	V <sub>gs</sub> =4.5V, V <sub>d</sub> =5V	10			A
Static drain-source on-resistance	R <sub>ds(on)</sub>	V <sub>gs</sub> =10V		44	65	mΩ
		I <sub>d</sub> =3.3A	T <sub>j</sub> =125°C	64	90	
		V <sub>gs</sub> =4.5V, I <sub>d</sub> =3.0A		53	75	mΩ
		V <sub>gs</sub> =2.5V, I <sub>d</sub> =1A		106	160	mΩ
Forward transconductance	G <sub>fs</sub>	V <sub>d</sub> =5V, I <sub>d</sub> =3.3A		11.7		S
Diode forward voltage	V <sub>sd</sub>	I <sub>s</sub> =1A, V <sub>gs</sub> =0V		0.81	1.00	V
Max. body-diode continuous current	I <sub>s</sub>				2.5	A
<b>DYNAMIC PARAMETERS</b>						
Input capacitance	C <sub>iss</sub>			226	270	pF
Output capacitance	C <sub>oss</sub>	V <sub>gs</sub> =0V, V <sub>d</sub> =15V, f=1MHz		39		pF
Reverse transfer capacitance	C <sub>rss</sub>			29		pF
Gate resistance	R <sub>g</sub>	V <sub>gs</sub> =0V, V <sub>d</sub> =0V, f=1MHz		1.4	1.7	Ω
<b>SWITCHING PARAMETERS</b>						
Total gate charge	Q <sub>g</sub>			3.00	3.60	nC
Gate-source charge	Q <sub>gs</sub>	V <sub>gs</sub> =4.5V, V <sub>d</sub> =15V, I <sub>d</sub> =3.3A		1.40		nC
Gate-drain charge	Q <sub>gd</sub>			0.55		nC
Turn-on delay time	t <sub>d(on)</sub>			2.6		ns
Turn-on rise time	t <sub>r</sub>	V <sub>gs</sub> =10V, V <sub>d</sub> =15V		3.2		ns
Turn-off delay time	t <sub>d(off)</sub>	R <sub>l</sub> =4.7 Ω, R <sub>gen</sub> =6 Ω		14.5		ns
Turn-off fall time	t <sub>f</sub>			2.1		ns
Body diode reverse recovery time	t <sub>rr</sub>	I <sub>f</sub> =3.3A, dI/dt=100A/μs		10.2	13.0	ns
Body diode reverse recovery charge	Q <sub>rr</sub>	I <sub>f</sub> =3.3A, dI/dt=100A/μs		3.8		nC
<b>SCHOTTKY PARAMETERS</b>						
Forward voltage drop	V <sub>f</sub>	I <sub>f</sub> =0.5A		0.39	0.50	V
Max. reverse leakage current	I <sub>rm</sub>	V <sub>r</sub> =16V			0.10	mA
		V <sub>r</sub> =16V, T <sub>j</sub> =125°C			20.00	
Junction capacitance	C <sub>t</sub>	V <sub>r</sub> =10V		34		pF
Schottky reverse recovery time	t <sub>rr</sub>	I <sub>f</sub> =1A, dI/dt=100A/μs		5.2	10.0	ns
Schottky reverse recovery charge	Q <sub>rr</sub>	I <sub>f</sub> =1A, dI/dt=100A/μs		0.8		nC

#### NOTE :

- The value of R<sub>θja</sub> is measured with the device mounted on 1in<sup>2</sup> FR-4 board of 2oz. Copper, in still air environment with T<sub>a</sub>=25°C. The value in any given applications depends on the user's specific board design, The current rating is based on the t ≤ 10s thermal resistance rating.
- Repetitive rating, pulse width limited by junction temperature.
- The R<sub>θja</sub> is the sum of the thermal impedance from junction to lead R<sub>θjl</sub> and lead to ambient.
- The static characteristics in Figures 1 to 6 are obtained using 80μs pulses, duty cycle 0.5%max.
- These tests are performed with the device mounted on 1in<sup>2</sup> FR-4 board with 2oz. Copper, in a still air environment with T<sub>a</sub>=25°C. The SOA curve provides a single pulse rating.

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## Typical electrical and thermal characteristics

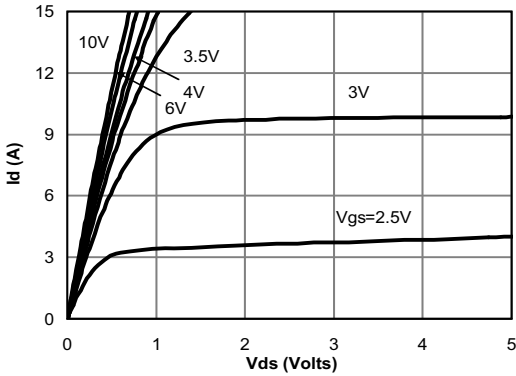


Figure 1: On-Region Characteristics

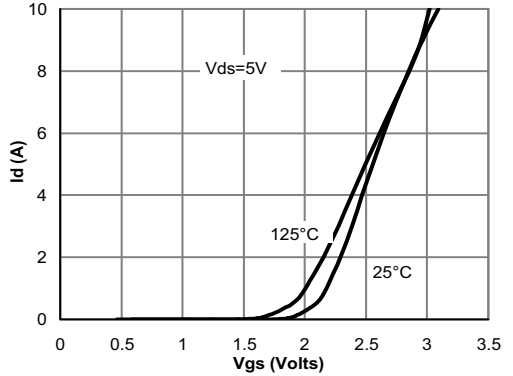


Figure 2: Transfer Characteristics

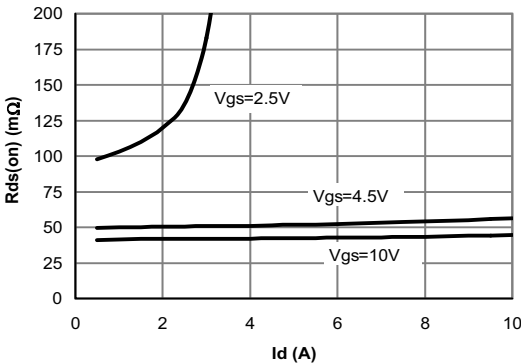


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

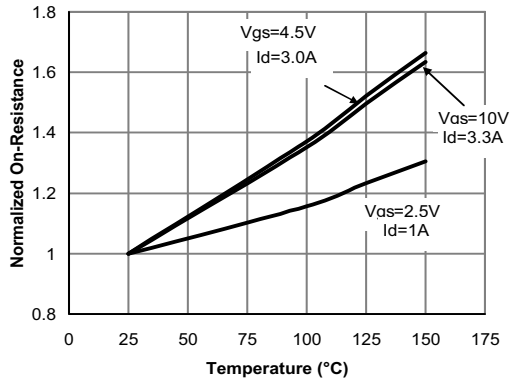


Figure 4: On-Resistance vs. Junction Temperature

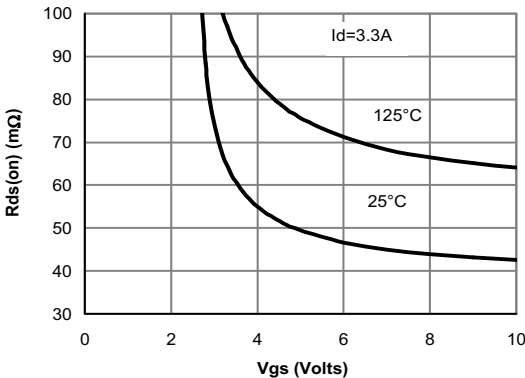


Figure 5: On-Resistance vs. Gate-Source Voltage

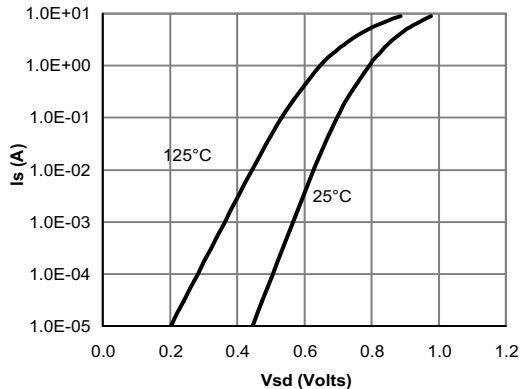


Figure 6: Body-Diode Characteristics

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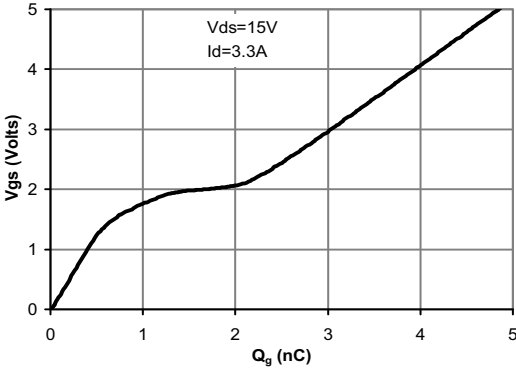


Figure 7: Gate-Charge Characteristics

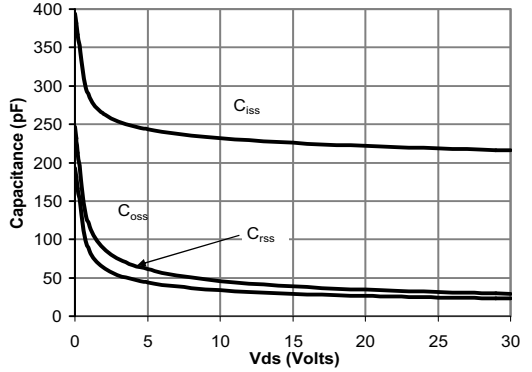


Figure 8: Capacitance Characteristics

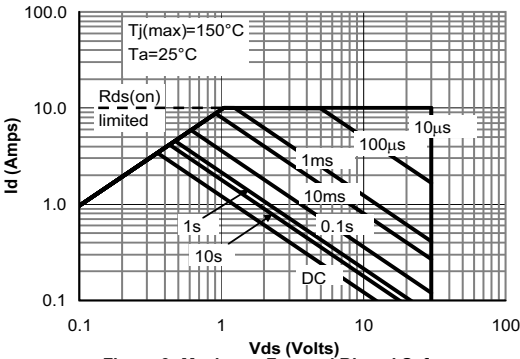


Figure 9: Maximum Forward Biased Safe Operating Area (Note E)

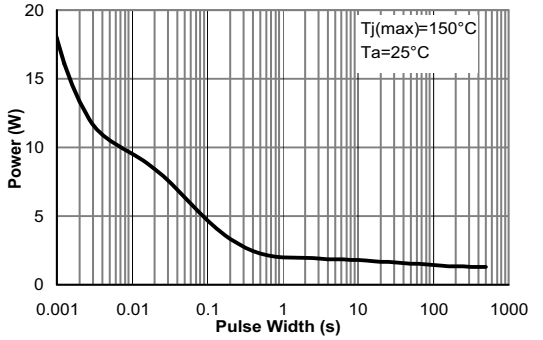


Figure 10: Single Pulse Power Rating Junction-to-Ambient (Note E)

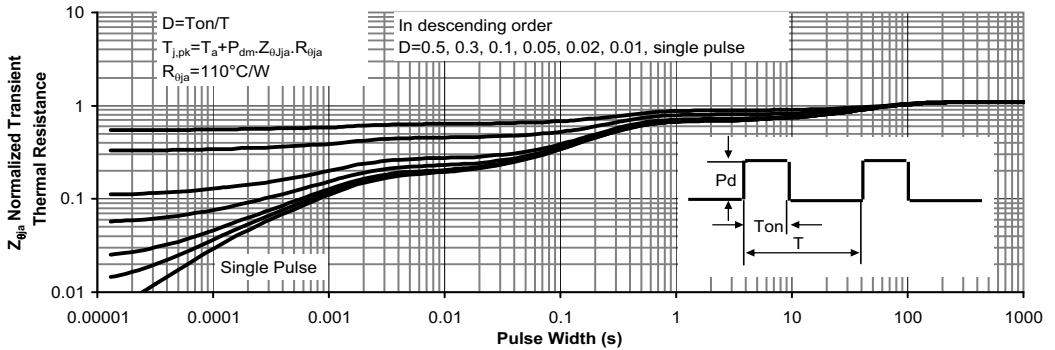


Figure 11: Normalized Maximum Transient Thermal Impedance

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## Typical electrical and thermal characteristics (Schottky)

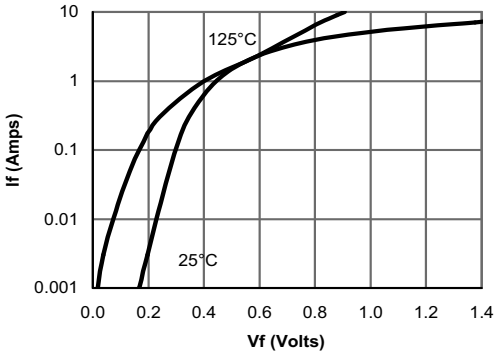


Figure 12: Schottky Forward Characteristics

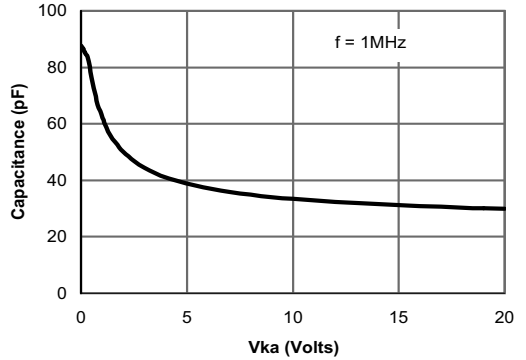


Figure 13: Schottky Capacitance Characteristics

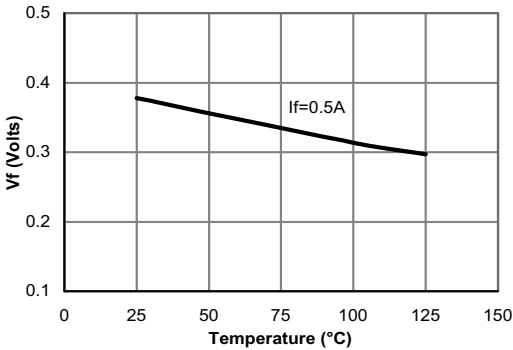


Figure 14: Schottky Forward Drop vs. Junction Temperature

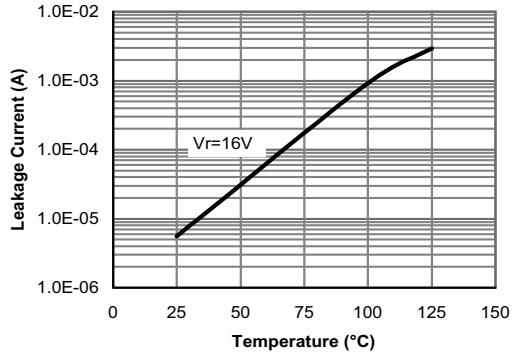


Figure 15: Schottky Leakage current vs. Junction Temperature

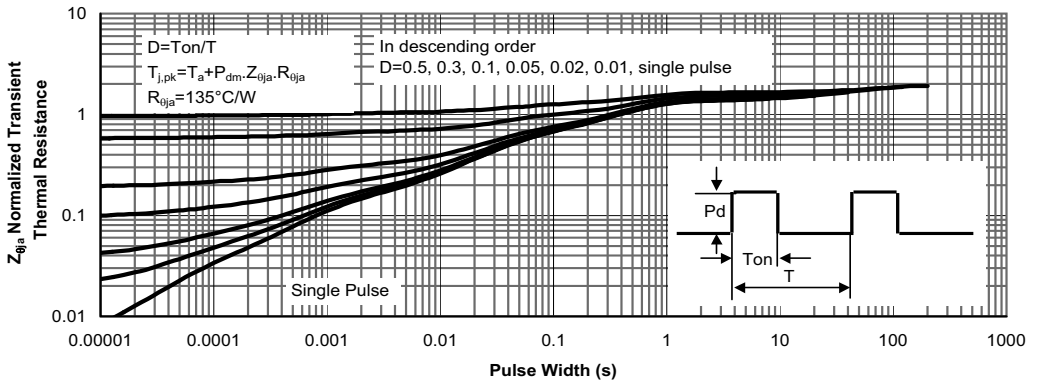


Figure 15: Schottky Normalized Maximum Transient Thermal Impedance