FAST ETHERNET 100BASE-LX10 SFP SINGLE MODE TRANSCEIVERS WITH DIGITAL DIAGNOSTICS

TRPAFELX

Product Description

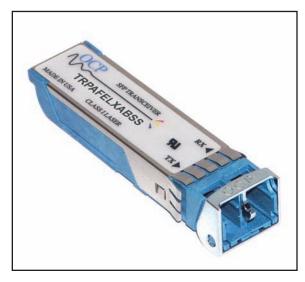
The TRPAFELX SFP series of fiber optic transceivers provide a quick and reliable interface for 100BASE-LX10 Fast Ethernet single mode applications. The transceivers are compliant with IEEE 802.3ah/D3.3 standard. The diagnostic functions, alarm and warning features as described in the Multi-Source Agreement (MSA) document, SFF-8472 (Rev. 9.4), are provided via an I²C serial interface. All transceiver modules satisfy Class I Laser Safety requirements in accordance with the U.S. FDA/CDRH and international IEC-60825 standards.

The transceivers connect to standard 20-pad SFP connectors for hot plug capability. This allows the system designer to make configuration changes or maintenance by simply plugging in different types of transceivers without removing the power supply from the host system.

The transceivers have bail-type latches, which offer an easy and convenient way to release the modules. The latch is compliant with the SFP MSA.

The transmitter incorporates a highly reliable 1310nm InGaAsP laser and an integrated driver circuit. The receiver features a transimpedance amplifier IC optimized for high sensitivity and wide dynamic range. The transmitter and receiver DATA interfaces are AC-coupled internally. LV-TTL Transmitter Disable control input and Loss of Signal (LOS) output interfaces are also provided.

The transceivers operate from a single +3.3V power supply over three operating case temperature ranges of -5° C to $+70^{\circ}$ C ("B" option), -5° C to $+85^{\circ}$ C ("E" option), or -40° C to $+85^{\circ}$ C ("A" option). The housing is made of plastic and metal for EMI immunity.



Features

- ☑ Compliant with IEEE 802.3ah/D3.3 (100BASE-LX10)
- ☑ Compatible with SFP MSA
- ☑ Digital Diagnostics through Serial Interface
- ☑ Internal Calibration for Digital Diagnostics
- ☑ 1310nm Fabry Perot Laser Transmitter
- ☑ Distances up to 10km
- ☑ Eye Safe (Class I Laser Safety)
- ☑ Duplex LC Optical Interface
- ☑ Hot-pluggable
- ☑ TX Fault & Loss of Signal Outputs
- ☑ TX Disable Input
- ☑ Single +3.3V Power Supply

Absolute Maximum Ratings

Par	ameter	Symbol	Minimum	Maximum	Units
Storage Temperature		T_{st}	- 40	+ 85	°C
	"B" option		- 5	+ 70	
Operating Case Temperature ¹	"E" option	T_{op}	- 5	+ 85	°C
remperature	"A" option	,	- 40	+ 85	
Supply Voltage		V_{cc}	0	+ 4.5	V
Input Voltage		V_{in}	0	V_{cc}	V
¹ Measured on top side of SF	P module at the front center ver	nt hole of the cage.			





Transmitter Performance Characteristics (Over Operating Case Temperature, V_{cc} = 3.13 to 3.47V)

All parameters guaranteed only at typical data rate

Parameter	Symbol	Minimum	Typical	Maximum	Units
Operating Data Rate ¹	В	-	125	-	Mb/s
Optical Output Power ²	$P_{\scriptscriptstyle O}$	- 15.0	-	- 8.0	dBm
Center Wavelength	λ_c	1260	-	1360	nm
Spectral Width (RMS)	$\Delta \lambda_{\scriptscriptstyle RMS}$	-	-	7.7	nm
Optical Modulation Amplitude	OMA	- 14.8	-	-	dBm
Extinction Ratio	P_{hi}/P_{lo}	5	-	-	dB
Optical Output Power of OFF Transmitter	P_{OFF}	-	-	- 45	dBm
Optical Return Loss Tolerance	ORLT	-	-	12	dB
Transmitter Dispersion Penalty	-	-	-	4.5	dB
Optical Output Eye		Comp	pliant with IEEE 802.3	ah/D3.3	

 $^{^{1}}$ Data rate ranges from 50Mb/s to 200Mb/s. However, some degradation may be incurred in overall performance.

Receiver Performance Characteristics (Over Operating Case Temperature, $V_{CC} = 3.13$ to 3.47V)

All parameters guaranteed only at typical data rate

	Parameter	Symbol	Minimum	Typical	Maximum	Units
Operating Data Rate	e ¹	В	-	125	-	Mb/s
Receiver Sensitivity	(10 ⁻¹² BER) ²	P_{min}	- 25.0	- 34.0	-	dBm
Receiver Sensitivity	as OMA	$P_{OMA, min}$	-24.8	-	-	dBm
Maximum Input Op	tical Power (10 ⁻¹² BER) ²	P_{max}	- 8.0	- 5.0	-	dBm
1 OC Thh -1-1-	Increasing Light Input	P_{los+}	-	-	- 25.0	-ID
LOS Thresholds	Decreasing Light Input	P_{los}	- 45.0	-	-	dBm
LOS Hysteresis ³		-	0.5	-	-	dB
Wavelength of Ope	ration	λ	1100	-	1600	nm
Receiver Reflectanc	e	-	-	-	- 12	db

¹ Data rate ranges from 50Mb/s to 200Mb/s. However, some degradation may be incurred in overall performance.

Laser Safety: All transceivers are Class I Laser products per FDA/CDRH and IEC-60825 standards. They must be operated under specified operating conditions.





Oplink Communications, Inc. DATE OF MANUFACTURE:

This product complies with 21 CFR 1040.10 and 1040.11

Meets Class I Laser Safety Requirements

²Measured average power coupled into single mode fiber. The minimum power specified is at Beginning-of-Life.

² Specified in average optical input power and when measured at 1310nm wavelength and 125Mb/s with optical frame based on test pattern specified in IEEE 802.3ah. The optical source Extinction Ratio is set at optimum value duing the sensitivity test.

 $^{^{3}}$ When measured at 1310nm wavelength and 125Mb/s with 2^{7} -1 PRBS.

Transmitter Electrical Interface (Over Operating Case Temperature, V_{CC} = 3.13 to 3.47V)

Parameter	Symbol	Minimum	Typical	Maximum	Units
Input Voltage Swing (TD+ & TD-) ¹	$V_{_{PP\text{-}DIF}}$	0.25	-	2.4	V
Input HIGH Voltage (TX Disable) ²	$V_{_{I\!H}}$	2.0	-	V_{cc}	V
Input LOW Voltage (TX Disable) ²	$V_{_{I\!L}}$	0	-	0.8	V
Output HIGH Voltage (TX_FAULT) ³	V_{OH}	2.0	-	V_{CC} + 0.3	V
Output LOW Voltage (TX_FAULT) ³	$V_{\scriptscriptstyle OL}$	0	-	0.8	V

¹ Differential peak-to-peak voltage.

Receiver Electrical Interface (Over Operating Case Temperature, V_{cc} = 3.13 to 3.47V)

Parameter	Symbol	Minimum	Typical	Maximum	Units
Output Voltage Swing (RD+ & RD-) ¹	$V_{_{PP\text{-}DIF}}$	0.6	-	2.0	V
Output HIGH Voltage (LOS) ²	$V_{_{OH}}$	2.0	-	V _{CC} + 0.3	V
Output LOW Voltage (LOS) ²	$V_{\scriptscriptstyle OL}$	0	-	0.5	V

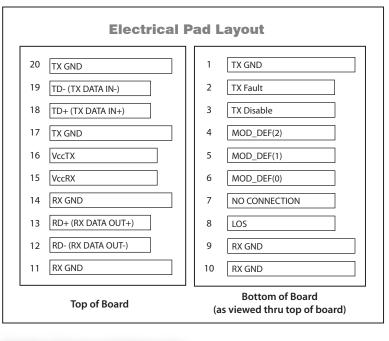
 $^{^{\}scriptscriptstyle 1}$ Differential peak-to-peak voltage across external 100 $\!\Omega$ load.

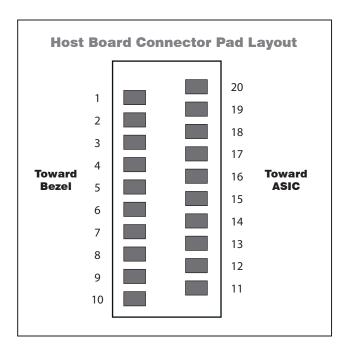
Electrical Power Supply Characteristics (Over Operating Case Temperature, $V_{\rm CC}$ = 3.13 to 3.47V)

Parameter	Symbol	Minimum	Typical	Maximum	Units
Supply Voltage	V_{cc}	3.13	3.3	3.47	V
Supply Current	I_{CC}	-	175	245	mA

Module Definition

MOD_DEF(0) pin 6	MOD_DEF(1) pin 5	MOD_DEF(2) pin 4	Interpretation by Host
TTL LOW	SCL	SDA	Serial module definition protocol

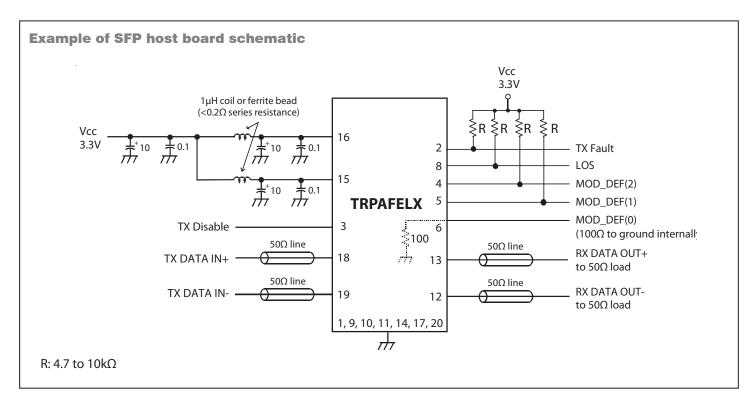




² There is an internal 4.7 to $10k\Omega$ pull-up resistor to *VccT*.

 $^{^3}$ Open collector compatible, 4.7 to $10k\Omega$ pull-up resistor to *Vcc* (Host Supply Voltage).

 $^{^2}$ Open collector compatible, 4.7 to $10k\Omega$ pull-up resistor to \emph{Vcc} (Host Supply Voltage).



Application Notes

Electrical Interface: All signal interfaces are compliant with the SFP MSA specification. The high speed DATA interface is differential AC-coupled internally and can be directly connected to a 3.3V SERDES IC. All low speed control and sense output signals are open collector TTL compatible and should be pulled up with a 4.7 - $10k\Omega$ resistor on the host board.

Loss of Signal (LOS): The Loss of Signal circuit monitors the level of the incoming optical signal and generates a logic HIGH when an insufficient photocurrent is produced.

TX Fault: The output indicates LOW when the transmitter is operating normally, and HIGH with a laser fault including laser end-of-life. TX Fault is an open collector/drain output and should be pulled up with a $4.7 - 10 k\Omega$ resistor on the host board. TX Fault is non-latching (automatically deasserts when fault goes away).

TX Disable: When the TX Disable pin is at logic HIGH, the transmitter optical output is disabled (less than -45dBm).

Serial Identification and Monitoring: The module definition of SFP is indicated by the three module definition pins, MOD_DEF(0), MOD_DEF(1) and MOD_DEF(2). Upon power up, MOD_

DEF(1:2) appear as NC (no connection), and MOD_DEF(0) is TTL LOW. When the host system detects this condition, it activates the serial protocol (standard two-wire I²C serial interface) and generates the serial clock signal (SCL). The positive edge clocks data into the EEPROM segments of the SFP that are not write protected, and the negative edge clocks data from the SFP. This device does not require clock stretching.

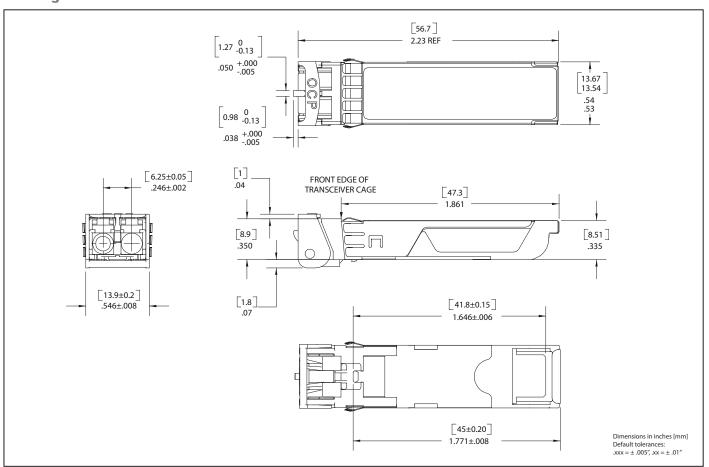
The serial data signal (SDA) is for serial data transfer. The host uses SDA in conjunction with SCL to mark the start and end of serial protocol activation. The supported monitoring functions are temperature, voltage, bias current, transmitter power, average receiver signal, all alarms and warnings, and software monitoring of TX Fault/LOS. The device is internally calibrated.

The data transfer protocol and the details of the mandatory and vendor specific data structures are defined in the SFP MSA, and SFF-8472, Rev. 9.4.

Power Supply and Grounding: The power supply line should be well-filtered. All $0.1\mu F$ power supply bypass capacitors should be as close to the transceiver module as possible.



Package Outline



Ordering Information

Model Name	Operating Case Temperature	Latch Color	Nominal Wavelength
TRPAFELXABSS	- 5°C to +70°C	Silver	1310nm
TRPAFELXAESS	- 5°C to +85°C	Silver	1310nm
TRPAFELXAASS	- 40°C to +85°C	Silver	1310nm