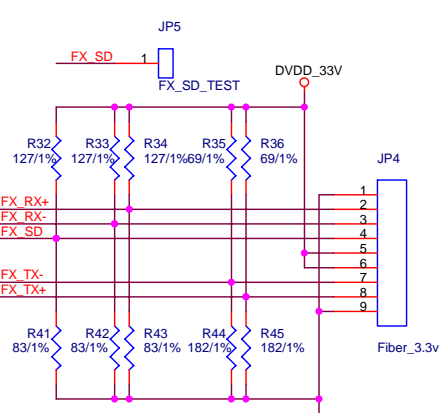
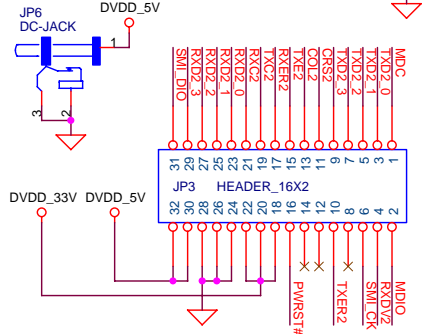
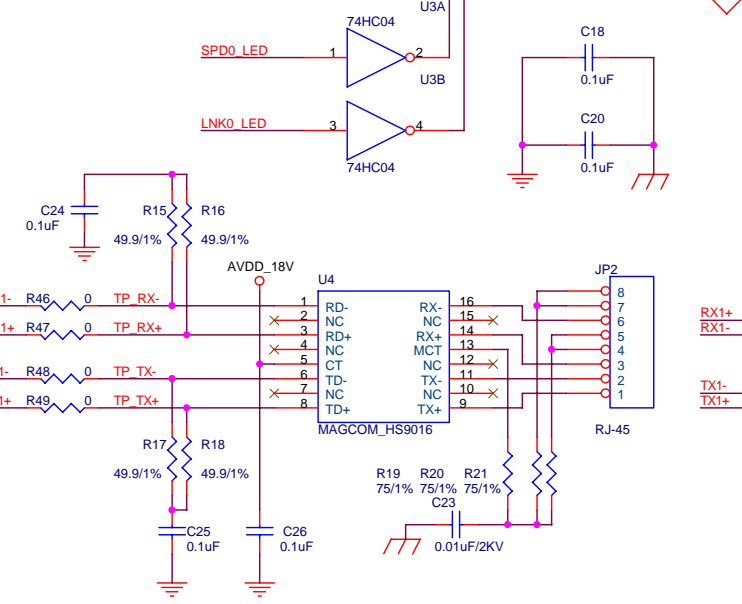
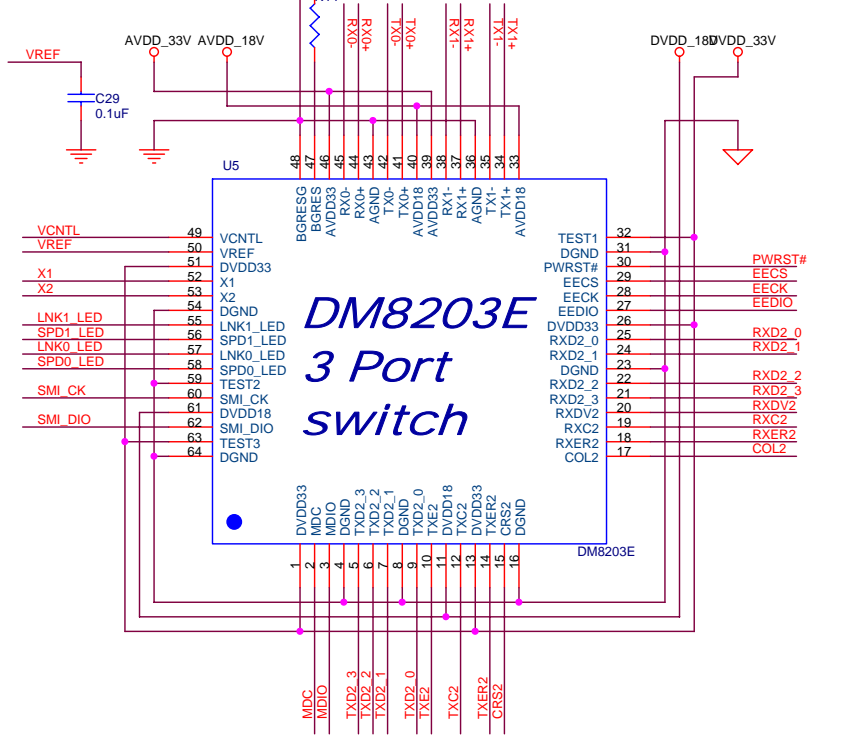
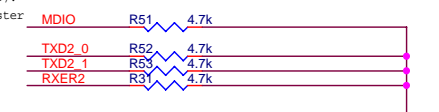
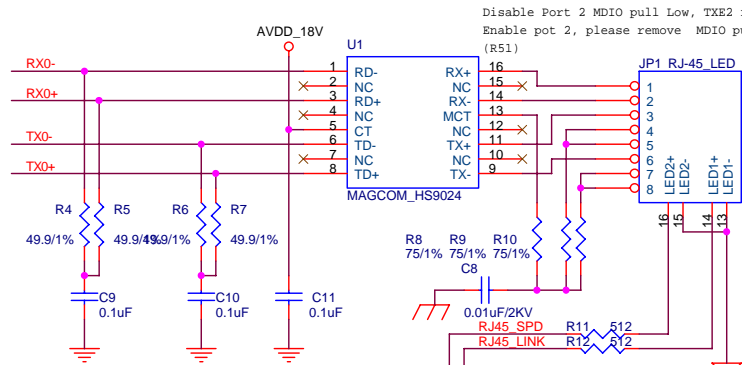
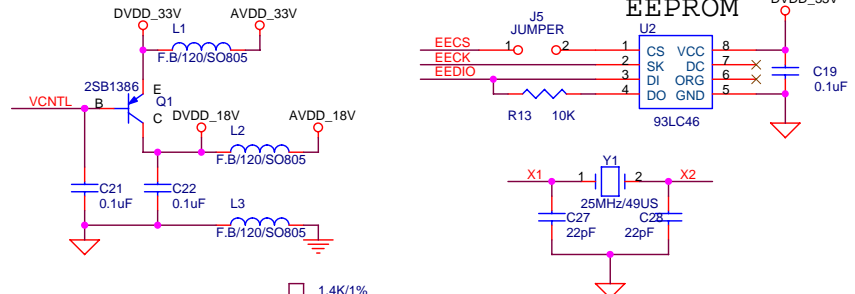
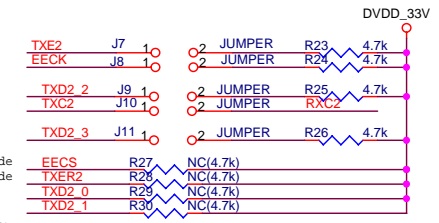


J7 on is P2 force link mode (100Mbps FULL mode)  
 J8 on is P2 fix mode (100Mbps mode)  
 J9, J10 on is P2 use Reverse-MII mode  
 J11 on is P2 use RMII mode  
 J5 on is use EEPROM



DAVICOM Semiconductor Inc.		
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