



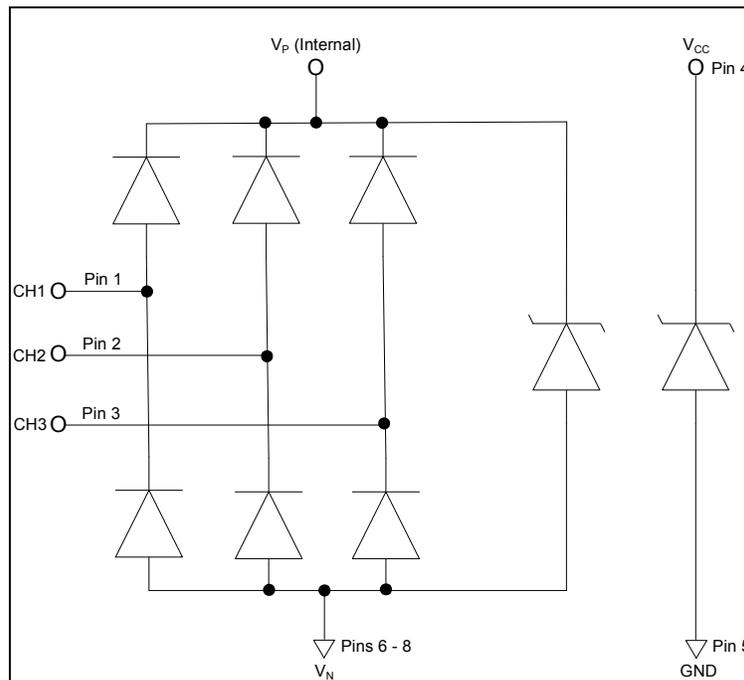
4-Channel Low Capacitance Dual-Voltage ESD Protection Array

CM1241

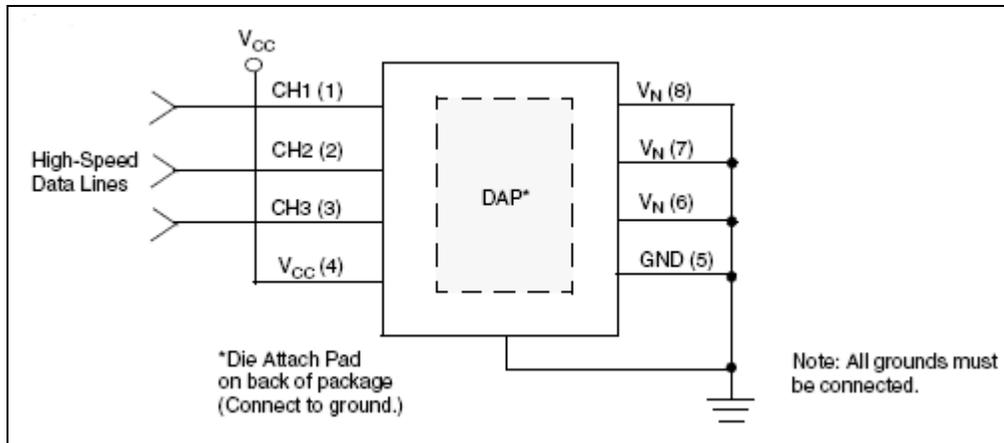
Features

- 3 channels of low voltage ESD protection
- 1 channel of high voltage ESD protection
- Provides ESD protection to IEC61000-4-2 Level 4:
 - ±8kV contact discharge (Pins 1-3)
 - ±15kV contact discharge (Pin 4)
- Low channel input capacitance
- Minimal capacitance change with temperature and voltage
- High voltage zener diode protects supply rail
- No need for external bypass capacitors
- Each I/O pin can withstand over 1000 ESD strikes*
- RoHS compliant, lead-free finish

Block Diagram

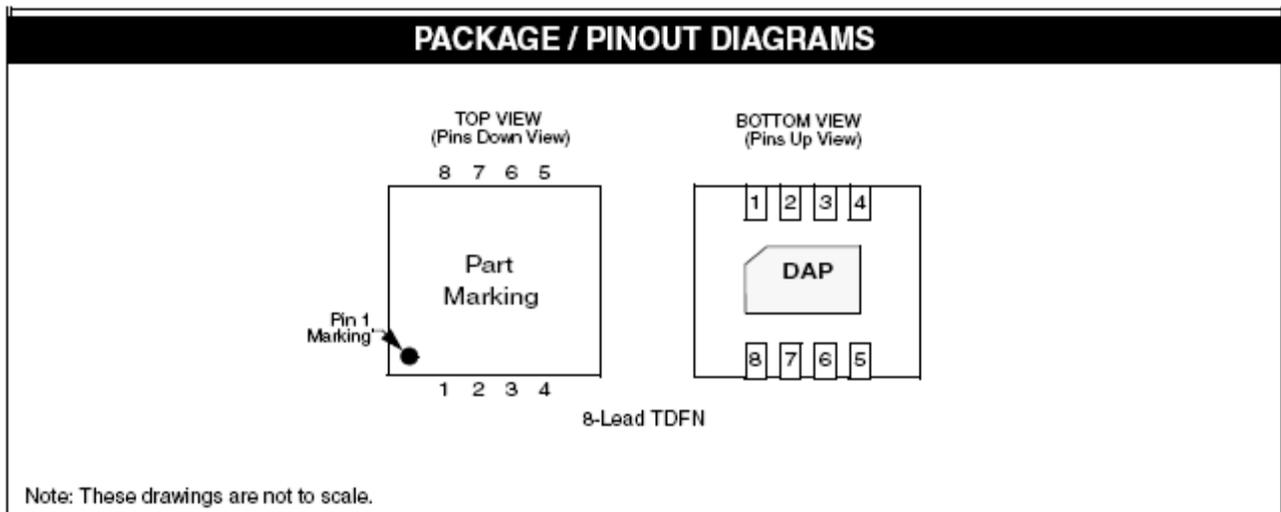


Typical Application



*Standard test condition is IEC61000-4-2 level 4 test circuit with each pin subjected to $\pm 8\text{kV}$ contact discharge for 1000 pulses. Discharges are timed at 1 second intervals and all 1000 strikes are completed in one continuous test run. The part is then subjected to standard production test to verify that all of the tested parameters are within spec after the 1000 strikes.

Package / Pinout Diagrams



CM1241

Pin Descriptions

4-CHANNEL, 8-LEAD, TDFN-8 PACKAGE			
Pin	Name	Type	Description
1	CH1	I/O	LV Low-capacitance ESD Channel
2	CH2	I/O	LV Low-capacitance ESD Channel
3	CH3	I/O	LV Low-capacitance ESD Channel
4	V _{CC}	HV V _{DD}	HV ESD Channel
5	GND		Ground
6	V _N		Negative Voltage Supply Rail
7	V _N		Negative Voltage Supply Rail
8	V _N		Negative Voltage Supply Rail
DAP	GND		Die Attach Pad (Ground)

Ordering Information

PART NUMBERING INFORMATION				
# of Channels	Leads	Package	Lead-free Finish	
			Ordering Part Number ¹	Part Marking
4	8	TDFN-8, 0.4mm	CM1241-04D4	AW1

Note 1: Parts are shipped in Tape and Reel form unless otherwise specified.

Specifications

ABSOLUTE MAXIMUM RATINGS

PARAMETER	RATING	UNITS
DC Voltage on Low-voltage Pins	6.0	V
DC Voltage on High-voltage Pins (V_{cc} pin)	14.5	V
Operating Temperature Range	-40 to +85	°C
Storage Temperature Range	-65 to +150	°C

STANDARD OPERATING CONDITIONS

PARAMETER	RATING	UNITS
Operating Temperature Range	-40 to +85	°C

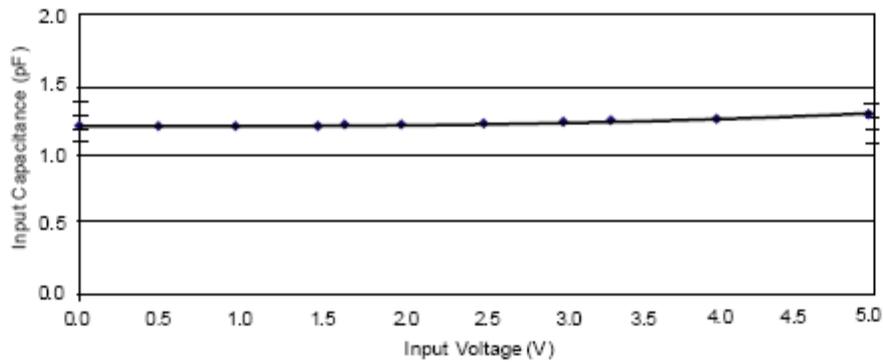
ELECTRICAL OPERATING CHARACTERISTICS (SEE NOTE 1)

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
V_F	LV Diode Reverse Voltage (Positive Voltage)	$I_F = 10\text{mA}; T_A = 25^\circ\text{C}$	6.8	8.2	9.2	V
	LV Diode Forward Voltage (Negative Voltage)	$I_F = 10\text{mA}; T_A = 25^\circ\text{C}$	-1.05	-0.9	-0.6	V
I_{LEAK}	LV Channel Leakage Current (Pins 1 and 2)	$T_A = -30^\circ\text{C to } 65^\circ\text{C}; V_{IN} = 3.3\text{V}, V_N = 0\text{V}$			100	nA
	LV Channel Leakage Current (Pin 3 only)	$T_A = -30^\circ\text{C to } 65^\circ\text{C}; V_{IN} = 3.3\text{V}, V_N = 0\text{V}$			100	nA
C_{IN}	LV Channel Input Capacitance	At 1 MHz, $V_N = 0\text{V}, V_{IN} = 1.65\text{V}$		1.2	1.5	pF
ΔC_{IN}	LV Channel Input Capacitance Matching	At 1 MHz, $V_N = 0\text{V}, V_{IN} = 1.65\text{V}$		0.02		pF
I_{LEAK_HV}	HV Channel Leakage Current	$T_A = 25^\circ\text{C}; V_{CC} = 11\text{V}, V_N = 0\text{V}$		0.1	1.0	μA
C_{IN_HV}	HV Channel Input Capacitance	At 1 MHz, $V_N = 0\text{V}, V_{IN} = 2.5\text{V}$		53		pF
V_{F_HV}	HV Diode Breakdown Voltage Positive Voltage	$I_F = 10\text{mA}; T_A = 25^\circ\text{C}$	14.6		17.7	V
V_{ESD}	ESD Protection Peak Discharge Voltage at any channel input, in system Contact discharge per IEC 61000-4-2 standard	$T_A = 25^\circ\text{C}$	± 8 (Pin 1-3) ± 15 (Pin 4)			kV kV
V_{CL}	LV Channel Clamp Voltage (Pin 1-3) Positive Transients Negative Transients	$T_A = 25^\circ\text{C}, I_{PP} = 1\text{A}, t_p = 8/20\mu\text{S}$		+9.64 -1.75		V V
R_{DYN}	Dynamic Resistance LV Channel Positive Transients LV Channel Negative Transients HV Channel Positive Transients HV Channel Negative Transients	$I_{PP} = 1\text{A}, t_p = 8/20\mu\text{S}$ Any I/O pin to Ground		0.72 0.59 1.20 0.36		Ω Ω Ω Ω

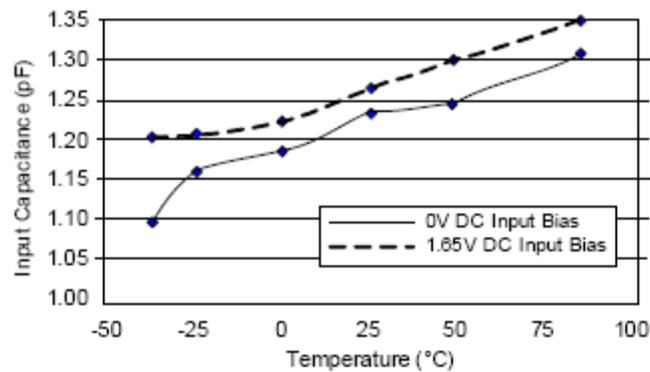
Note 1: All parameters specified at $T_A = -40^\circ\text{C}$ to $+85^\circ\text{C}$ unless otherwise noted.

Performance Information

Input channel capacitance performance curves for low voltage pins



Typical Variation of C_{IN} vs. V_{IN}
 (Low voltage Inputs, $f=1\text{MHz}$, $V_N = 0\text{V}$)



Typical Variation of C_{IN} vs. Temp
 (Low voltage Inputs, $f=1\text{MHz}$, $V_N = 0\text{V}$)

Performance Information (cont'd)

Typical filter performance for low voltage pins

Nominal conditions unless specified; otherwise, 50 ohm environment.

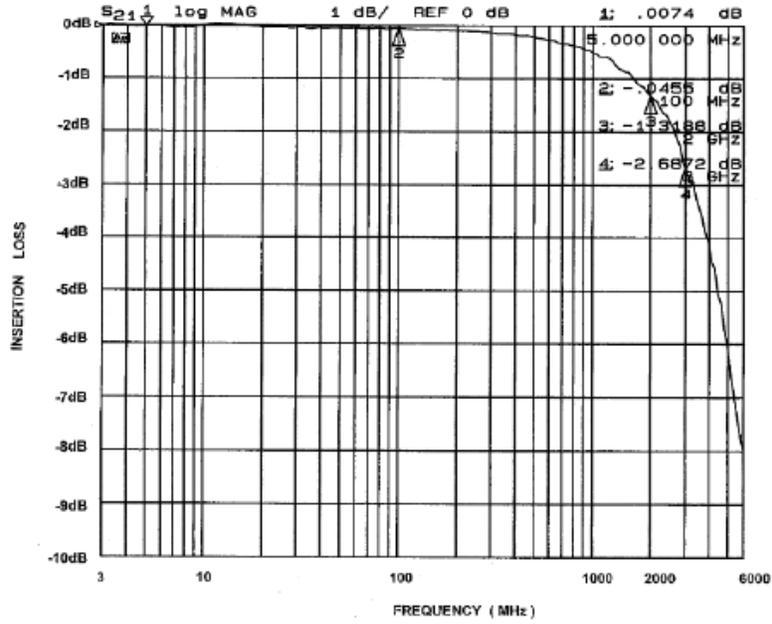


Figure 1. Channel 1 Vs. All GND Pins (0V DC Bias)

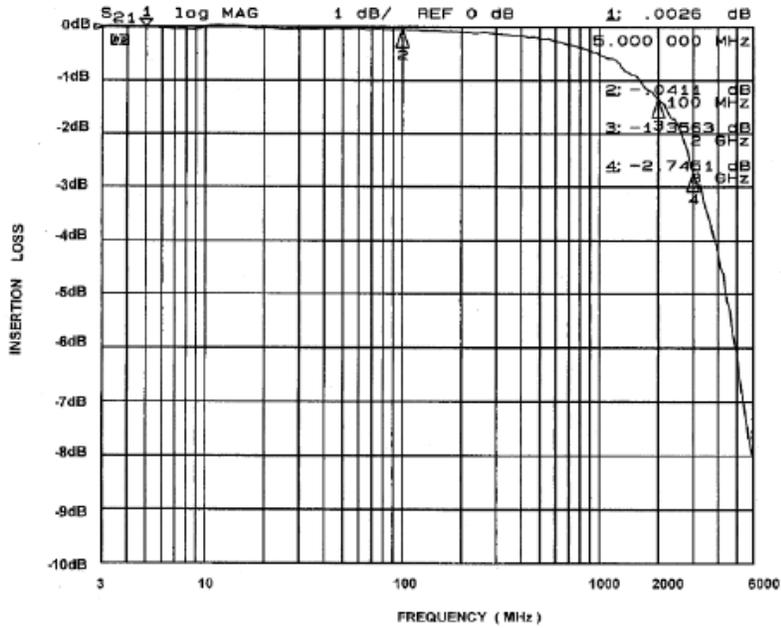


Figure 2. Channel 2 Vs. All GND Pins (0V DC Bias)

Performance Information (cont'd)

Typical filter performance for low voltage pins

Nominal conditions unless specified; otherwise, 50 ohm environment.

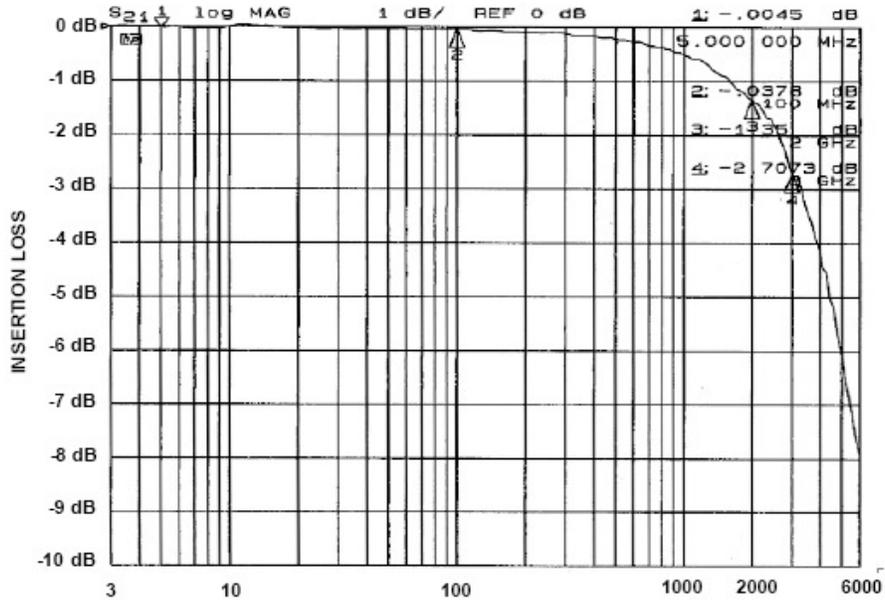


Figure 3. Channel 3 Vs. All GND Pins (0V DC Bias)

CM1241

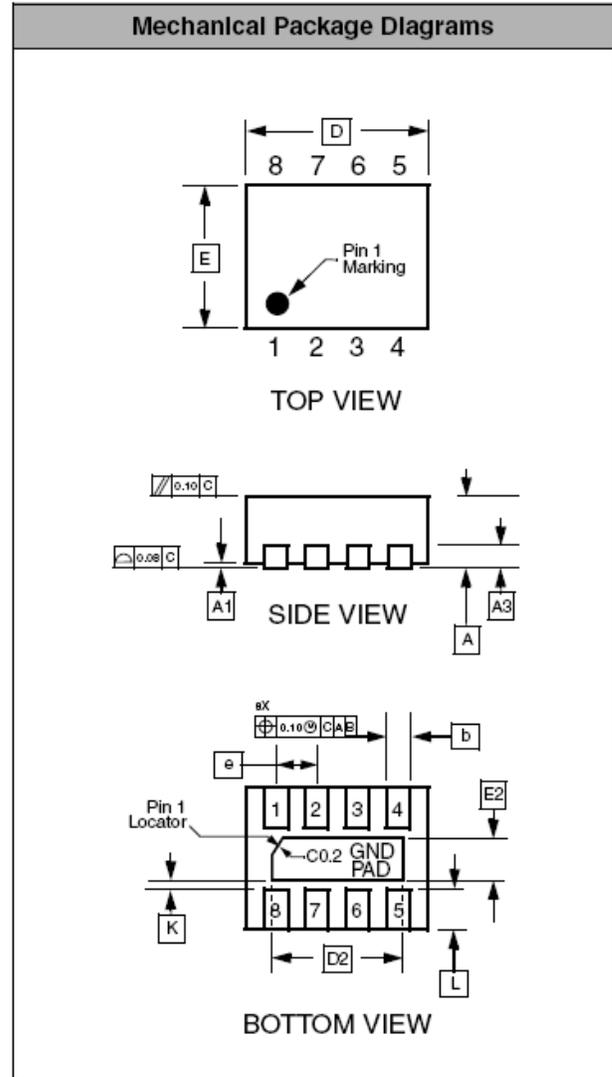
Mechanical Details

TDFN-08 Mechanical Specifications, 0.4mm

Dimensions for the CM1241 supplied in a 8-lead, 0.4mm pitch TDFN package are presented below.

PACKAGE DIMENSIONS						
Package	TDFN					
JEDEC No.	MO-229C*					
Leads	8					
Dim.	Millimeters			Inches		
	Min	Nom	Max	Min	Nom	Max
A	0.70	0.75	0.80	0.028	0.030	0.031
A1	0.00	0.02	0.05	0.000	0.001	0.002
A3	0.200 REF			0.008 REF		
b	0.15	0.20	0.25	0.006	0.008	0.010
D	1.60	1.70	1.80	0.063	0.067	0.071
D2	1.10	1.20	1.30	0.043	0.047	0.051
E	1.25	1.35	1.45	0.049	0.053	0.057
E2	0.30	0.40	0.50	0.012	0.016	0.020
e	0.40 BSC			0.016 BSC		
K	0.22 REF			0.009 REF		
L	0.15	0.25	0.35	0.006	0.010	0.014
# per tape and reel	3000 pieces					
Controlling dimension: millimeters						

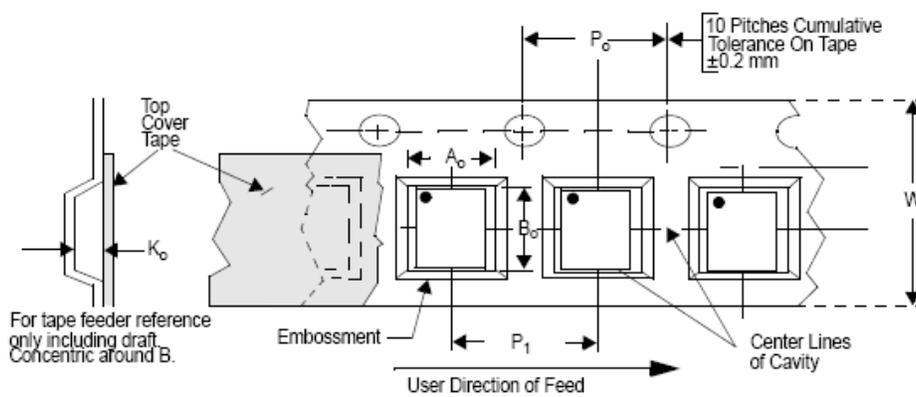
*This package is compliant with JEDEC standard MO-229C with the exception of the D, D2, E, E2, K and L dimensions as called out in the table above.



Dimensions for 8-Lead, 0.4mm Pitch TDFN Package

Tape and Reel Specifications

PART NUMBER	PACKAGE SIZE (mm)	POCKET SIZE (mm) $B_0 \times A_0 \times K_0$	TAPE WIDTH W	REEL DIAMETER	QTY PER REEL	P_0	P_1
CM1241	1.70 X 1.35 X 0.75	2.00 X 1.65 X 1.05	8mm	178mm (7")	3000	4mm	4mm



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