

TECHNICAL SPECIFICATION

MODEL NO.: ED050SC3

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Customer•s Confirmation

Customer

Date

Вy

PVI •s Confirmation

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Confirmed By Prepared By



Revision History

Rev.	Issued Date	Revised Contents
1.0	August.15.2008	New
2.0	October.7.2008	Add Page 7 6-2) Absolute maximum rating Page 16 11.Bar Code definition Page 17 12.Border definition Modify Page 4 3. Mechanical Specifications Page 7 6-3) Panel DC characteristics Page 9 7.Power on sequence Page 14 9. Handling, Safety and Environment Requirements Del Preliminary specification Page 15 10.Reliability test 14.Stylus Tapping Top R:0.4mm Load: 300gf Speed: 5 times/sec Total 13,500times
		change to Top R:0.8mm Load: 300gf Speed: 2 times/sec Total 13,500times
		Del Page 13 Note 8-1

TECHNICAL SPECIFICATION

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1. Application

The display is a TFT active matrix electrophoretic display, with associated interface and control logic, and a reference system design.

The 5" active area contains 800x600 pixels, and has full 1~4 bit display capabilities

An integrated circuit containing interface, timing and control logic is supplied with each panel.

2. Features

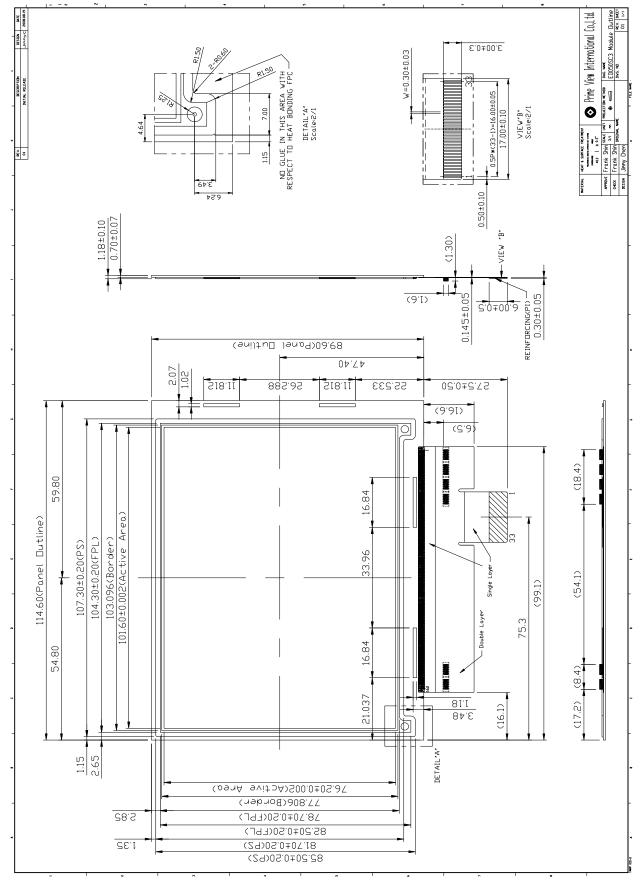
- High contrast TFT electrophoretic
- ➢ 800x600 display
- ➢ High reflectance
- ➢ Ultra wide viewing angle
- ➢ Ultra low power consumption
- > Pure reflective mode
- ➢ Bi-stable
- Commercial temperature range
- Landscape, portrait mode
- Antiglare hard-coated front-surface

3. Mechanical Specifications

Parameter	Specifications	Unit	Remark
Screen Size	5	Inch	
Display Resolution	800 (H) ×600(V)	Pixel	
Active Area	101.6 (H)×76.2 (V)	mm	
Pixel Pitch	0.127(H)x0.127(V)	mm	
Pixel Configuration	Rectangle		
Outline Dimension	114.6(H) ×89.6(V) ×1.18(D) (panel area height) 114.6(H) ×89.6(V) ×2.0(D) (FPC area height with capacitor)	mm	Panel height is measured without released film
Module Weight	24±2	g	



4. Mechanical Drawing of EPD Module





5. Input/Output Terminals

5-1)Pinout List

Pin #	Signal	Description
1	VNEG	Negative power supply source driver
2	NC	NO Connection
3	VPOS	Positive power supply source driver
4	NC	NO Connection
5	VSS	Ground
6	NC	NO Connection
7	VDD	Digital power supply drivers
8	XCL	Clock source driver
9	XLE	Latch enable source driver
10	XOE	Output enable source driver
11	XSHR	Shift direction source driver
12	XSTL2	Start pulse source driver
13	D0	Data signal source driver
14	D1	Data signal source driver
15	D2	Data signal source driver
16	D3	Data signal source driver
17	D4	Data signal source driver
18	D5	Data signal source driver
19	D6	Data signal source driver
20	D7	Data signal source driver
21	NC	NO Connection
22	VCOM	Common connection
23	NC	NO Connection
24	VGG	Positive power supply gate driver
25	NC	NO Connection
26	VEE	Negative power supply gate driver
27	NC	NO Connection
28	MODE2	Output mode selection gate driver
29	MODE1	Output mode selection gate driver
30	XRL	Shift direction gate driver
31	SPV	Start pulse 1st gate driver
32	CKV	Clock 1st gate driver
33	BORDER	Border connection

6.Electrical Characteristics

6-1) Panel interface description

This panel is driven by ASIC PVI-6001A or "Apollo" display controller ASIC. See control product specification for details.

6-2) Absolute maximum rating

Parameter	Symbol	Rating	Unit
Logic Supply Voltage	VDD	-0.3 to +7	V
Positive Supply Voltage	V _{POS}	-0.3 to +18	V
Negative Supply Voltage	V_{NEG}	+0.3 to -18	V
Max .Drive Voltage	V_{POS} - V_{NEG}	36	V
Range			
Supply Voltage	VGG	-0.3 to +45	V
Supply Voltage	VEE	-25.0 to +0.3	V
Supply Range	VGG-VEE	-0.3 to +45	V
Operating Temp. Range	TOTR	0 to +50	°C
Storage Temperature	TSTG	-25 to +70	°C

6-3) Panel DC characteristics

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Signal ground	V _{SS}		-	0	-	V
· · · · · ·	V_{DD}		3.0	3.3	3.6	V
Logic Voltage supply	I _{VDD}	$V_{DD}=3.3V$	-	0.5	1	mA
	V_{EE}		-21	-20	-19	V
Gate Negative supply	I_{EE}	$V_{EE} = -20V$	-	5	15	mA
	V _{GG}		21	22	23	V
Gate Positive supply	I _{GG}	$V_{GG} = 22V$	-	1.4	4.2	mA
Corres No sotires suggiter	V _{NEG}		-15.4	-15	-14.6	V
Source Negative supply	I _{NEG}	$V_{\rm NEG} = -15V$	-	22	44	mA
Correct Descitions around to	V _{POS}		14.6	15	15.4	V
Source Positive supply	I _{POS}	$V_{POS} = 15V$	-	24	48	mA
Dordor oursely	N/	V _{POS} = 15V	14.6	15	15.4	V
Border supply	V _{Border}	V_{NEG} = -15V	-15.4	-15	-14.6	V
Asymmetry source	V _{Asym}	$V_{POS} + V_{NEG}$	-800	0	800	mV
Common volto co	V _{COM}		-2.5	Adjusted	-1.0	V
Common voltage	I _{COM}		-	0.25	-	mA
Maximum power panel	P _{MAX}		-	-	1850	mW
Standby power panel	P _{STBY}		-	-	0.4	mW
Typical power panel	P _{TYP}		-	850	-	mW
Operating temperature			0	-	50	°C
Storage temperature			-25	-	70	°C
Maximum image update time at 25°C			-	1000	-	ms

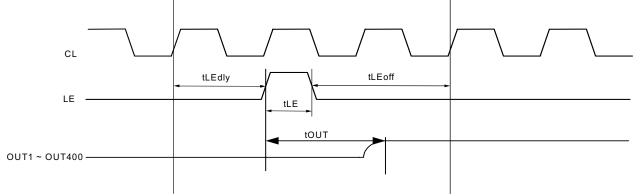
- The maximum power and maximum currents are specified for the worse case power consumption. This occurs when 2 horizontal inverted stripped images are displayed
- The typical power is the consumed power when "typical images" are displayed.
- The standby power is the consumed power when the panel controller is in standby mode.
- The listed electrical/optical characteristics are only guaranteed under the controller & waveform provided by PVI
- Vcom is recommended to be set in the range of assigned value $\pm 0.1V$

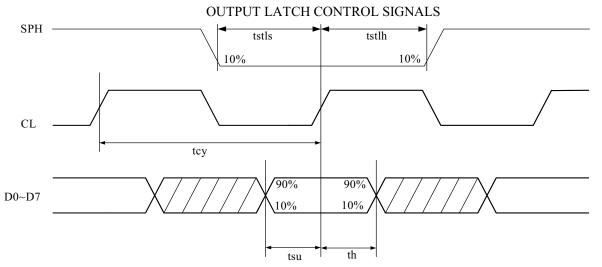
6-4)Panel AC characteristics

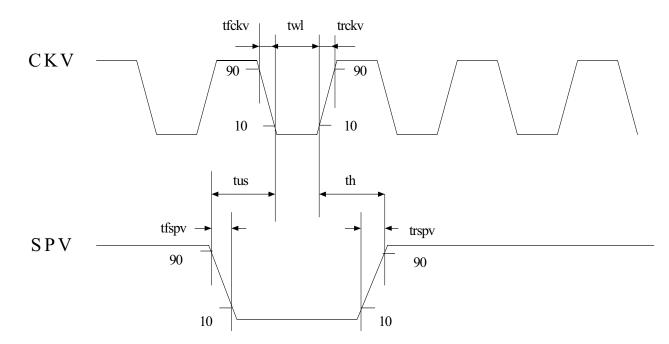
```
VDD=3.0V to 3.6V, unless otherwise specified.
```

Parameter	Symbol	Min.	Тур.	Max.	Unit	App Pin	
Clock frequency	fckv	-	-	200	kHz		
Minimum "L" clock pulse width	twL	0.5	-	-	us	QUIL	
Clock rise time	trckv	-	-	100	ns	CKV	
Clock fall time	tfckv	-	-	100	ns		
Data setup time	tSU	100	-	-	ns	CHAR CDU	
Data hold time	tH	100	-	-	ns	CKV, SPV	
Pulse rise time	trspv	-	-	100	ns		
Pulse fall time	tfspv	-	-	100	ns	SPV	
Clock CL cycle time	tcy	50	-	DC	ns		
D0 D7, SPH setup time	tsu	8	-	-	ns		
D0 D7, SPH hold time	th	1	-	-	ns	Below	
LE on delay time	tLEdly	40	_	_	ns	table	
LE high-level pulse width	tLEw	40	_	-	ns		
LE off delay time	tLEoff	40	-	-	ns]	

CLOCK & DATA TIMING







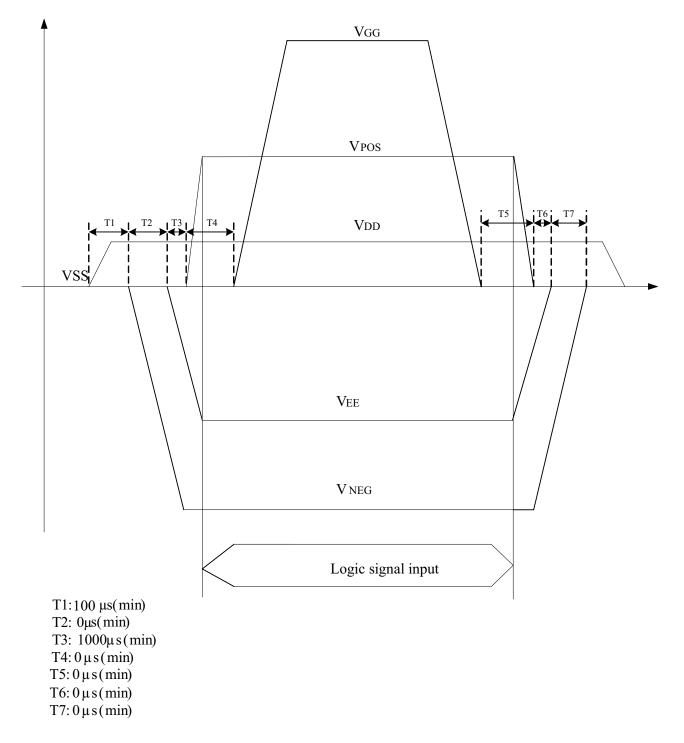
6-5) Power Consumption

Parameter	Symbol	Conditions	TYP	Max	Unit	Remark
Maximum panel power consumption during update.	-	-	-	1850	mW	
Power consumption in standby mode	-	-	-	0.4	mW	
Typical panel power	-	-	850	-	mW	

7. Power on Sequence

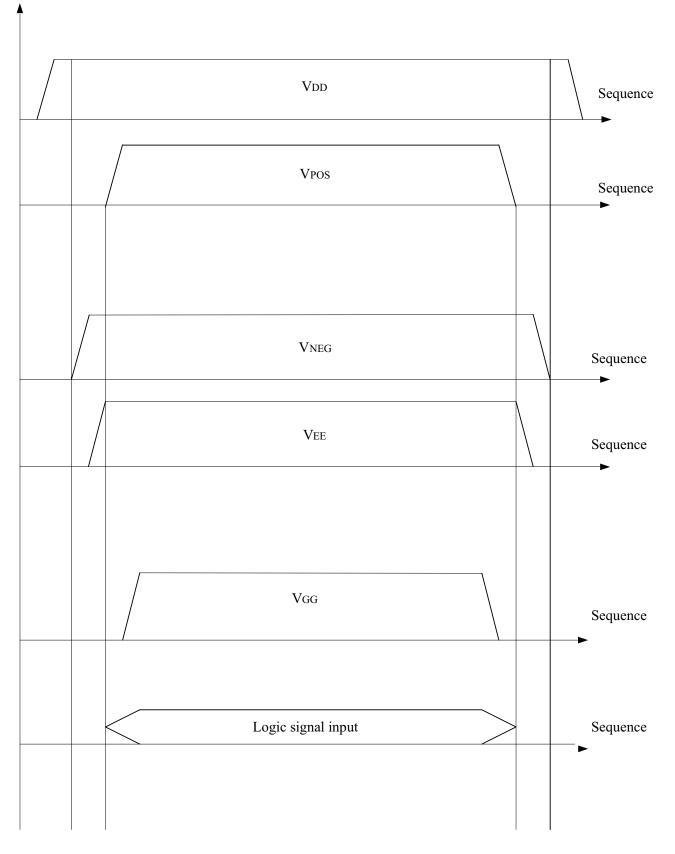
- 1. VSS \rightarrow VDD \rightarrow VNEG \rightarrow VPOS (Source driver)
- 2. VEE \rightarrow VGG(Gate driver)

* Power on sequence's timing chart as blew :





* It is expanded as blew.



8. Optical characteristics

8-1)Specifications

Measurements are made with that the illumination is under an angle of 30 degrees, the detection is perpendicular unless otherwise specified.

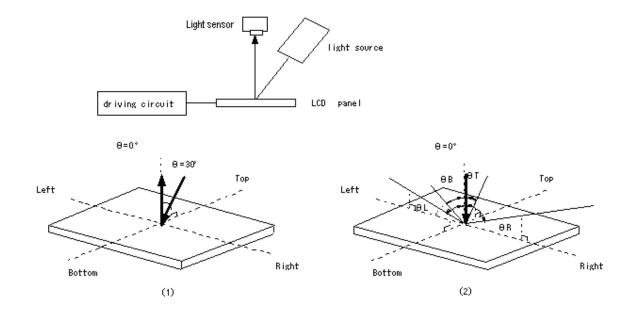
					T =	25°C	
SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT	Note
R	Reflectance	White	30	35	-	%	-
Gn	N _{th} Grey Level	-	-	DS+(WS-DS)×n/(m-1)	-	L*	-
CR	Contrast Ratio	-	6	-	-		-
T _{update}	Update time	2~4-bit mode 1-bit mode		1000 540	-	ms ms	-

WS: White state, DS: Dark state, Gray state from Dark to White :DS \circ G1 \circ G2... \circ Gn... \circ Gm-2 \circ WS m:4 \circ 8 \circ 16 when 2 \circ 3 \circ 4 bits mode

8-2)Definition of contrast ratio

The contrast ratio (CR) is the ratio between the reflectance in a full white area (Rl) and the reflectance in a dark area (Rd):

CR = Rl/Rd

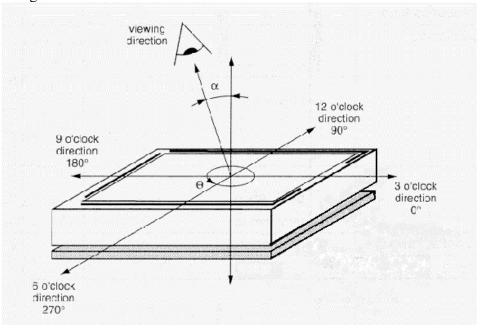


8-3)Reflection Ratio

The reflection ratio is expressed as:

 $R = Reflectance Factor_{white board} x (L_{center} / L_{white board})$

 L_{center} is the luminance measured at center in a white area (R=G=B=1). $L_{white board}$ is the luminance of a standard white board. Both are measured with equivalent illumination source. The viewing angle shall be no more than 2 degrees.



 $\alpha = declination / \theta = azimuth$

9.HANDLING, SAFETY AND ENVIROMENTAL REQUIREMENTS

WARNING

The display glass may break when it is dropped or bumped on a hard surface. Handle with care. Should the display break, do not touch the electrophoretic material. In case of contact with electrophoretic material, wash with water and soap.

CAUTION

The display module should not be exposed to harmful gases, such as acid and alkali gases, which corrode electronic components.

Disassembling the display module can cause permanent damage and invalidate the warranty agreements.

Observe general precautions that are common to handling delicate electronic components. The glass can break and front surfaces can easily be damaged. Moreover the display is sensitive to static electricity and other rough environmental conditions.

Data sheet stat	Data sheet status					
Product This data sheet contains final product specifications.						
specification						
Limiting value	Limiting values					
Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress						
above one or m	ore of the limiting values may cause permanent damage to the device. These are stress					
ratings only and	d operation of the device at these or at any other conditions above those given in the					
Characteristics	Characteristics sections of the specification is not implied. Exposure to limiting values for extended					
periods may affect device reliability.						
Application information						
Where application information is given, it is advisory and does not form part of the specification.						

10. Reliability test

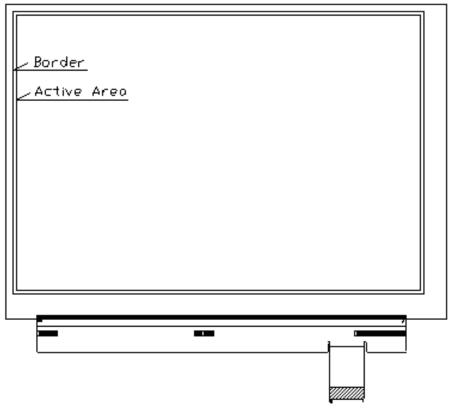
10.1	cellability test			
	TEST	CONDITION	METHOD	REMARK
1	High-Temperature	T = +50°C, RH = 30%	IEC 60	At the end of the test, electric, mechanical,
I	Operation	for 240 hrs	068-2-2Bp	and optical specifications shall be satisfied.
2	Low-Temperature	T = 0°C for 240 hrs	IEC 60	At the end of the test, electric, mechanical,
2	Operation		068-2-2Ab	and optical specifications shall be satisfied.
3	High-Temperature	T = +70°C, RH=23%	IEC 60	At the end of the test, electric, mechanical,
5	Storage	for 240 hrs	068-2-2Bp	and optical specifications shall be satisfied.
4	Low-Temperature	T = -25°C for 240 hrs	IEC 60	At the end of the test, electric, mechanical,
-	Storage		068-2-1Ab	and optical specifications shall be satisfied.
5	High-Temperature,	T = +40°C, RH = 90%	IEC 60	At the end of the test, electric, mechanical,
5	High-Humidity Operation	for 168 hrs	068-2-3CA	specifications shall be satisfied.
6	High Temperature,	T = +50℃, RH=80%	IEC 60	At the end of the test, electric, mechanical,
0	High- Humidity Storage	for 240hrs	068-2-3CA	specifications shall be satisfied.
7	Temperature Cycle	1 cycle:[-25°C 30min]→[+70°C 30	IEC 60 068-2-14	At the end of the test, electric, mechanical,
		min] :100 cycles		specifications shall be satisfied.
8	UV exposure Resistance	765 W/m 2 for 168hrs,40 $^\circ\!\mathbb{C}$	IEC60 068-2-5Sa	Optical characteristics shall be satisfied.
		1.04G, Frequency: 10~500Hz	Full packed for	At the end of the test, electric, mechanical,
9	Package Vibration	Direction: X,Y,Z	shipment	and optical specifications shall be satisfied.
		Duration: 1 hours in each direction	Shiphien	
10	Package Drop Impact	Drop from height of 122 cm on concrete surface. Drop sequence: 1 corner, 3 edges, 6 faces One drop for each.	Full packed for shipment	At the end of the test, electric, mechanical, and optical specifications shall be satisfied.
11	Electrostatic Effect (non-operating)	(Machine model)+/- 250V 0 Ω , 200pF	IEC 62179, IEC 62180	At the end of the test, electric, mechanical, specifications shall be satisfied.
12	Altitude test Operation	700hPa (= 3000m)48Hr		At the end of the test, electric, mechanical, specifications shall be satisfied.
13	Altitude test Storage	260hPa (= 10000m)48Hr		At the end of the test, electric, mechanical, specifications shall be satisfied.
14	Stylus Tapping	POLYACETAL Pen: Top R:0.8mm Load: 300gf Speed: 2 times/sec Total 13,500times,		Pass criteria – no glass breakage or damage to microcapsules.

Actual EMC level to be measured on customer application

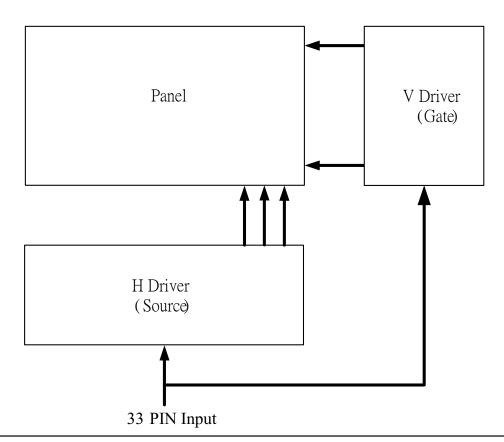
11. Bar Code definition
$\begin{array}{c ccccccccccccccccccccccccccccccccccc$
ED050SC3:E0Y
2 : Internal control codes:
3 : FPL reversion code
V100:3,V110:4
4 : FPL batch code:
(BL/P/B)001~009:01~99, 100~109:A0~A9, 110-119:B0~B9 320~329:Z0~Z9 5: Year:
F:2005 / G:2006 / H:2007 / I:2008 / / Z:2025
6 : Month:
1:Jan. 2:Feb 9:Sep. A:Oct. B:Nov. C:Dec.
7 : Serial number
8 : MFG code:
TOC:T, PVI:P



12. Border definition



13.Block Diagram



OPRIME VIEW 14.Packing

REV DESCRIPTION DESIGN DATE 01 Jimmyc INITIAL RELEASE 08.08.19 ranslate Chinese to English of Note: '08.11.06 Patrickl A Empty tray (5) 1st Layer 5 2nd Layer 6) 12 Layers 2) Total NOTE: Таре 6 50-0100091 CARTON INTERNAL 1.One layer include: 1 piece of cushion sheet, 5 50-0500041 摺□袋450*380*700mm 抗靜電 6pcs panel & 1 piece of tray. 2.Q'TY: 72 pcs panel/carton. 3.Dimension: 455*375*190mm 4 EPD 5" Panel 72 3 50E0200031 EPE CUSHION SHEE 抗靜雷 12 2 50E0300071 ΡS TRAY 抗靜電 4.Weight: 4.7 KG <u>13</u> 2 1 50-0300491 EPE FOAM BMake sure tray stacked with 180° rotation. We can check this by lateral side view. ITEM PART NO. DESCRIPTION QTY REMARK MTL.SPEC. UNSPECIFIED TOL'S REMARK 元太科技股份有限公司 ANGLE $\langle \diamond \rangle$ Prime View Internation Co., ltd. ROUGHNESS SCALE UNIT SHEET DWG.TITLE APPROVE Frank Shin 1 OF 1 ED050SC3 PACKING DRAW 1:1 mm снеск Frank Shin DWG.NO. MTL.NO. REV. $\mathop{\rm A_4}_{\rm size}$ Jimmyc DESIGN 02