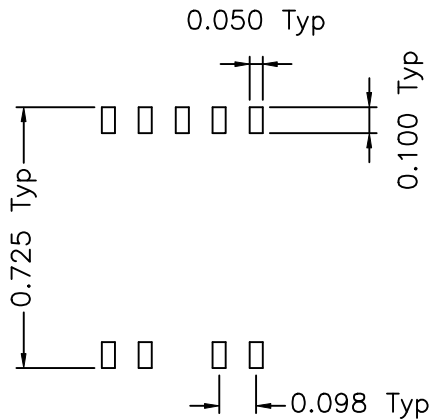
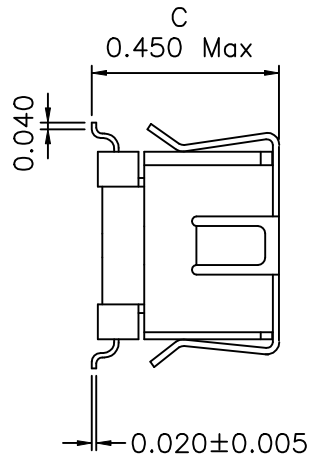
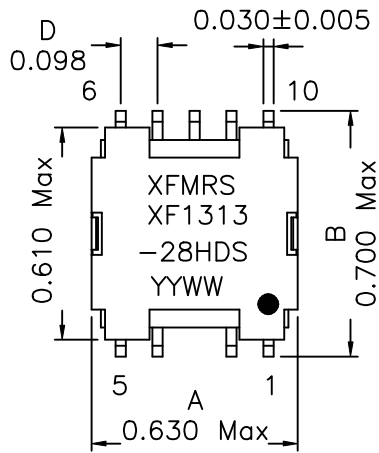
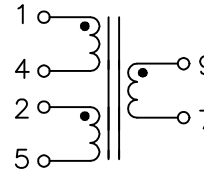


### 1. Dimensions:



Suggested PCB Layout

### 2.Schematic:



### 3.Electrical Specifications: @25°C

PIN1-5 OCL: 2.0mH ±6% @10KHz 0.1V, 0/160mADC  
(tie 2+4), Ls.

PIN1-5 LL: 30uH Max @100KHz 100mV  
(tie 2+4, 7+9), Ls.

URNS RATIO: (1-5):(9-7)=2:1±1%

DC Res.: P1-4 2.5 Ohms±10%

DC Res.: P2-5 2.5 Ohms±10%

DC Res.: P9-7 1.45 Ohms±10%

THD: -60dB typ., @1KHz

INSERTION LOSS: 1dB Max @40KHz

LONGITUDINAL BALANCE: 50dB Min. @1KHz-300KHz

ISOLATION VOLTAGE: 2050VAC for 1 sec between 1-9  
tie(2+4)

Notes:

1. Solderability: Leads shall meet MIL-STD-202, Method 208D for solderability.
2. Flammability: UL94V-0
3. ASTM oxygen index: > 28%
4. Insulation System: Class F 155°C. UL file E151556
5. Operating Temperature Range: All listed parameters are to be within tolerance from -40°C to +85°C
6. Storage Temperature Range: -55°C to +125°C
7. Aqueous wash compatible

DOC. REV B/1

<b>XFMRs Inc</b>	Title: HDSL TRANSFORMER		
	UNLESS OTHERWISE SPECIFIED		REV. B
	TOLERANCES:		P/N: XF1313-28HDS
	.xxx ±0.010		DWN. 廖玉坤 Jun-17-03
Dimensions in INCH		CHK. 李清儿 Jun-17-03	
SHEET 1 OF 1		APP. BW Jun-17-03	