Single N-channel MOSFET

ELM32416LA-S

■ General description

ELM32416LA-S uses advanced trench technology to provide excellent Rds(on), low gate charge and low gate resistance.

■ Features

- Vds=25V
- Id=46A
- Rds(on) $< 17 \text{m} \Omega$ (Vgs=10V)
- Rds(on) $< 25m\Omega$ (Vgs=7V)

■ Maximum absolute ratings

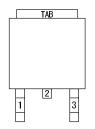
Parameter		Symbol	Limit	Unit	Note		
Gate-source voltage		Vgs	±20	V			
Continuous drain current	Ta=25℃	1.3	46	Δ.			
	Ta=100°C	Id	28	A			
Pulsed drain current		Idm	140	А	3		
Avalanche current		Iar	20	А			
Avalanche energy	L=0.1mH	Eas	140	mJ			
Repetitive avalanche energy	L=0.05mH	Ear	5.6	mJ	4		
Power dissipation	Ta=25℃	Dη	55	W			
	Ta=100°C	Pd	33]			
Junction and storage temperature range		Tj, Tstg	-55 to 150	$^{\circ}$ C			

■Thermal characteristics

Parameter		Symbol	Тур.	Max.	Unit	Note
Maximum junction-to-case	Steady-state	Rθjc		3.0	°C/W	
Maximum junction-to-ambient	Steady-state	Rθja		70.0	°C/W	
Maximum case-to-heatsink		$R\theta cs$	0.7		°C/W	

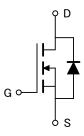
■Pin configuration

TO-252-3 (TOP VIEW)



Pin No.	Pin name
1	GATE
2	DRAIN
3	SOURCE

■ Circuit





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■Electrical characteristics

Ta=25℃

Parameter	Symbol	Condition	Min.	Тур.	Max.	Unit	Note	
STATIC PARAMETERS								
Drain-source breakdown voltage	BVdss	$Id=250 \mu A$, $Vgs=0V$	25			V		
Zero gate voltage drain current	Idss	Vds=20V, Vgs=0V			25	μΑ		
		Vds=20V, Vgs=0V, Tj=125°C			250			
Gate-body leakage current	Igss	$Vds=0V$, $Vgs=\pm 20V$			± 250	nΑ		
Gate threshold voltage	Vgs(th)	Vds=Vgs, Id=250 μ A	0.8	1.2	2.5	V		
On state drain current	Id(on)	Vgs=10V, Vds=10V	45			Α	1	
Static drain-source on-resistance	Rds(on)	Vgs=10V, Id=20A		14	17	$ m \Omega$	2 1	
	IXuS(OII)	vgs-1v, 10-18A	18		25	$ m \Omega$	1	
Forward transconductance	Gfs	Vds=15V, Id=30A		16		S	1	
Diode forward voltage	Vsd	If=Is, Vgs=0V			1.3	V	1	
Max. body-diode continuous current	Is				45	А		
Pulsed body-diode current	Ism				150	А	3	
DYNAMIC PARAMETERS								
Input capacitance	Ciss			600		рF		
Output capacitance	Coss	Vgs=0V, Vds=15V, f=1MHz		290		рF		
Reverse transfer capacitance	Crss			100		рF		
SWITCHING PARAMETERS								
Total gate charge	Qg	Vgs=10V, Vds=12.5V		25.0		пC	2	
Gate-source charge	Qgs	Id=20A		2.9		пC	2	
Gate-drain charge	Qgd	10-20/4		7.0		пC	2	
Turn-on delay time	td(on)			7		ns	2	
Turn-on rise time	tr	Vgs=10V, Vds=15V, Id ≅ 30A		7		ns	2	
Turn-off delay time	td(off)	Rl=1 Ω ,Rgen=2.5 Ω		24		ns	2	
Turn-off fall time	tf			6		ns	2	
Body diode reverse recovery time	trr			37		ns		
Peak reverse recovery current	Irm(rec)	If=Is, dI/dt=100A/μs		200		А		
Body diode reverse recovery charge	Qrr			0.043		μС		

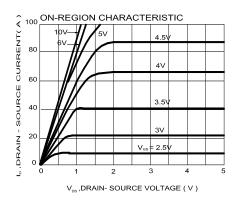
NOTE:

- 1. Pulse test : Pulsed width $\leq 300\,\mu\,\mathrm{sec}$ and Duty cycle $\leq 2\%$.
- 2. Independent of operating temperature.
- 3. Pulsed width limited by maximum junction temperature.
- 4. Duty cycle \leq 1%.

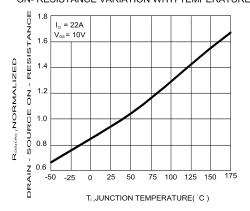


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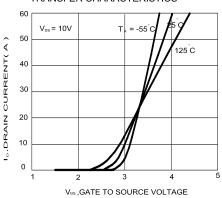
■ Typical electrical and thermal characteristics



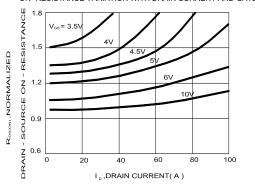
ON- RESISTANCE VARIATION WITH TEMPERATURE



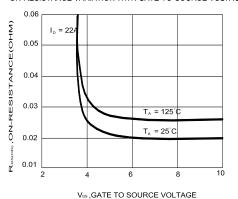
TRANSFER CHARACTERISTICS



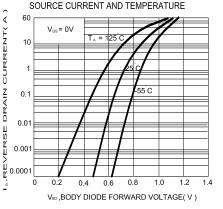
ON- RESISTANCE VARIATION WITH DRAIN CURRENT AND GATE



ON-RESISTANCE VARIATION WITH GATE-TO-SOURCE VOLTAGE



BODY DIODE FORWARD VOLTAGE VARIATION WITH



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