## **PNP Silicon Epitaxial Planar Transistor**

for switching and AF amplifier applications.

The transistor is subdivided into three group, M, L and K according to its DC current gain.

On special request, these transistors can be manufactured in different pin configurations.



1. Emitter 2. Collector 3. Base TO-92 Plastic Package

### Absolute Maximum Ratings (T<sub>a</sub> = 25 °C)

Parameter	Symbol	Value	Unit
Collector Base Voltage	-V <sub>CBO</sub>	30	V
Collector Emitter Voltage	-V <sub>CEO</sub>	25	V
Emitter Base Voltage	-V <sub>EBO</sub>	5	V
Collector Current	-l <sub>C</sub>	700	mA
Base Current	-I <sub>B</sub>	150	mA
Power Dissipation	P <sub>tot</sub>	600	mW
Junction Temperature	T <sub>j</sub>	150	°C
Storage Temperature Range	T <sub>S</sub>	- 55 to + 150	°C

#### Characteristics at T<sub>a</sub> = 25 °C

Characteristics at Ia = 23 C		1	1	1
Parameter	Symbol	Min.	Max.	Unit
DC Current Gain at -V <sub>CE</sub> = 1 V, -I <sub>C</sub> = 100 mA Current Gain Group M L K at -V <sub>CE</sub> = 1 V, -I <sub>C</sub> = 700 mA	h <sub>FE</sub> h <sub>FE</sub> h <sub>FE</sub>	90 135 200 50	180 270 400	- - - -
Collector Base Cutoff Current at -V <sub>CB</sub> = 30 V	-I <sub>CBO</sub>	-	100	nA
Emitter Base Cutoff Current at -V <sub>EB</sub> = 5 V	-I <sub>EBO</sub>	-	100	nA
Collector Emitter Saturation Voltage at $-I_C = 700 \text{ mA}$ , $-I_B = 70 \text{ mA}$	-V <sub>CE(sat)</sub>	-	0.6	V
Base Emitter Saturation Voltage at $-I_C = 700 \text{ mA}$ , $-I_B = 70 \text{ mA}$	-V <sub>BE(sat)</sub>	-	1.2	V
Base Emitter Voltage at $-V_{CE} = 6 \text{ V}$ , $-I_C = 10 \text{ mA}$	-V <sub>BE</sub>	0.6	0.7	V
Gain Bandwidth Product at $-V_{CE} = 6 \text{ V}$ , $-I_E = 10 \text{ mA}$	f <sub>T</sub>	50	-	MHz
Collector to Base Capacitance at -V <sub>CB</sub> = 6 V, f = 1 MHz	СОВ	-	40	pF



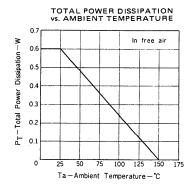
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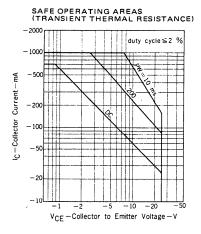
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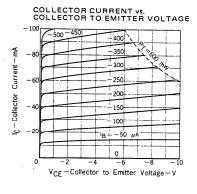


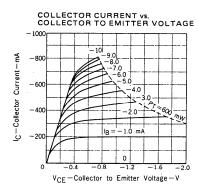


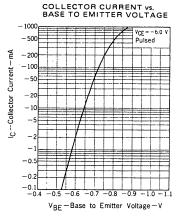


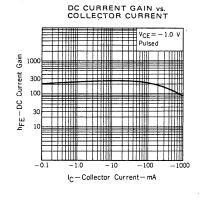


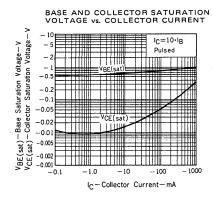


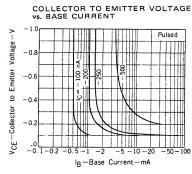


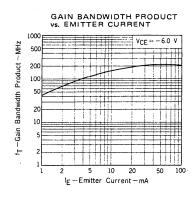














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