



40V DUAL N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

| V _{(BR)DSS} | R _{DS(on)} | I _D T _A = 25°C |
|----------------------|-------------------------------|---|
| 40V | 27mΩ @ V _{GS} = 10V | 7.1A |
| | 47mΩ @ V _{GS} = 4.5V | 5.4A |

Description and Applications

This MOSFET has been designed to minimize the on-state resistance and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- Motor control
- Backlighting
- DC-DC Converters
- Power management functions

Features and Benefits

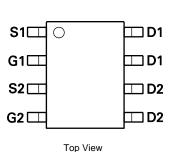
- Low on-resistance
- Fast switching speed
- "Green" component and RoHS compliant (Note 1)
- Qualified to AEC-Q101 Standards for High Reliability

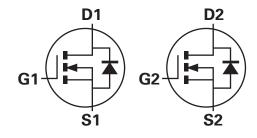
Mechanical Data

- Case: SO-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0 (Note 1)
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals Connections: See diagram below
- Terminals: Finish Matte Tin annealed over Copper lead frame. Solderable per MIL-STD-202, Method 208
- Weight: 0.074 grams (approximate)



Top View





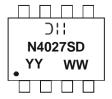
Equivalent Circuit

Ordering Information (Note 1)

| Product | Marking | Reel size (inches) | Tape width (mm) | Quantity per reel |
|---------------|---------|--------------------|-----------------|-------------------|
| DMN4027SSD-13 | N4027SD | 13 | 12 | 2,500 |

Note: 1. Diodes, Inc. defines "Green" products as those which are RoHS compliant and contain no halogens or antimony compounds; further information about Diodes Inc.'s "Green" Policy can be found on our website. For packaging details, go to our website.

Marking Information



DII = Manufacturer's Marking
N4027SD = Product Type Marking Code
YYWW = Date Code Marking
YY = Year (ex: 09 = 2009)
WW = Week (01-53)



Maximum Ratings @T_A = 25°C unless otherwise specified

| (| Characteristic | | Symbol | Value | Unit | |
|------------------------------------|-----------------------|------------------------------|------------------|-------|------|--|
| Drain-Source voltage | | | V _{DSS} | 40 | V | |
| Gate-Source voltage | | (Note 2) | V _{GS} | ±20 | V | |
| Continuous Drain current | | (Note 4) | | 7.1 | | |
| | $V_{GS} = 10V$ | $T_A = 70^{\circ}C$ (Note 4) | ID | 5.7 | А | |
| | | (Note 3) | | 5.4 | | |
| Pulsed Drain current | V _{GS} = 10V | (Note 5) | I _{DM} | 28.0 | А | |
| Continuous Source current (| Body diode) | (Note 4) | I _S | 3.3 | А | |
| Pulsed Source current (Body diode) | | (Note 5) | I _{SM} | 28.0 | А | |

Thermal Characteristics @T_A = 25°C unless otherwise specified

| Characteristic | Symbol | Value | Unit | |
|---|---------------|-----------------------------------|--------------|------------|
| | (Notes 3 & 6) | | 1.25 10.0 | |
| Power dissipation Linear derating factor | (Notes 3 & 7) | P _D | 1.8 14.3 | W mW/°C |
| | (Notes 4 & 6) | | 2.1 17.1 | |
| | (Notes 3 & 6) | | 100 | |
| Thermal Resistance, Junction to Ambient | (Notes 3 & 7) | $R_{\theta JA}$ | 70 | |
| | (Notes 4 & 6) | | 58 | °C/W |
| Thermal Resistance, Junction to Lead | (Notes 6 & 8) | R _{θJL} | 53 | |
| Operating and storage temperature range | | T _J , T _{STG} | -55 to 150 | °C |

Notes: 2. AEC-Q101 V_{GS} maximum is $\pm 16V.$

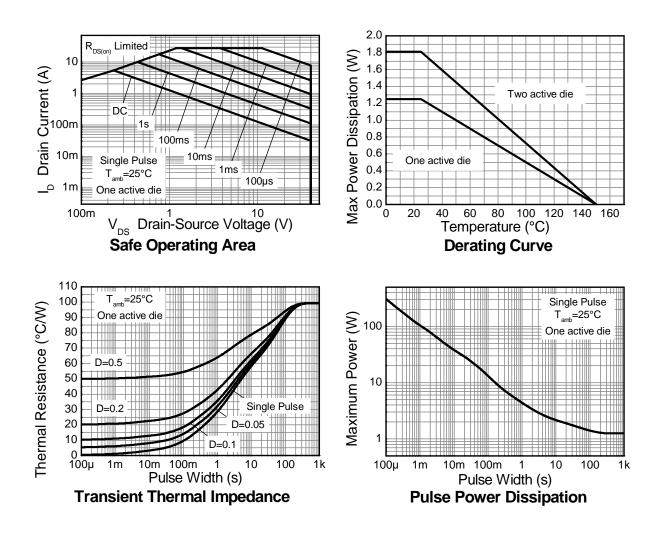
3. For a device surface mounted on 25mm x 25mm x 1.6mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions; the device is measured when operating in a steady-state condition.

4. Same as note (3), except the device is measured at t ≤ 10 sec.
5. Same as note (3), except the device is pulsed with D= 0.02 and pulse width 300 µs. The pulse current is limited by the maximum junction temperature.
6. For a dual device with one active die.
7. For a device with two active die running at equal power.
6. The pulse current is limited by the maximum junction temperature.

8. Thermal resistance from junction to solder-point (at the end of the drain lead).



Thermal Characteristics





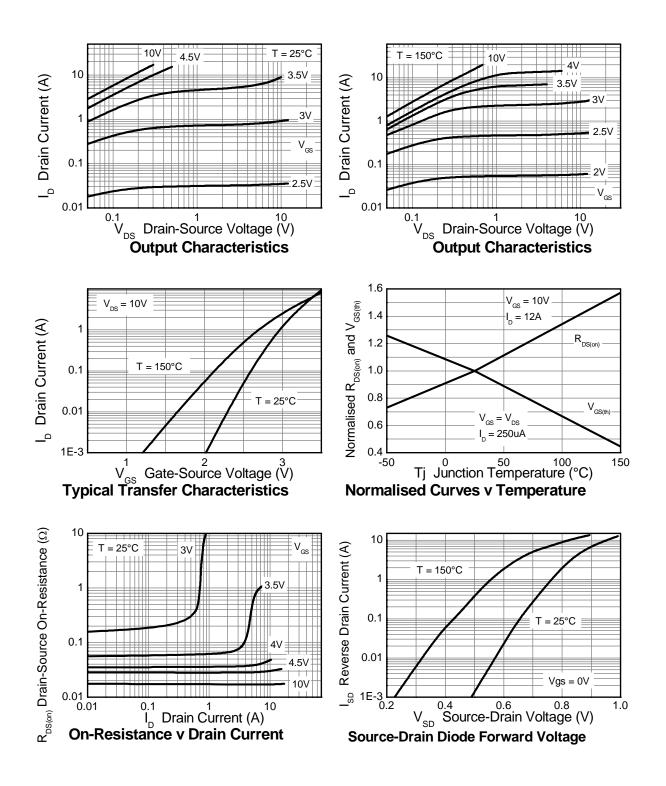
Electrical Characteristics @T_A = 25°C unless otherwise specified

| Characteristic | Symbol | Min | Тур | Max | Unit | Test Condition | |
|--|--------------------------|-----|-------|-------|------|--|--|
| OFF CHARACTERISTICS | | | | | • | | |
| Drain-Source Breakdown Voltage | BV _{DSS} | 40 | | | V | I _D = 250μA, V _{GS} = 0V | |
| Zero Gate Voltage Drain Current | I _{DSS} | | — | 0.5 | μA | V _{DS} = 40V, V _{GS} = 0V | |
| Gate-Source Leakage | I _{GSS} | | — | ±100 | nA | V _{GS} = ±20V, V _{DS} = 0V | |
| ON CHARACTERISTICS | | | | | | | |
| Gate Threshold Voltage | V _{GS(th)} | 1.0 | _ | 3.0 | V | $I_D=250\mu A, V_{DS}=V_{GS}$ | |
| Statia Drain Source On Desistance (Note 0) | | _ | 0.017 | 0.027 | 0 | V _{GS} = 10V, I _D = 7A | |
| Static Drain-Source On-Resistance (Note 9) | R _{DS} (ON) | | 0.031 | 0.047 | Ω | V _{GS} = 4.5V, I _D = 6A | |
| Forward Transconductance (Notes 9 & 10) | g fs | _ | 22.8 | | S | V _{DS} = 15V, I _D = 7A | |
| Diode Forward Voltage (Note 9) | V _{SD} | _ | 0.85 | 1.1 | V | I _S = 7A, V _{GS} = 0V | |
| Reverse recovery time (Note 10) | t _{rr} | | 12.1 | | ns | -I _S = 2.1, di/dt= 100A/μs | |
| Reverse recovery charge (Note 10) | Q _{rr} | _ | 5.1 | | nC | | |
| DYNAMIC CHARACTERISTICS (Note 10) | | | | | | | |
| Input Capacitance | C _{iss} | _ | 604 | | pF | V _{DS} = 20V, V _{GS} = 0V f= 1MHz | |
| Output Capacitance | Coss | _ | 106 | | pF | | |
| Reverse Transfer Capacitance | C _{rss} | _ | 59.6 | | pF | | |
| Total Gate Charge (Note 11) | Qg | _ | 6.3 | _ | nC | V _{GS} = 4.5V | |
| Total Gate Charge (Note 11) | Qg | _ | 12.9 | | nC | V _{DS} = 20V | |
| Gate-Source Charge (Note 11) | Q _{gs} | | 2.4 | | nC | V _{GS} = 10V I _D = 7A | |
| Gate-Drain Charge (Note 11) | Q _{qd} | _ | 3.3 | | nC | 7 | |
| Turn-On Delay Time (Note 11) | t _{D(on)} | _ | 3.1 | | ns | · | |
| Turn-On Rise Time (Note 11) | tr | | 3.1 | | ns | V _{DD} = 20V, V _{GS} = 10V I _D = 1A, R _G ≅ 6.0Ω | |
| Turn-Off Delay Time (Note 11) | t _{D(off)} | _ | 15.4 | | ns | | |
| Turn-Off Fall Time (Note 11) | t _f | _ | 7.5 | | ns | 1 | |

 9. Measured under pulsed conditions. Pulse width ≤ 300μs; duty cycle ≤ 2%
 10. For design aid only, not subject to production testing.
 11. Switching characteristics are independent of operating junction temperatures. Notes:

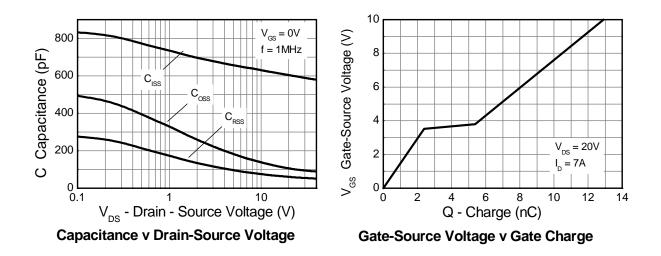


Typical Characteristics





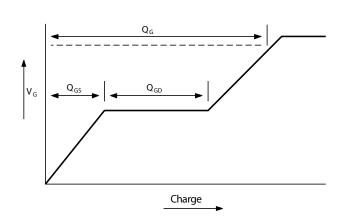
Typical Characteristics - continued



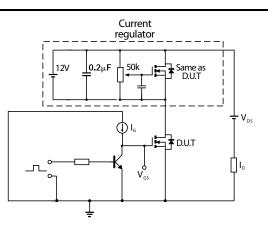
Test Circuits

 V_{DS} 90%

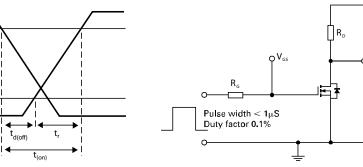
10% $\rm V_{GS}$



Basic gate charge waveform



Gate charge test circuit

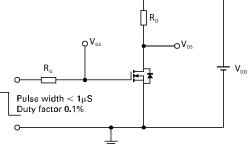


Switching time waveforms

t_r

t_(on)

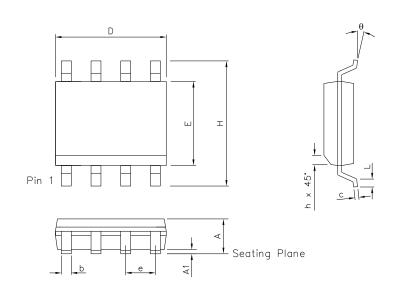
t_{d(on)}



Switching time test circuit

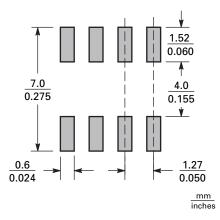


Package Outline Dimensions



| DIM | Inc | hes | Millim | neters | DIM | Inches | | Millimeters | |
|-----|-------|-------|--------|--------|-----|-----------|-------|-------------|------|
| | Min. | Max. | Min. | Max. | | Min. | Max. | Min. | Max. |
| А | 0.053 | 0.069 | 1.35 | 1.75 | е | 0.050 BSC | | 1.27 BSC | |
| A1 | 0.004 | 0.010 | 0.10 | 0.25 | b | 0.013 | 0.020 | 0.33 | 0.51 |
| D | 0.189 | 0.197 | 4.80 | 5.00 | С | 0.008 | 0.010 | 0.19 | 0.25 |
| н | 0.228 | 0.244 | 5.80 | 6.20 | θ | 0° | 8° | 0° | 8° |
| E | 0.150 | 0.157 | 3.80 | 4.00 | h | 0.010 | 0.020 | 0.25 | 0.50 |
| L | 0.016 | 0.050 | 0.40 | 1.27 | - | - | - | - | - |

Suggested Pad Layout





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