TOSHIBA Field Effect Transistor Silicon P-Channel MOS Type (π-MOSV)

2SJ610

Switching Regulator, DC/DC Converter and Motor Drive Applications

- Low drain-source ON-resistance: $R_{DS (ON)} = 1.85 \Omega (typ.)$
- High forward transfer admittance: $|Y_{fS}| = 18 \text{ S (typ.)}$
- Low leakage current: $I_{DSS} = -100 \mu A (V_{DS} = -250 V)$
- Enhancement mode: $V_{th} = -1.5$ to -3.5 V ($V_{DS} = 10$ V, $I_{D} = 1$ mA)

Absolute Maximum Ratings (Ta = 25°C)

Characteristic			Symbol	Rating	Unit	
Drain-source voltage			V_{DSS}	-250	V	
Drain-gate voltage ($R_{GS} = 20 \text{ k}\Omega$)			V_{DGR}	-250	V	
Gate-source voltage			V_{GSS}	±20	٧	
Drain current	DC	(Note 1)	I _D	-2.0	А	
	Pulse	(t = 1 ms) (Note 1)	I _{DP}	-4.0		
Drain power dissipation			PD	20	W	
Single-pulse avalanche energy (Note 2)			E _{AS}	180	mJ	
Avalanche current			I _{AR}	-2.0	Α	
Repetitive avalanche energy (Note 3)			E _{AR}	2.0	mJ	
Channel temperature			T _{ch}	150	°C	
Storage temperature range			T _{stg}	-55 to 150	°C	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

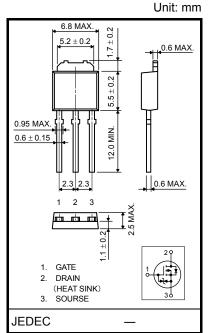
Characteristic	Symbol	Max	Unit	
Thermal resistance, channel to case	R _{th (ch-c)}	6.25	°C/W	
Thermal resistance, channel to ambient	R _{th (ch-a)}	125	°C/W	

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: $V_{DD} = -50$ V, $T_{ch} = 25$ °C (initial), L = 75 mH, $I_{AR} = -2.0$ A, $R_G = 25$ Ω

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Handle with care.

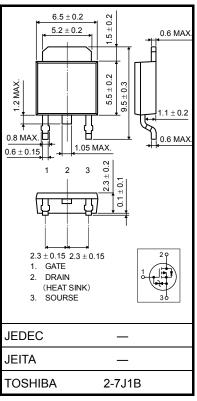


SC-64

2-7B1B

Weight: 0.36 g (typ.)

JEITA TOSHIBA



Weight: 0.36 g (typ.)

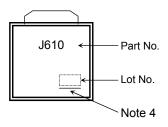
Electrical Characteristics (Ta = 25°C)

Characteristic		Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I _{GSS}	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±10	μА
Drain cutoff current		I _{DSS}	$V_{DS} = -250 \text{ V}, V_{GS} = 0 \text{ V}$	_	_	-100	μА
Drain-source breakdown voltage		V (BR) DSS	$I_D = -10 \text{ mA}, V_{GS} = 0 \text{ V}$	-250	_	_	V
Gate threshold ve	oltage	V _{th}	$V_{DS} = -10 \text{ V}, I_D = -1 \text{ mA}$	-1.5	_	-3.5	V
Drain-source ON-resistance		R _{DS} (ON)	$V_{GS} = -10 \text{ V}, I_D = -1.0 \text{ A}$		1.85	2.55	Ω
Forward transfer admittance		Y _{fs}	$V_{DS} = -10 \text{ V}, I_D = -1.0 \text{ A}$	0.5	1.8	_	S
Input capacitance		C _{iss}	V _{DS} = -10 V, V _{GS} = 0 V, f = 1 MHz		381	_	pF
Reverse transfer capacitance		C _{rss}			52	_	
Output capacitance		C _{oss}]		157	_	
Switching time	Rise time	t _r	$V_{GS} = 1.0 \text{ A} V_{OUT} = 1.0 \text{ A} $		5		
	Turn-on time	t _{on}		_	20		no
	Fall time	t _f		_	6	_	ns
	Turn-off time	t _{off}		_	36		
Total gate charge		Qg	V 200 V V 10 V	_	24	_	nC
Gate-source charge		Q _{gs}	$V_{DD} \approx -200 \text{ V}, V_{GS} = -10 \text{ V},$ $I_{D} = -2.0 \text{ A}$	_	11	_	
Gate-drain charge		Q _{gd}	- 1102.0 A	_	13	_	

Source-Drain Ratings and Characteristics (Ta = 25°C)

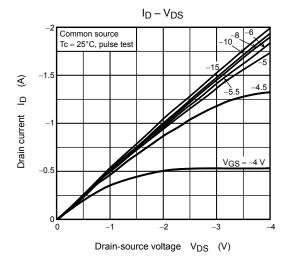
Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I_{DR}	_	_	_	-2.0	Α
Pulse drain reverse current (Note 1)	I _{DRP}	_	_	_	-4.0	Α
Forward voltage (diode)	V_{DSF}	$I_{DR} = -2.0 \text{ A}, V_{GS} = 0 \text{ V}$	_	_	2.0	V
Reverse recovery time	t _{rr}	$I_{DR} = -2.0 \text{ A}, V_{GS} = 0 \text{ V},$	_	120	_	ns
Reverse recovery charge	Q _{rr}	dl _{DR} /dt = 100 A/μs	_	540	_	nC

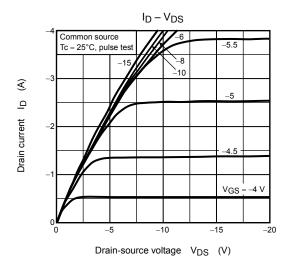
Marking

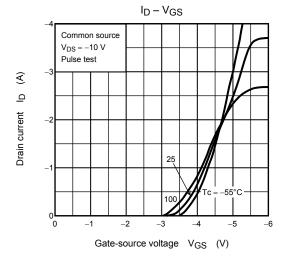


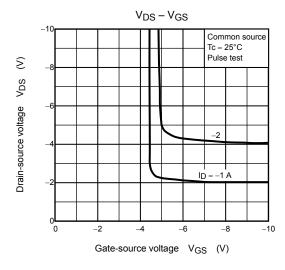
Note 4 : A line under a Lot No. identifies the indication of product Labels [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

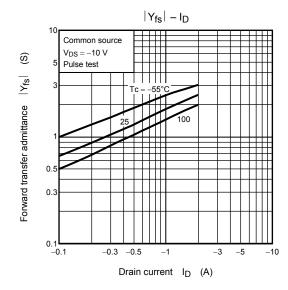
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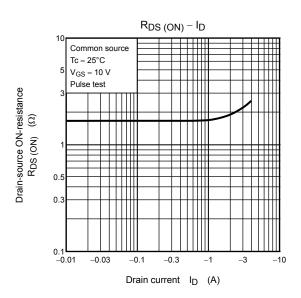


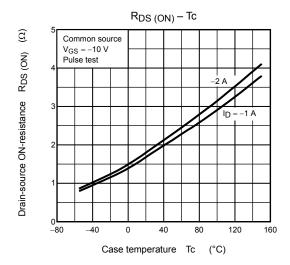


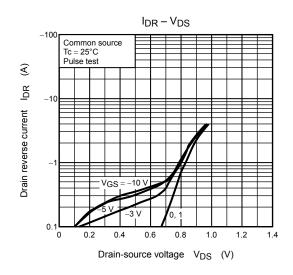


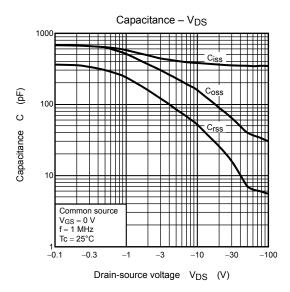


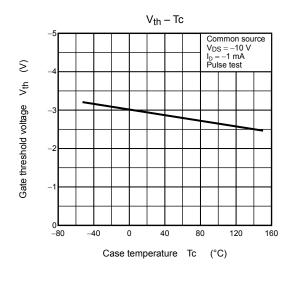


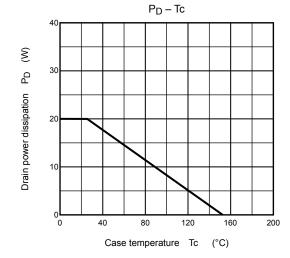


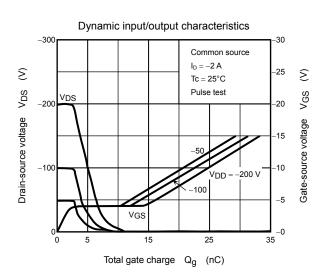


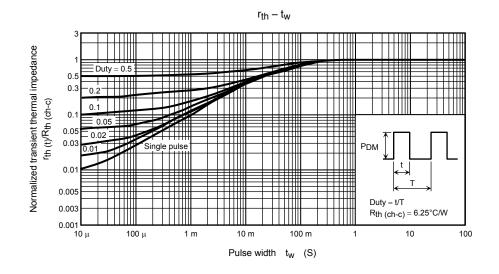


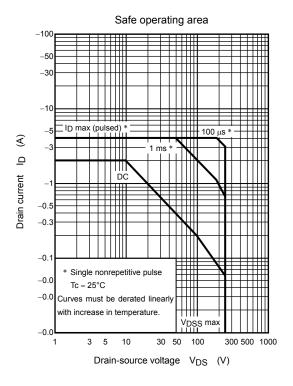


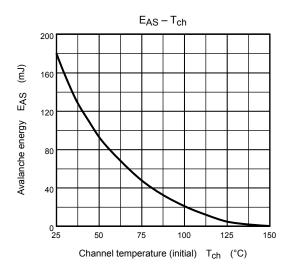


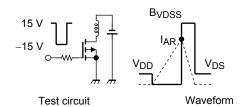












$$\begin{aligned} R_G &= 25~\Omega \\ V_{DD} &= -50~V,~L = 75~mH \end{aligned} \qquad EAS &= \frac{1}{2} \cdot L \cdot I^2 \cdot \left(\frac{BVDSS}{BVDSS} - V_{DD} \right) \end{aligned}$$

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