

Features

- Low On-Resistance
 - $25m\Omega @ V_{GS} = 4.5V$
 - 29mΩ @ V_{GS} = 2.5V •
 - $37m\Omega @ V_{GS} = 1.8V$ •
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Lead Free By Design/RoHS Compliant (Note 1)
- "Green" Device (Note 2)
- Qualified to AEC-Q101 Standards for High Reliability



DMG3414U

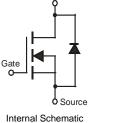
N-CHANNEL ENHANCEMENT MODE MOSFET

Mechanical Data

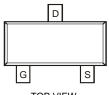
- Case: SOT-23
- Case Material: Molded Plastic, "Green" Molding Compound. • UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020D
- Terminals: Finish Matte Tin annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208
- Terminals Connections: See Diagram Below
- Marking Information: See Page 4
- Ordering Information: See Page 4
- Weight: 0.008 grams (approximate)







Drain



TOP VIEW

Maximum Ratings $@T_A = 25^{\circ}C$ unless otherwise specified

Charact	eristic		Symbol	Value	Units	
Drain-Source Voltage			V _{DSS}	20	V	
Gate-Source Voltage			V _{GSS}	±8	V	
Continuous Drain Current (Note 3)	Steady State	T _A = 25°C T _A = 70°C	Ι _D	4.2 3.2	A	
Pulsed Drain Current (Note 4)			I _{DM}	30	А	

Thermal Characteristics

Characteristic	Symbol	Value	Unit	
Power Dissipation (Note 3)	PD	0.78	W	
Thermal Resistance, Junction to Ambient @T _A = 25°C	R _{θJA}	162	°C/W	
Operating and Storage Temperature Range	TJ, TSTG	-55 to +150	°C	

Notes: 1. No purposefully added lead.

2. Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead_free/index.php.

3. Device mounted on FR-4 PCB with 2oz. Copper and test pulse width t \leq 10s.

4. Repetitive rating, pulse width limited by junction temperature.



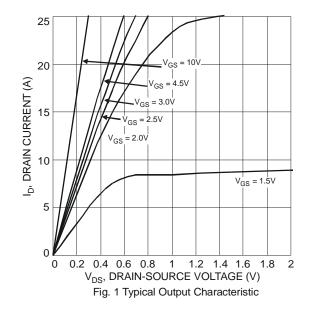
Electrical Characteristics @T_A = 25°C unless otherwise specified

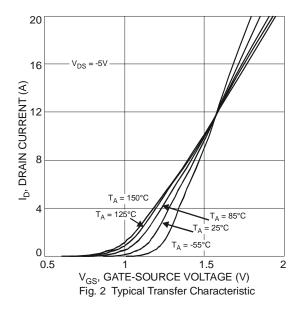
			_					
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition		
OFF CHARACTERISTICS (Note 5)								
Drain-Source Breakdown Voltage	BV _{DSS}	20			V	$V_{GS} = 0V, I_D = 250 \mu A$		
Zero Gate Voltage Drain Current T _J = 25°C	IDSS	_		1.0	μΑ	$V_{DS} = 20V, V_{GS} = 0V$		
Gate-Source Leakage	I _{GSS}	_	—	±100	nA	$V_{GS} = \pm 8V, V_{DS} = 0V$		
ON CHARACTERISTICS (Note 5)								
Gate Threshold Voltage	V _{GS(th)}	0.5		0.9	V	$V_{DS} = V_{GS}$, $I_D = 250 \mu A$		
		_	19	25		$V_{GS} = 4.5V, I_D = 8.2A$		
Static Drain-Source On-Resistance	R _{DS} (ON)		22	29	mΩ	$V_{GS} = 2.5V, I_D = 3.3A$		
			28	37		$V_{GS} = 1.8V, I_D = 2.0A$		
Forward Transfer Admittance	Y _{fs}	_	7	_	S	$V_{DS} = 10V, I_D = 4A$		
DYNAMIC CHARACTERISTICS			_	_	_			
Input Capacitance	Ciss	_	829.9		pF			
Output Capacitance	Coss	_	85.3		pF	V _{DS} = 10V, V _{GS} = 0V f = 1.0MHz		
Reverse Transfer Capacitance	C _{rss}	_	81.2		pF			
Total Gate Charge	Qg	_	9.6	_	nC			
Gate-Source Charge	Q _{gs}	_	1.5	—	nC	$V_{GS} = 4.5V, V_{DS} = 10V, I_D = 8.2A$		
Gate-Drain Charge	Q _{gd}	_	3.5		nC			
Turn-On Delay Time	t _{D(on)}	_	8.1	_	ns			
Turn-On Rise Time	tr	_	8.3	_	ns	$V_{DD} = 10V, V_{GS} = 4.5V,$		
Turn-Off Delay Time	t _{D(off)}	_	40.1		ns	$R_L = 10\Omega$, $R_G = 6\Omega$, $I_D = 1A$		
Turn-Off Fall Time	t _f	_	9.6		ns			

Notes:

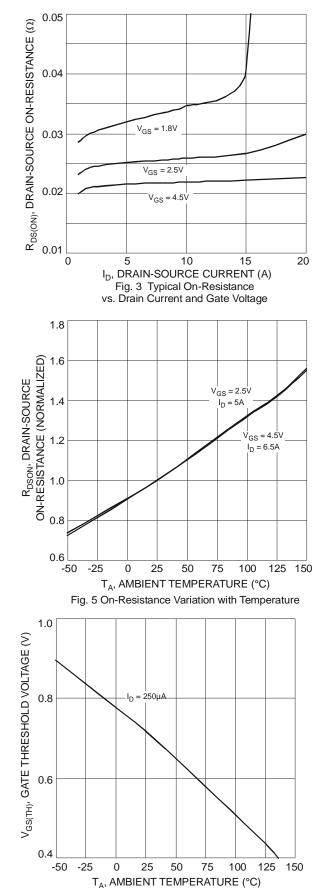
NEW PRODUCT

5. Short duration pulse test used to minimize self-heating effect.

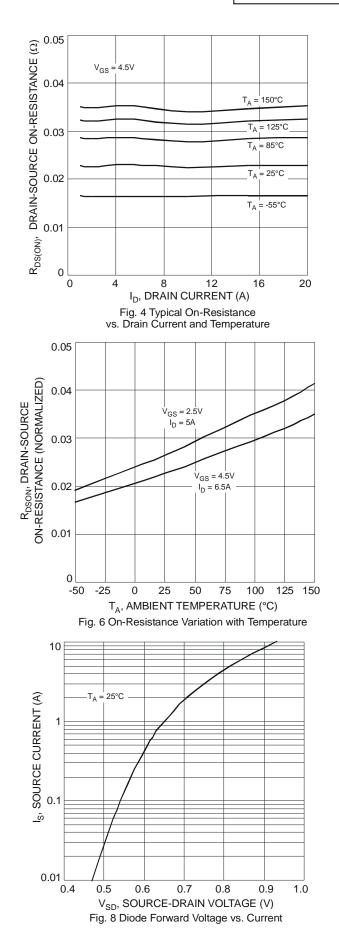




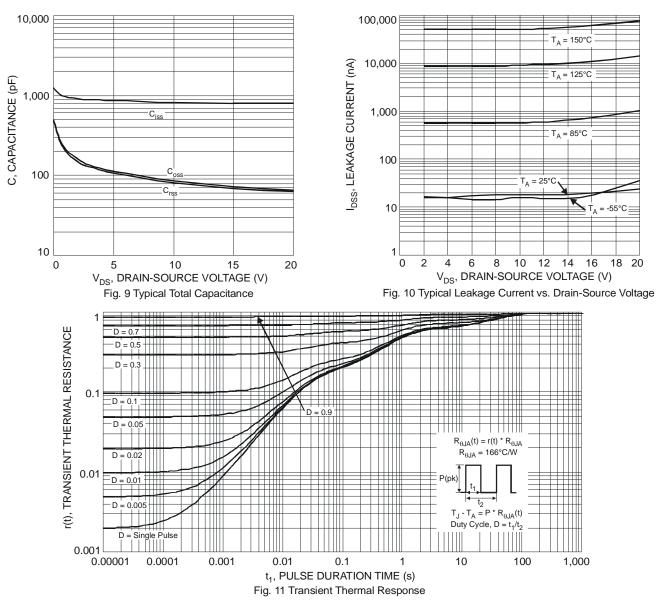












Ordering Information (Note 6)

Part Number	Case	Packaging
DMG3414U-7	SOT-23	3000/Tape & Reel

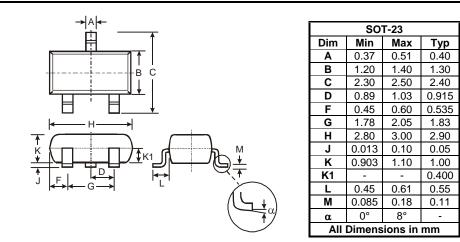
Notes: 6. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

Marking Information

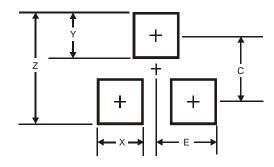
					 N8 ≥ 	YM = Y = Y	Date Code ear (ex: W		-			
Date Code Key		_										
Year	200	9	2010		2011	20	12	2013		2014	2	2015
Code	W		Х		Y		Z	А		В		С
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



Package Outline Dimensions



Suggested Pad Layout



Dimensions	Value (in mm)
Z	2.9
Х	0.8
Y	0.9
С	2.0
E	1.35



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