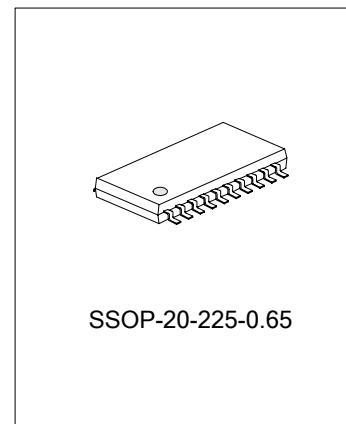


## THREE OR FOUR-CELL Li<sup>+</sup> BATTERY PROTECTION CIRCUIT

### DESCRIPTION

The SA45141 is a three- or four-cell Lithium-Ion Batteries protection circuit. SA45141 protects for Lithium-Ion Batteries. It has functions below: Overcharge detection, Overdischarge detection, Overcurrent detection. When the Lithium-Ion Batteries is in state of overcharge, overdischarge and overcurrent. The IC turns off and on the external MOSFET to protect the batteries.



### FEATURES

- \* Low current consumption
  - Overcharge: 55 $\mu$ A (VCELL=4.4V CON=0V)
  - Normal: 27 $\mu$ A (VCELL=3.5V CON=0V)
  - Overdischarge: 2 $\mu$ A (VCELL=1.8V CON=0V)
  - Overdischarge: 0.1 $\mu$ A Max(VCELL=1.0V CON=0V)
- \* Overcharge detection voltage:  
two types (4.350±0.025V, 4.250±0.025V)
- \* Overcharge detection hysteresis voltage:  
two types (200±60mV, 8±8mV)
- \* Overdischarge detection voltage: three types(2.3±0.1V, 2.6±0.2V , 2.9±0.1V)
- \* Overdischarge release voltage: two types (3.0±0.15V, 3.0±0.20V)
- \* Overcurrent detection voltage: two types(150±15mV,200±15mV)

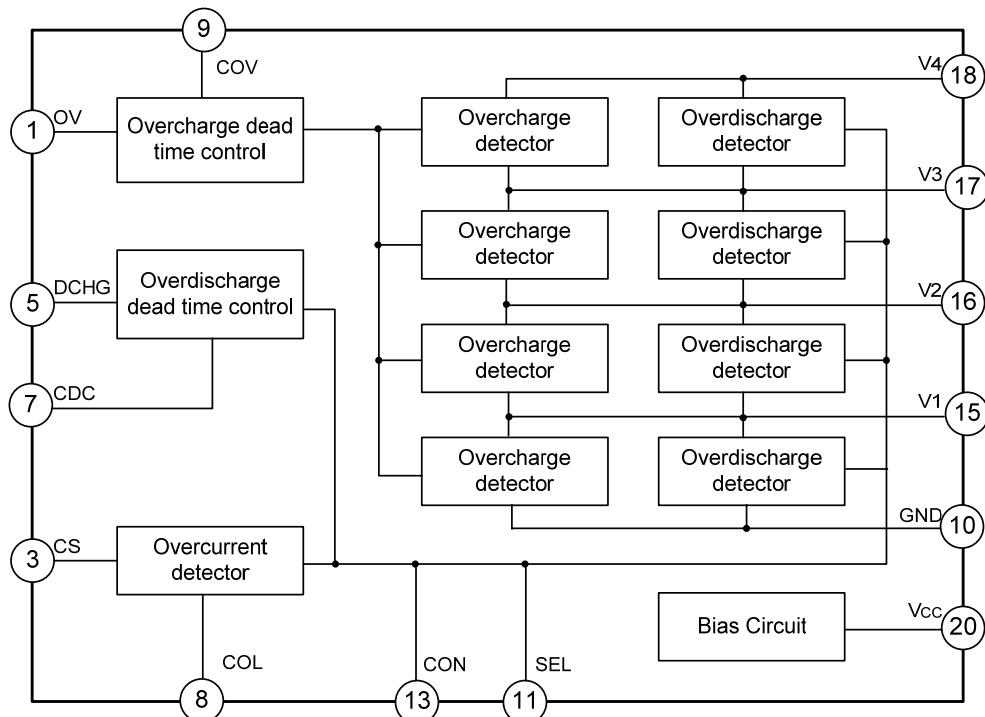
### APPLICATIONS

- \* 3-cell or 4-cell lithium-ion batteries
- \* Notebook PC
- \* Electric bicycle
- \* Electric tools

### ORDERING INFORMATION

Device	Package	Overcharge detect voltage (V)	Overcharge detect hysteresis voltage (mV)	Overdischarge detect voltage (V)	Overdischarge release voltage (V)	Overcurrent detect voltage (mV)
SA45141SC	SSOP-20-225-0.65	4.350±0.025	200±60	2.3±0.1	3.00±0.15	150±15
SA45141SD		4.350±0.025	200±60	2.6±0.2	3.00±0.20	150±15
SA45141SE		4.350±0.025	200±60	2.9±0.1	3.00±0.15	200±15
SA45141SH		4.250±0.025	8±8	2.9±0.1	3.00±0.15	200±15

### BLOCK DIAGRAM



### ABSOLUTE MAXIMUM RATING

Characteristics	Symbol	Ratings		Unit
Supply Voltage	VCC max	-0.3~24		V
OV Pin Voltage	VOV max	-0.3~24		V
SEL Pin Voltage	VSEL max	-0.3~24		V
CON Pin Voltage	VCON max	-0.3~24		V
Allowable Dissipation	Pd	300		mW
Storage Temperature Range	Tstg	-40~125		°C
Operating Temperature Range	Tamb	-20~70		°C

### ELECTRICAL CHARACTERISTICS

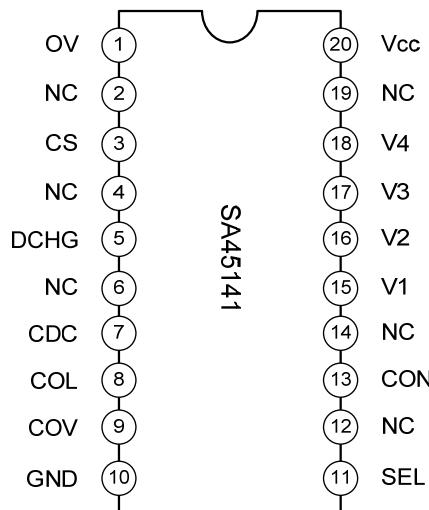
(Unless otherwise stated, Tamb=25°C, VCC=V4+V3+V2+V1, VCELL=3.5, CON=GND, SEL=VCC)

Characteristics	Symbol	Test conditions	Min.	Typ.	Max.	Unit
Overcharge Detection Voltage	VU	Tamb=0°C ~50°C	SA45141SC			V
			SA45141SD	4.325	4.350	
			SA45141SE		4.375	
			SA45141SH	4.225	4.250	

<b>Characteristics</b>	<b>Symbol</b>	<b>Test conditions</b>	<b>Min.</b>	<b>Typ.</b>	<b>Max.</b>	<b>Unit</b>
Overcharge Detection Hysteresis Voltage	$\Delta V_u$	SA45141SC,SA45141SD ,SA45141SE	140	200	260	mV
		SA45141SH	--	8	16	
Overdischarge Detection Voltage	$V_d$	SA45141SC	2.20	2.30	2.40	V
		SA45141SD	2.40	2.60	2.80	
		SA45141SE ,SA45141SH	2.80	2.90	3.00	
Overdischarge Release Voltage	$V_{dF}$	SA45141SC ,SA45141SE ,SA45141SH	2.85	3.00	3.15	V
		SA45141SD	2.80	3.00	3.20	
		SA45141SC	0.45	0.70	0.95	
Overdischarge Detection Hysteresis Voltage	$\Delta V_d$	SA45141SD	0.00	0.40	0.8	V
		SA45141SE,SA45141SH	--	0.10	0.35	
		SA45141SC,SA45141SD	135	150	165	
Overcurrent Detection Voltage	$V_{OC}$	SA45141SE,SA45141SH	185	200	215	mV
			--	20	40	
Overcurrent Detection Hysteresis Voltage	$\Delta V_{OC}$		--			mV
Overcurrent Release				Load release: more than $5M\Omega$		
Consumption Current (Vcc pin) 1	Icc1	$V_{CELL}=4.4V$	--	55	110	$\mu A$
Consumption Current (VCC pin) 2	Icc2	$V_{CELL}=3.5V$	--	27	50	$\mu A$
Consumption Current (VCC pin) 3	Icc3	$V_{CELL}=1.8V$	--	2	4	$\mu A$
Consumption Current(Vcc pin) 4	Icc4	$V_{CELL}=3.5V$ CON= Vcc	--	12	20	$\mu A$
Consumption Current (VCC pin) 5	Icc5	$V_{CELL}=1.8V$ CON=VCC	--	1	2	$\mu A$
Consumption Current (V4 pin) 1	IV41	$V_{CELL}=4.4V$	--	10	20	$\mu A$
Consumption Current (V4 pin) 2	IV42	$V_{CELL}=3.5V$	--	8	15	$\mu A$
Consumption Current (V4 pin) 3	IV43	$V_{CELL}=1.8V$	--	2.5	5.0	$\mu A$
V1 Pin Input Current	IV1	$V_{CELL}=3.5V$	-0.3	0	0.3	$\mu A$
V2 Pin Input Current	IV2	$V_{CELL}=3.5V$	-0.3	0	0.3	$\mu A$
V3 Pin Input Current	IV3	$V_{CELL}=3.5V$	-0.3	0	0.3	$\mu A$
Overcharge Detection dead time	tov	$C_{OV}=0.1\mu F$	0.5	1.0	1.5	s
Overdischarge Detection dead time	tcDCD	$C_{DCD}=0.1\mu F$	0.5	1.0	1.5	s
Overcurrent Detection dead time 1	tCOL1	$C_{OL}=0.001\mu F$	5	10	15	ms
Overcurrent Detection dead time 2	tCOL2	$C_{OL}=0.001\mu F$ , $V_{CC-CS}>1.0V$	--	1.5	3.0	ms
Overcurrent Detection dead time 3	tCOL3	$C_{OL}=0.001\mu F$	5	10	15	ms
DCHG Pin Source Current	IsODCH	$V_{CELL}=1.8V$ , SW:A $V_{DCHG}=V_{CC}-0.8V$	20	--	--	$\mu A$
DCHG Pin Sink Current DCHG	IsIDCH	$V_{CELL}=3.5V$ , SW:A $V_{DCHG}=0.8V$	20	--	--	$\mu A$
DCHG Pin Output Voltage(High)	VTHDCH	$V_{CC}-V_{DCHG}$ , $I_{SO}=20\mu A$ SW: B	--	--	0.8	V
DCHG Pin Output Voltage(Low)	VTHDCL	$V_{DCHG}-GND$ , $I_{SI}=-20\mu A$ SW: B	--	--	0.8	V

Characteristics	Symbol	Test conditions	Min.	Typ.	Max.	Unit
OV Pin Sink Current	IsIOV	Vov=0.4V	100	--	--	µA
OV Pin Leak Current	ILKOV	Vov=24V	--	--	0.1	µA
CON Pin Low Voltage	VCONL	DCHG="HIGH"	--	--	0.4	V
CON Pin High Voltage	VCONH	DCHG= "LOW"	VCC-0.4	--	--	V
CON Pin Current	ICON	VCELL=3.5V, CON=0.4V	--	1	2	µA
SEL Pin Low Voltage	VSELL	3 cells	--	--	0.4	V
SEL Pin High Voltage	VSELH	4 cells	VCC-0.4	--	--	V
SEL Pin Current	ISEL	VCELL=3.5V, SEL=0.4V	--	1	2	µA

### PIN CONFIGURATIONS



### PIN DESCRIPTION

Pin no.	Pin name	I/O	Description
1	OV	O	Overcharge detection output pin
2	NC	--	Not connected
3	CS	I	Overcurrent detection input pin
4	NC	--	Not connected
5	DCHG	O	Discharge control FET drive output pin Normal state: DCHG=Low; Overdischarge state: DCHG=High
6	NC	--	Not connected
7	CDC	--	Overdischarge detection dead time setting pin
8	COL	--	Overcurrent detection dead time setting pin
9	COV	--	Overcharge detection dead time setting pin
10	GND	--	Ground
11	SEL	I	3 or 4 cell select switch pin SEL=V1 or GND:3 cell ; SEL=Vcc:4 cell
12	NC	--	Not connected

<b>Pin no.</b>	<b>Pin name</b>	<b>I/O</b>	<b>Description</b>
13	CON	I	Discharge FET ON/OFF pin CON=low: DCHG=low, normal ; CON=high: DCHG=high, discharge prohibited
14	NC	--	Not connected
15	V1	I	V1 cell positive electrode side ,V2 cell negative electrode side
16	V2	I	V2 cell positive electrode side ,V3 cell negative electrode side
17	V3	I	V3 cell positive electrode side ,V4 cell negative electrode side
18	V4	I	V4cell positive electrode side
19	NC	--	Not connected
20	Vcc	--	Power supply input pin

## FUNCTION DESCRIPTION

This IC is a lithium-ion batteries (3-cell or 4-cell) protection IC. It has functions below: Overcharge detection, Overdischarge detection, Overcurrent detection. It controls the batteries charging or discharging by control the external MOSFET off and on.

### 1. Overcharge mode

The battery voltage of either 3 cell batteries or 4 cell batteries goes above overcharge detection voltage. The IC shuts down the charging loop. Dead time can be set by connecting a capacitor.

### 2. Normal mode

When the battery voltage of both 3 cell batteries and 4 cell batteries are above overdischarge detection voltage and below overcharge detection voltage, it can realize the normal charging and discharging function.

### 3. Overdischarge mode

The battery voltage of either 3 cell batteries or 4 cell batteries drops below overdischarge detection voltage. The IC shuts down the discharging loop. Dead time of overdischarge detection can be set by connecting a capacitor.

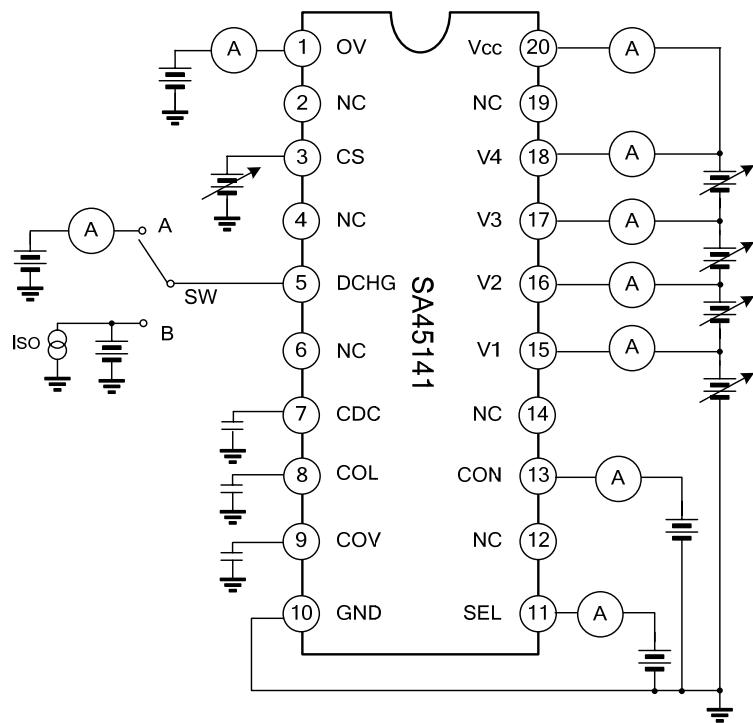
### 4. Overcurrent mode

The voltage between CS-Vcc goes above overcurrent detection voltage during discharge. The IC shuts down the discharging loop.

### 5. Discharge FET ON/OFF pin CON

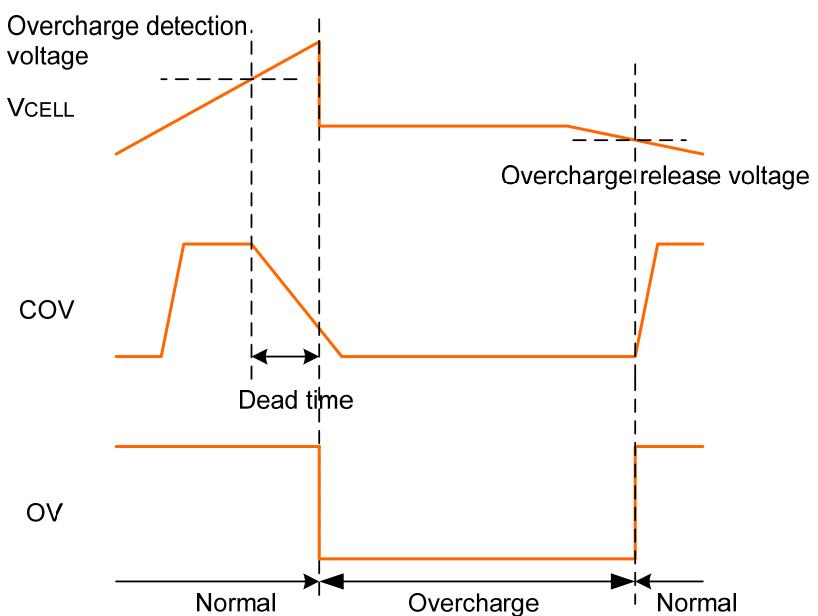
CON=low: DCHG=low, normal; CON=high: DCHG=high, discharge prohibited.

## TEST CIRCUIT

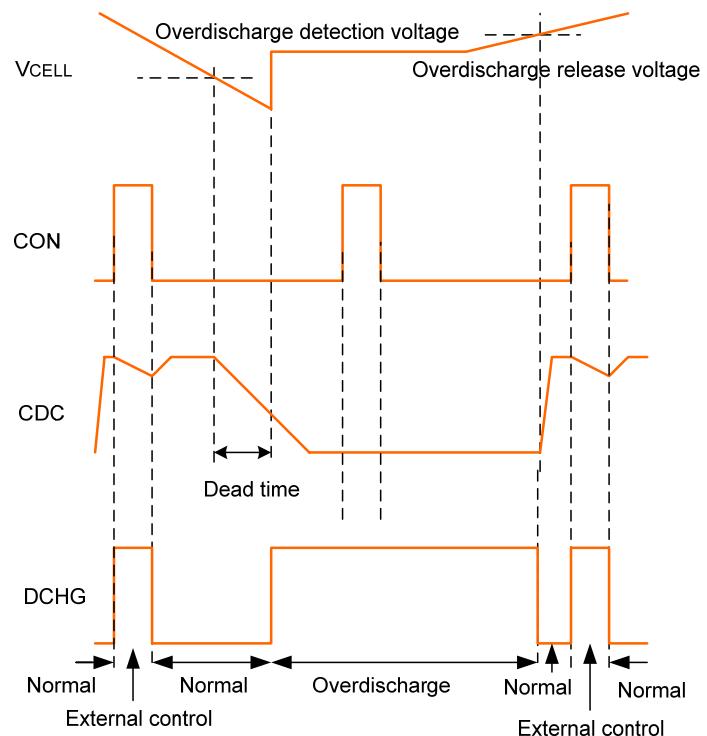


## TIMING CHART

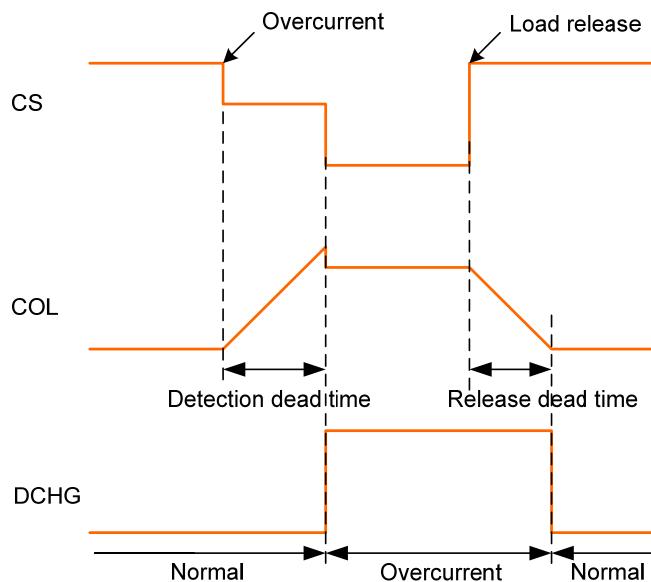
### Overcharge mode



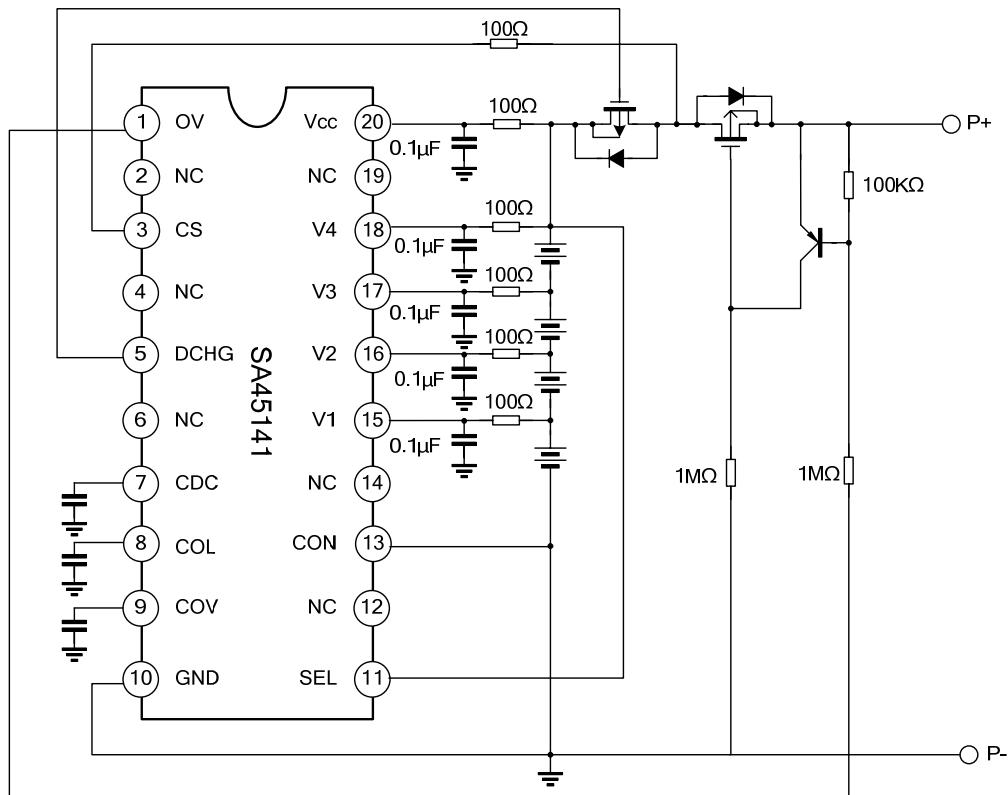
### Overdischarge mode



### Overcurrent mode



TYPICAL APPLICATION CIRCUIT

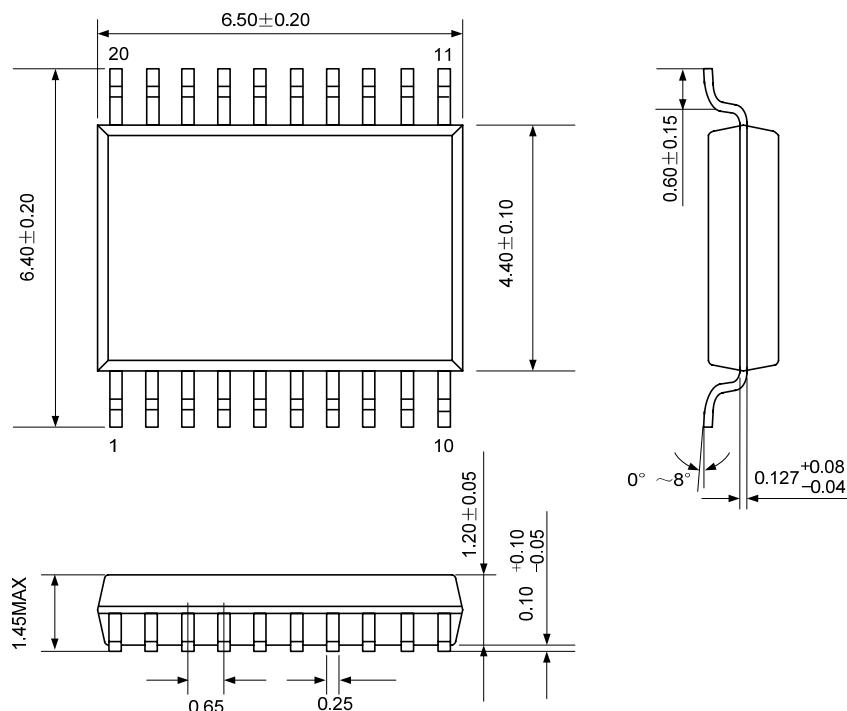


Note: The circuit and parameters are reference only, please set the parameters of the real application circuit based on the real test .

## PACKAGE OUTLINE

**SSOP-20-225-0.65**

**Unit: mm**



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