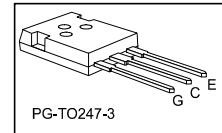
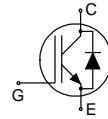


## Reverse conducting IGBT with monolithic body diode

### Features:

- Powerful monolithic body diode with low forward voltage designed for soft commutation only
- TrenchStop® technology applications offers:
  - very tight parameter distribution
  - high ruggedness, temperature stable behavior
  - low  $V_{CEsat}$
  - easy parallel switching capability due to positive temperature coefficient in  $V_{CEsat}$
- Low EMI
- Qualified according to JEDEC J-STD-020 and JESD-022 for target applications
- Pb-free lead plating; RoHS compliant
- Complete product spectrum and PSpice Models:  
<http://www.infineon.com/igbt/>



### Applications:

- Inductive cooking

Type	$V_{CE}$	$I_C$	$V_{CEsat}, T_{vj}=25^\circ\text{C}$	$T_{vjmax}$	Marking	Package
IHW15N120R3	1200V	15A	1.48V	175°C	H15N120R3	PG-T0247-3

### Maximum ratings

Parameter	Symbol	Value	Unit
Collector-emitter voltage	$V_{CE}$	1200	V
DC collector current, limited by $T_{vjmax}$ $T_C = 25^\circ\text{C}$ $T_C = 100^\circ\text{C}$	$I_C$	30.0 15.0	A
Pulsed collector current, $t_p$ limited by $T_{vjmax}$	$I_{Cpuls}$	45.0	A
Turn off safe operating area $V_{CE} \leq 1200\text{V}$ , $T_{vj} \leq 175^\circ\text{C}$	-	45.0	A
Diode forward current, limited by $T_{vjmax}$ $T_C = 25^\circ\text{C}$ $T_C = 100^\circ\text{C}$	$I_F$	30.0 15.0	A
Diode pulsed current, $t_p$ limited by $T_{vjmax}$	$I_{Fpuls}$	45.0	A
Gate-emitter voltage Transient Gate-emitter voltage ( $t_p = 10\mu\text{s}$ , $D < 0.010$ )	$V_{GE}$	$\pm 20$ $\pm 25$	V
Power dissipation $T_C = 25^\circ\text{C}$ Power dissipation $T_C = 100^\circ\text{C}$	$P_{tot}$	254.0 127.0	W
Operating junction temperature	$T_{vj}$	-40...+175	°C
Storage temperature	$T_{stg}$	-55...+175	°C
Soldering temperature, wavesoldering 1.6 mm (0.063 in.) from case for 10s		260	°C
Mounting torque, M3 screw Maximum of mounting processes: 3	$M$	0.6	Nm

## Thermal Resistance

Parameter	Symbol	Conditions	Max. Value	Unit
<b>Characteristic</b>				
IGBT thermal resistance, junction - case	$R_{th(j-c)}$		0.59	K/W
Diode thermal resistance, junction - case	$R_{th(j-c)}$		0.59	K/W
Thermal resistance junction - ambient	$R_{th(j-a)}$		40	K/W

Electrical Characteristic, at  $T_{vj} = 25^\circ\text{C}$ , unless otherwise specified

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
<b>Static Characteristic</b>						
Collector-emitter breakdown voltage	$V_{(BR)CES}$	$V_{GE} = 0V, I_c = 0.50\text{mA}$	1200	-	-	V
Collector-emitter saturation voltage	$V_{CEsat}$	$V_{GE} = 15.0\text{V}, I_c = 15.0\text{A}$ $T_{vj} = 25^\circ\text{C}$ $T_{vj} = 125^\circ\text{C}$ $T_{vj} = 175^\circ\text{C}$	-	1.48	1.70	V
Diode forward voltage	$V_F$	$V_{GE} = 0\text{V}, I_F = 15.0\text{A}$ $T_{vj} = 25^\circ\text{C}$ $T_{vj} = 125^\circ\text{C}$ $T_{vj} = 175^\circ\text{C}$	-	1.55	1.75	V
Gate-emitter threshold voltage	$V_{GE(th)}$	$I_c = 0.40\text{mA}, V_{CE} = V_{GE}$	5.1	5.8	6.4	V
Zero gate voltage collector current	$I_{CES}$	$V_{CE} = 1200\text{V}, V_{GE} = 0\text{V}$ $T_{vj} = 25^\circ\text{C}$ $T_{vj} = 175^\circ\text{C}$	-	-	100.0 2500.0	$\mu\text{A}$
Gate-emitter leakage current	$I_{GES}$	$V_{CE} = 0\text{V}, V_{GE} = 20\text{V}$	-	-	100	nA
Transconductance	$g_{fs}$	$V_{CE} = 20\text{V}, I_c = 15.0\text{A}$	-	13.9	-	S
Integrated gate resistor	$r_G$			none		$\Omega$

Electrical Characteristic, at  $T_{vj} = 25^\circ\text{C}$ , unless otherwise specified

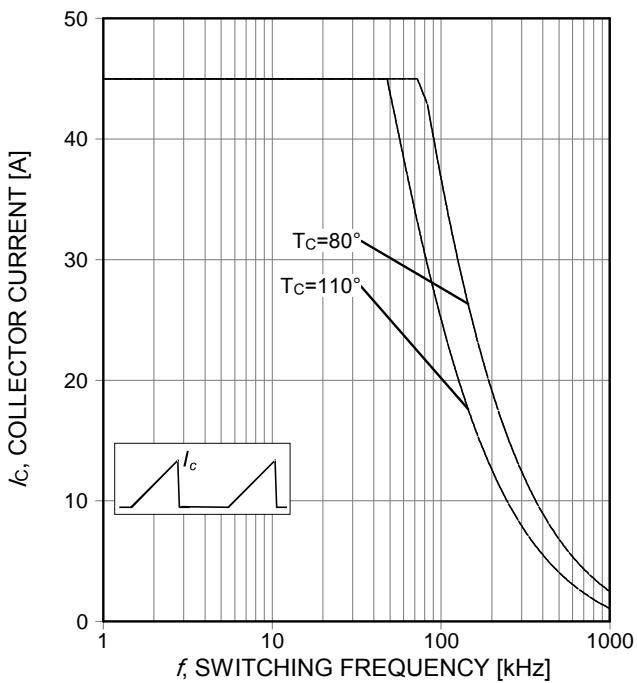
Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
<b>Dynamic Characteristic</b>						
Input capacitance	$C_{ies}$		-	1165	-	pF
Output capacitance	$C_{oes}$	$V_{CE} = 25\text{V}, V_{GE} = 0\text{V}, f = 1\text{MHz}$	-	40	-	
Reverse transfer capacitance	$C_{res}$		-	32	-	
Gate charge	$Q_G$	$V_{CC} = 960\text{V}, I_c = 15.0\text{A}, V_{GE} = 15\text{V}$	-	165.0	-	nC
Internal emitter inductance measured 5mm (0.197 in.) from case	$L_E$		-	13.0	-	nH

**Switching Characteristic, Inductive Load, at  $T_{vj} = 25^\circ\text{C}$** 

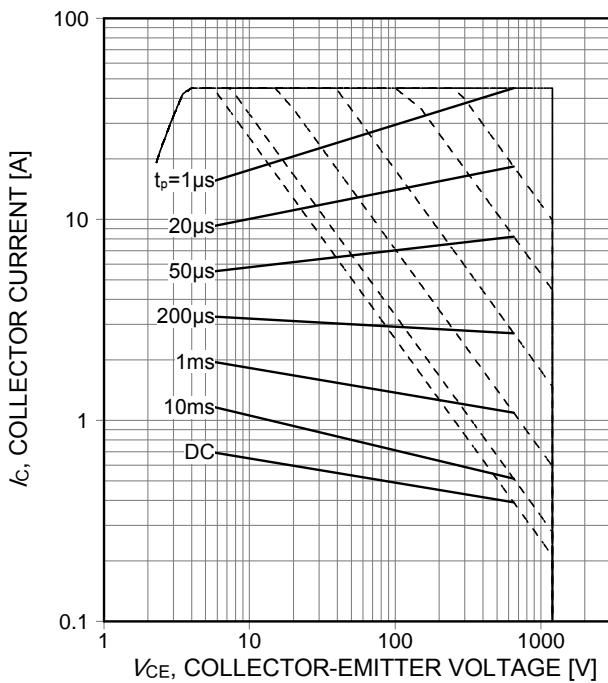
Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
<b>IGBT Characteristic</b>						
Turn-off delay time	$t_{d(\text{off})}$	$T_{vj} = 25^\circ\text{C}$ , $V_{CC} = 600\text{V}$ , $I_C = 15.0\text{A}$ , $V_{GE} = 0.0/15.0\text{V}$ ,	-	300	-	ns
Fall time	$t_f$	$r_G = 14.6\Omega$ , $L_\sigma = 180\text{nH}$ , $C_\sigma = 39\text{pF}$	-	46	-	ns
Turn-off energy	$E_{\text{off}}$	$L_\sigma$ , $C_\sigma$ from Fig. E Energy losses include "tail" and diode reverse recovery.	-	0.70	-	mJ

**Switching Characteristic, Inductive Load, at  $T_{vj} = 175^\circ\text{C}$** 

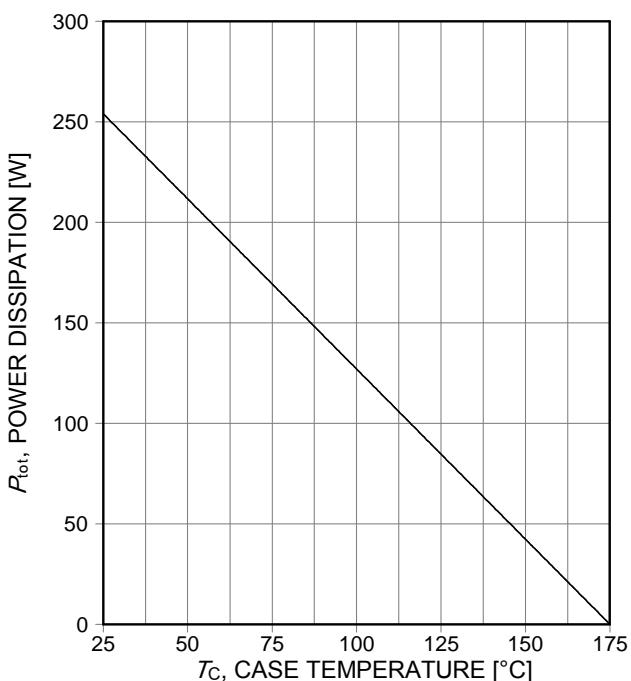
Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
<b>IGBT Characteristic</b>						
Turn-off delay time	$t_{d(\text{off})}$	$T_{vj} = 175^\circ\text{C}$ , $V_{CC} = 600\text{V}$ , $I_C = 15.0\text{A}$ , $V_{GE} = 0.0/15.0\text{V}$ ,	-	370	-	ns
Fall time	$t_f$	$r_G = 14.6\Omega$ , $L_\sigma = 180\text{nH}$ , $C_\sigma = 39\text{pF}$	-	90	-	ns
Turn-off energy	$E_{\text{off}}$	$L_\sigma$ , $C_\sigma$ from Fig. E Energy losses include "tail" and diode reverse recovery.	-	1.25	-	mJ



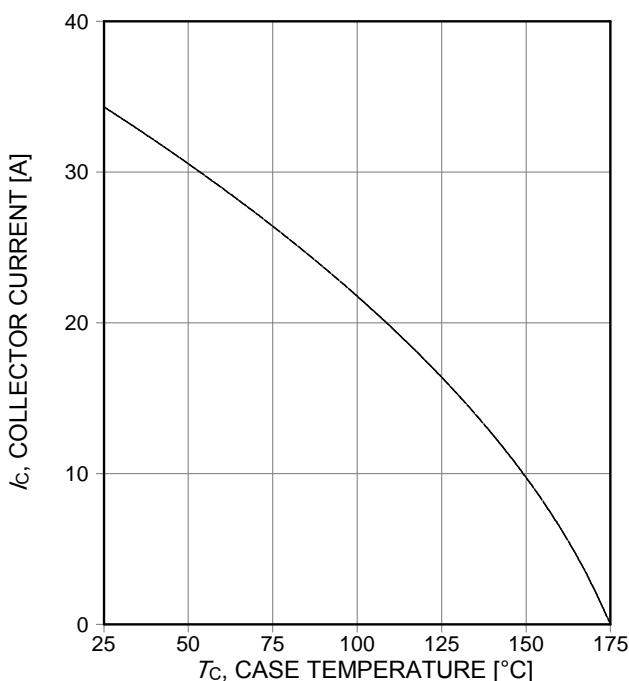
**Figure 1. Collector current as a function of switching frequency**  
 $(T_j \leq 175^\circ\text{C}, D=0.5, V_{CE}=600\text{V}, V_{GE}=15/0\text{V}, R_G=14,6\Omega)$



**Figure 2. Forward bias safe operating area**  
 $(D=0, T_c=25^\circ\text{C}, T_j \leq 175^\circ\text{C}; V_{GE}=15\text{V})$

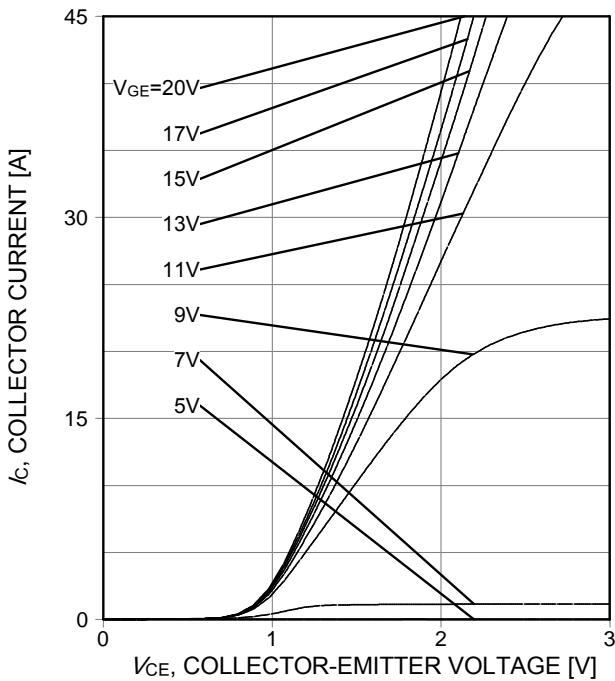


**Figure 3. Power dissipation as a function of case temperature**  
 $(T_j \leq 175^\circ\text{C})$

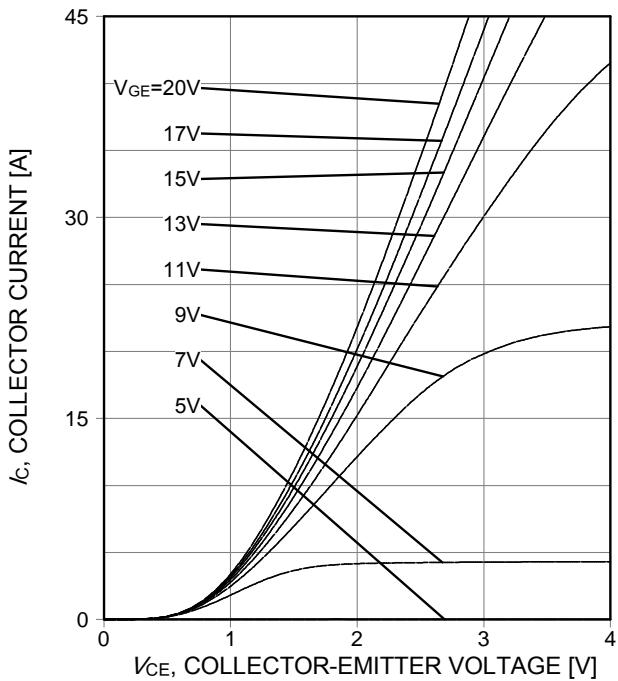


**Figure 4. Collector current as a function of case temperature**  
 $(V_{GE} \geq 15\text{V}, T_j \leq 175^\circ\text{C})$

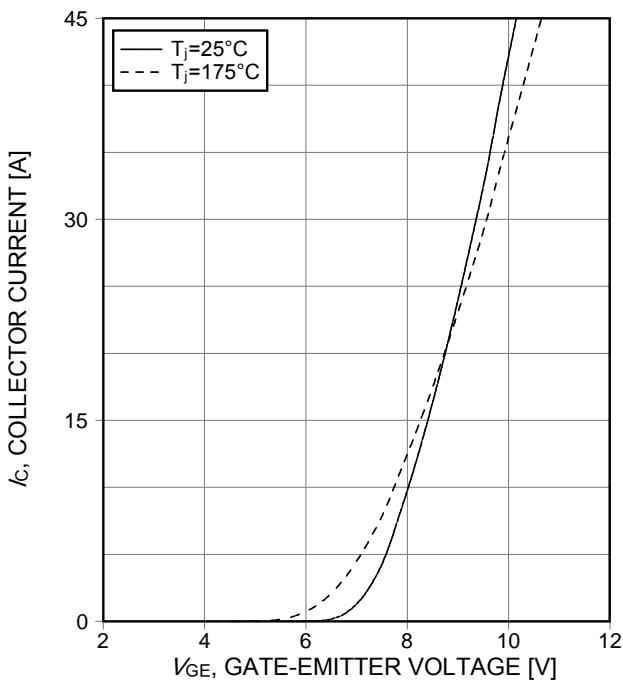
IIH-series



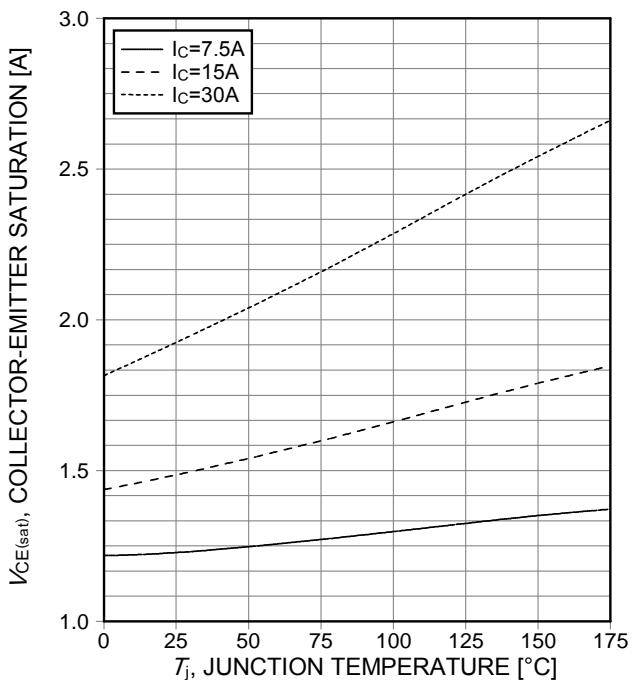
**Figure 5. Typical output characteristic**  
( $T_j=25^\circ\text{C}$ )



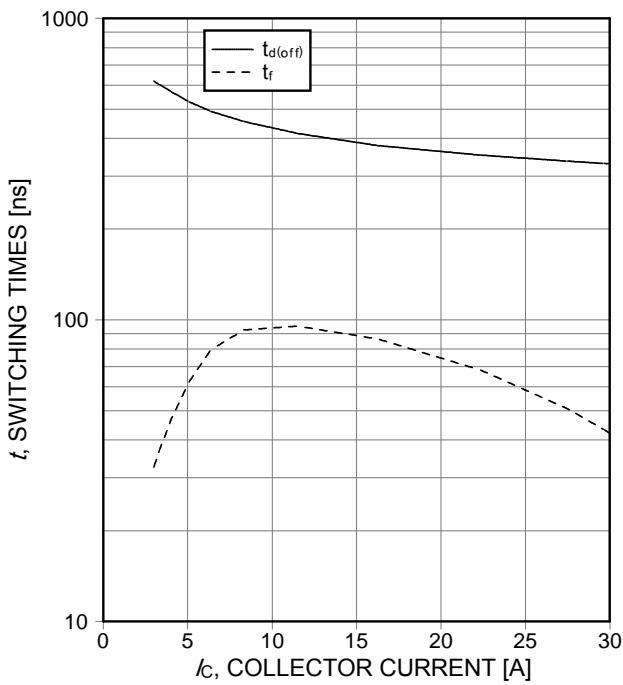
**Figure 6. Typical output characteristic**  
( $T_j=175^\circ\text{C}$ )



**Figure 7. Typical transfer characteristic**  
( $V_{CE}=20\text{V}$ )

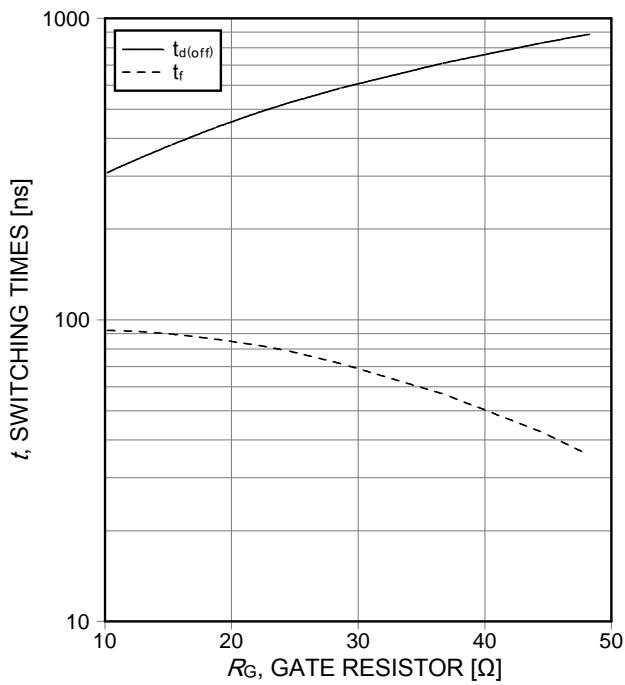


**Figure 8. Typical collector-emitter saturation voltage as a function of junction temperature**  
( $V_{GE}=15\text{V}$ )



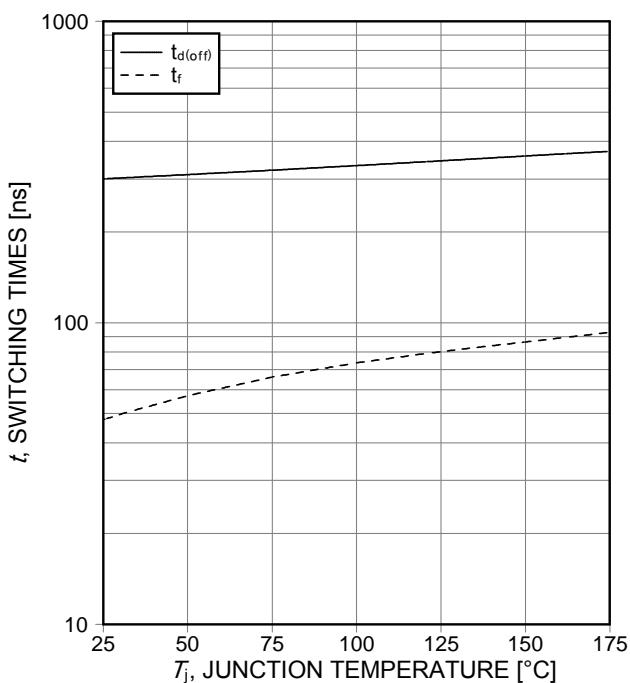
**Figure 9. Typical switching times as a function of collector current**

(ind. load,  $T_j=175^\circ\text{C}$ ,  $V_{CE}=600\text{V}$ ,  $V_{GE}=15/0\text{V}$ ,  $R_G=14,6\Omega$ , test circuit in Fig. E)



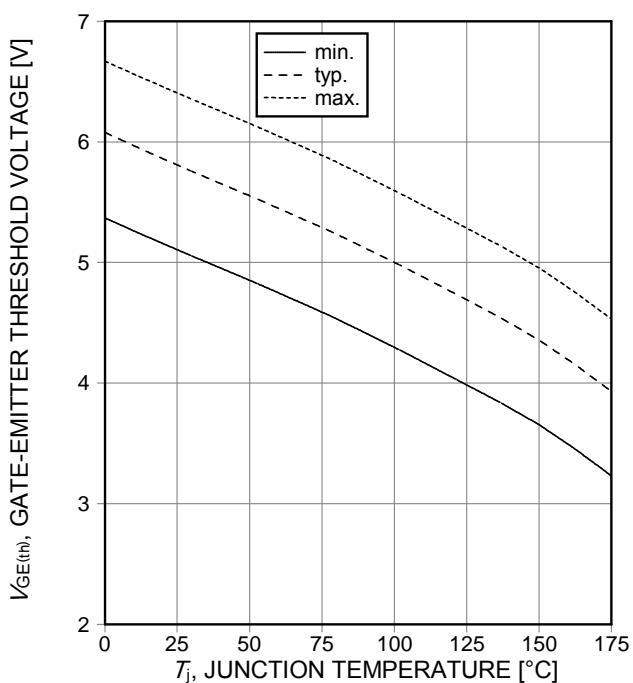
**Figure 10. Typical switching times as a function of gate resistor**

(ind. load,  $T_j=175^\circ\text{C}$ ,  $V_{CE}=600\text{V}$ ,  $V_{GE}=15/0\text{V}$ ,  $I_c=15\text{A}$ , test circuit in Fig. E)



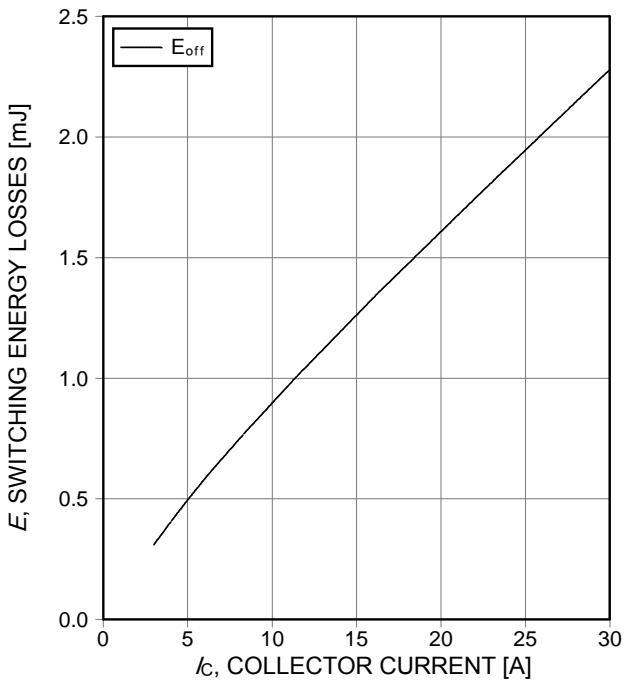
**Figure 11. Typical switching times as a function of junction temperature**

(ind. load,  $V_{CE}=600\text{V}$ ,  $V_{GE}=15/0\text{V}$ ,  $I_c=15\text{A}$ ,  $R_G=14,6\Omega$ , test circuit in Fig. E)

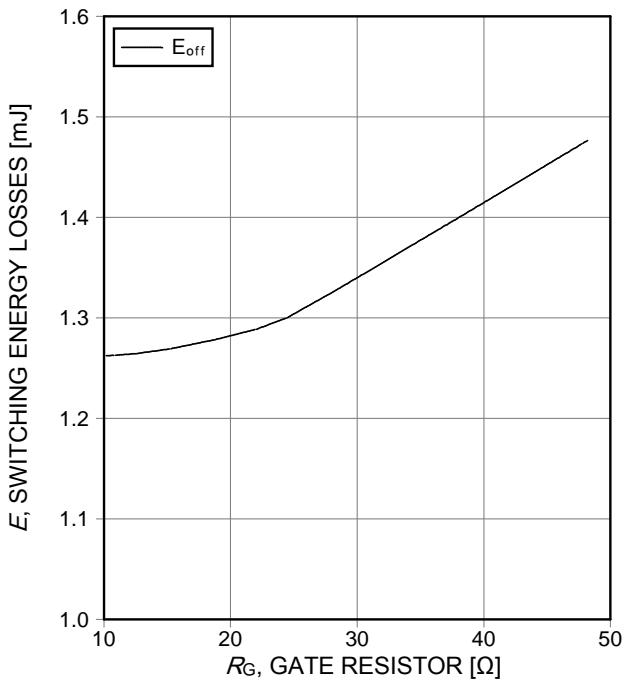


**Figure 12. Gate-emitter threshold voltage as a function of junction temperature**

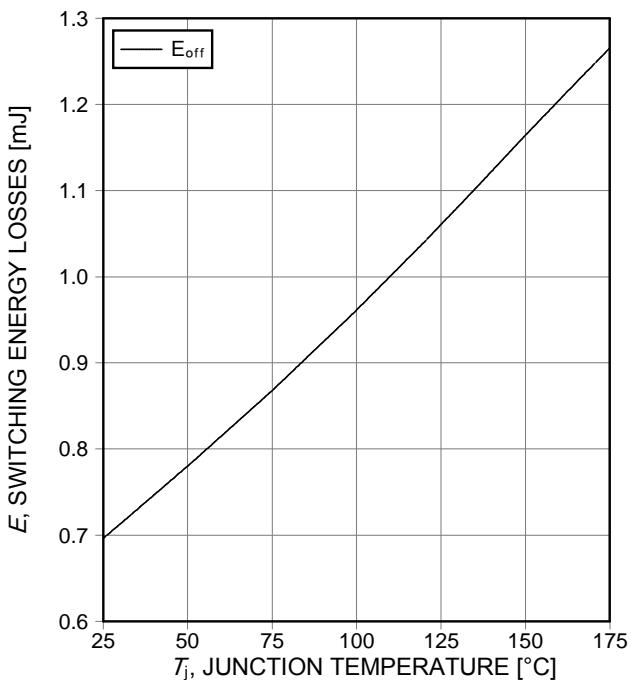
( $I_c=0.4\text{mA}$ )



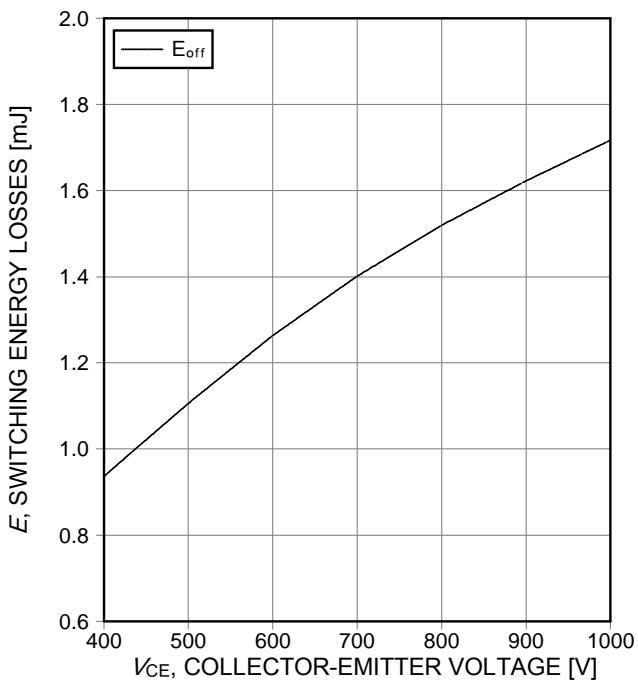
**Figure 13. Typical switching energy losses as a function of collector current**  
(ind. load,  $T_j=175^\circ\text{C}$ ,  $V_{CE}=600\text{V}$ ,  
 $V_{GE}=15/0\text{V}$ ,  $R_G=14,6\Omega$ , test circuit in Fig. E)



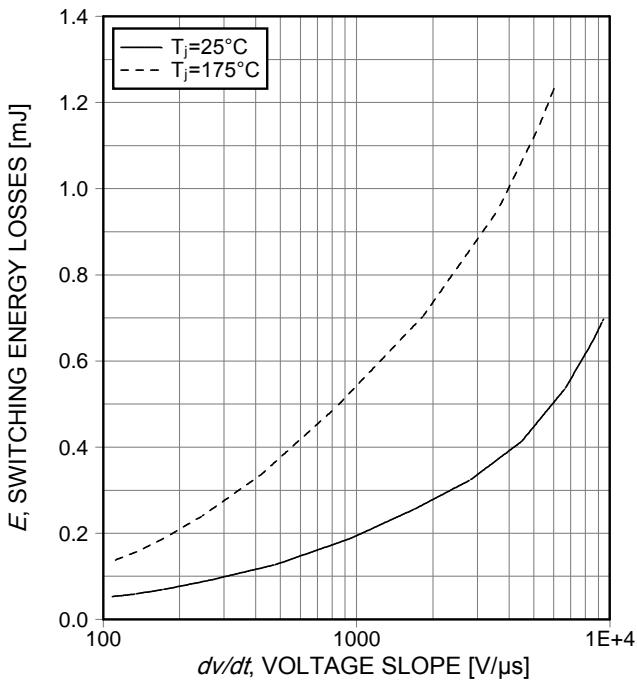
**Figure 14. Typical switching energy losses as a function of gate resistor**  
(ind. load,  $T_j=175^\circ\text{C}$ ,  $V_{CE}=600\text{V}$ ,  
 $V_{GE}=15/0\text{V}$ ,  $I_C=15\text{A}$ , test circuit in Fig. E)



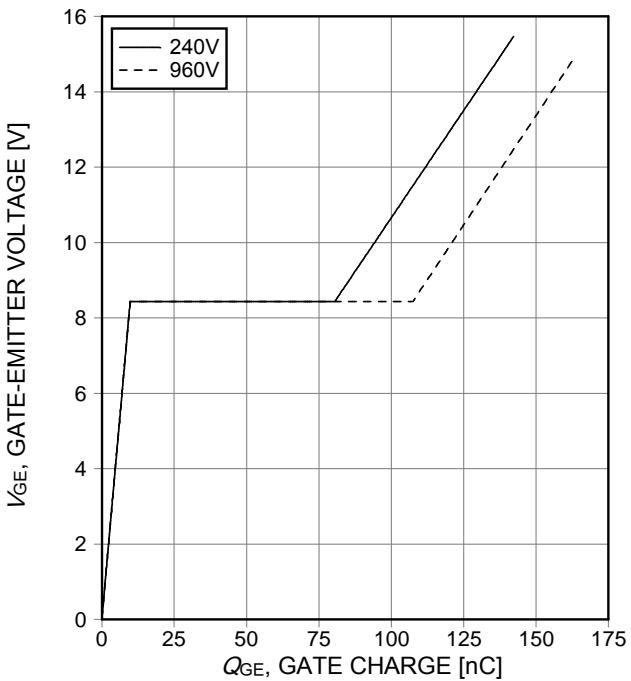
**Figure 15. Typical switching energy losses as a function of junction temperature**  
(ind. load,  $V_{CE}=600\text{V}$ ,  $V_{GE}=15/0\text{V}$ ,  $I_C=15\text{A}$ ,  
 $R_G=14,6\Omega$ , test circuit in Fig. E)



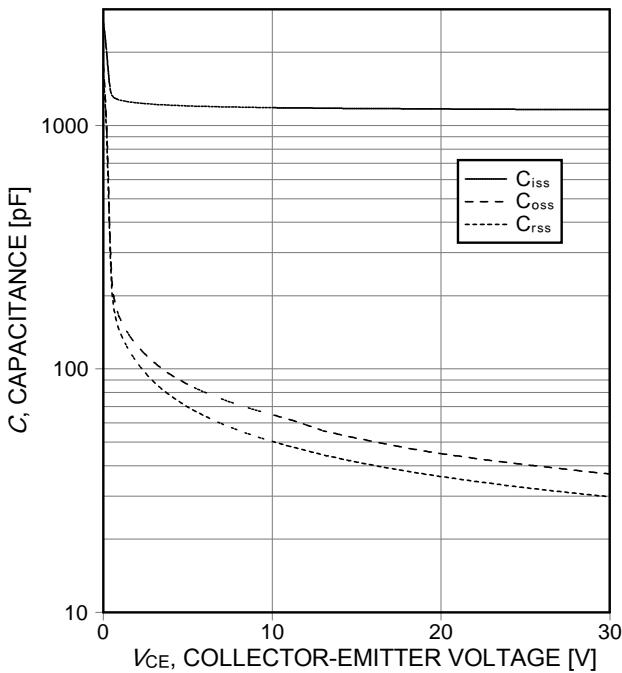
**Figure 16. Typical switching energy losses as a function of collector-emitter voltage**  
(ind. load,  $T_j=175^\circ\text{C}$ ,  $V_{GE}=15/0\text{V}$ ,  $I_C=15\text{A}$ ,  
 $R_G=14,6\Omega$ , test circuit in Fig. E)



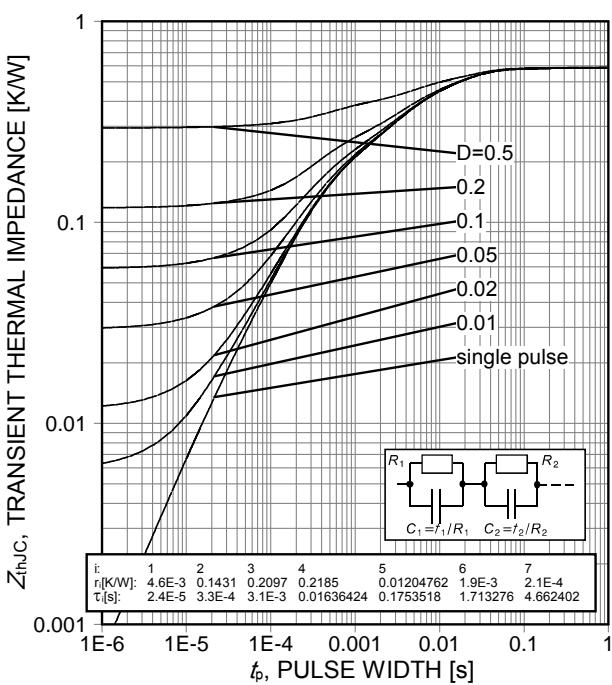
**Figure 17. Typical turn off switching energy loss for soft switching**  
(ind load,  $V_{CE}=600V$ ,  $V_{GE}=15/0V$ ,  $I_c=15A$ ,  $R_G=14.6\Omega$ , test circuit in Fig. E)



**Figure 18. Typical gate charge**  
( $I_c=15A$ )



**Figure 19. Typical capacitance as a function of collector-emitter voltage**  
( $V_{GE}=0V$ ,  $f=1MHz$ )



**Figure 20. IGBT transient thermal impedance**  
( $D=t_p/T$ )

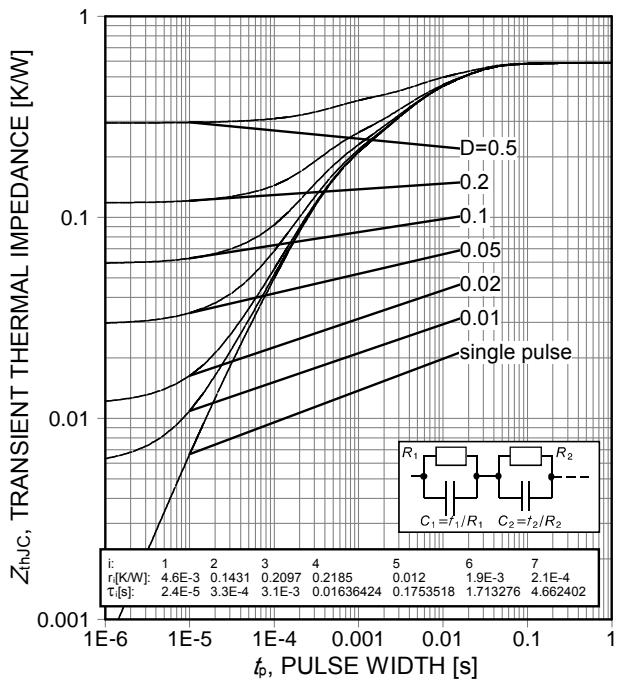


Figure 21. Diode transient thermal impedance as a function of pulse width ( $D=t_p/T$ )

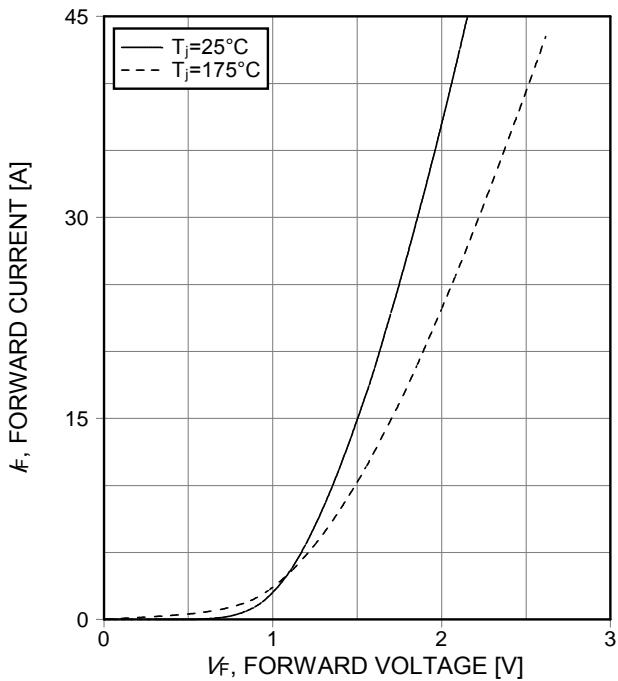


Figure 22. Typical diode forward current as a function of forward voltage

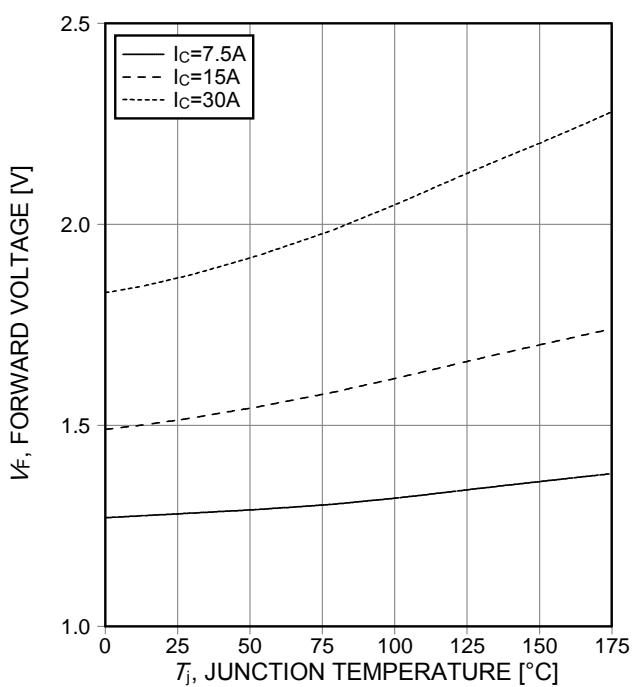
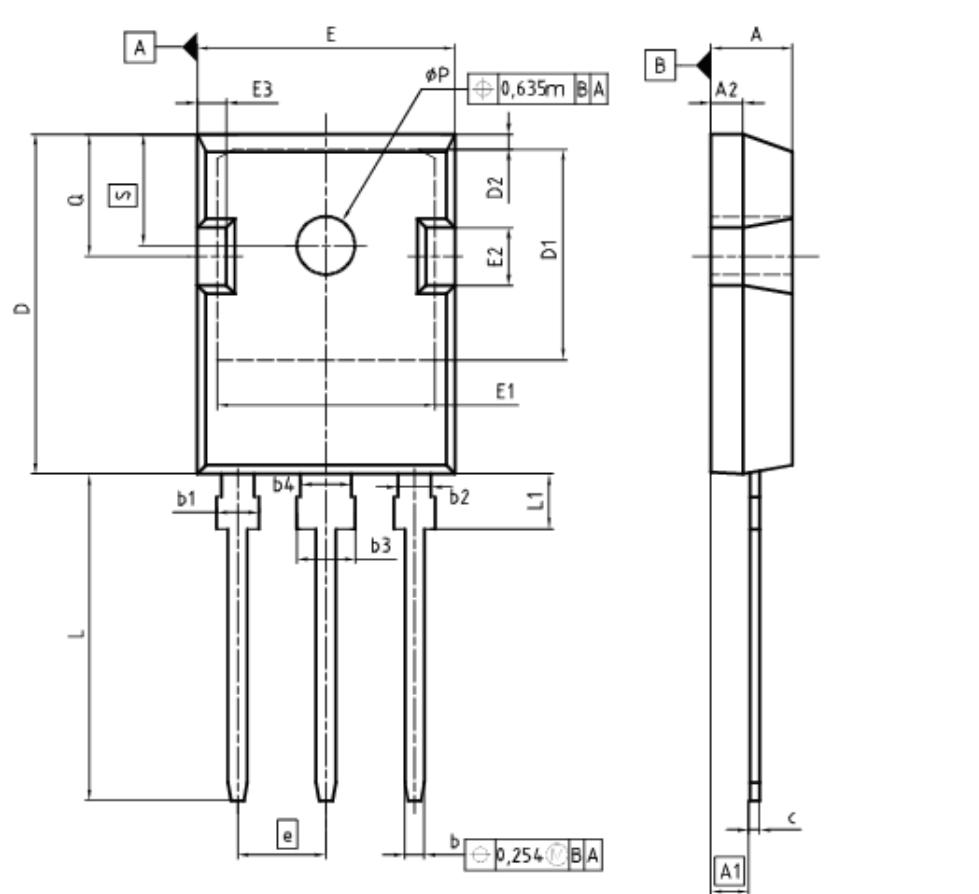


Figure 23. Typical diode forward voltage as a function of junction temperature

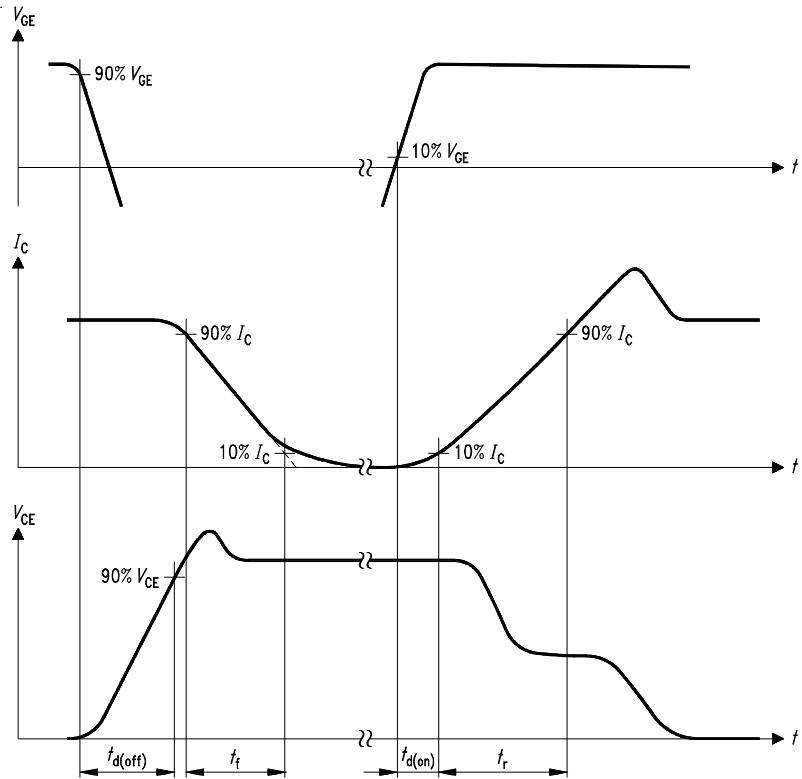
T0247-3



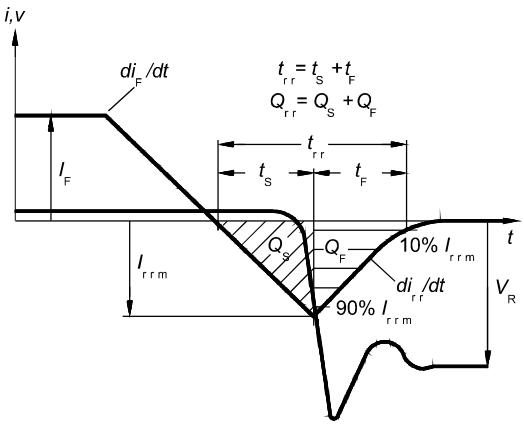
DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	4.83	5.21	0.180	0.205
A1	2.27	2.54	0.089	0.100
A2	1.85	2.16	0.073	0.085
b	1.07	1.33	0.042	0.052
b1	1.90	2.41	0.075	0.096
b2	1.90	2.16	0.075	0.085
b3	2.87	3.38	0.113	0.133
b4	2.87	3.13	0.113	0.123
c	0.55	0.68	0.022	0.027
D	20.80	21.10	0.819	0.831
D1	16.25	17.65	0.640	0.696
D2	0.96	1.35	0.037	0.053
E	15.70	16.13	0.618	0.635
E1	13.10	14.15	0.516	0.557
E2	3.68	5.10	0.145	0.201
E3	1.00	2.60	0.039	0.102
e	5.44		0.214	
N	3		3	
L	19.80	20.32	0.780	0.800
L1	4.10	4.47	0.161	0.176
øP	3.50	3.70	0.138	0.146
Q	5.49	6.00	0.216	0.236
S	6.04	6.30	0.238	0.248

DOCUMENT NO.	Z8B00003327
SCALE	0
0	5
5	7.5mm
EUROPEAN PROJECTION	
ISSUE DATE	01-10-2009
REVISION	04

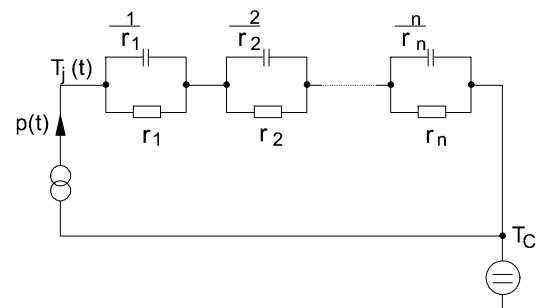
## IH-series



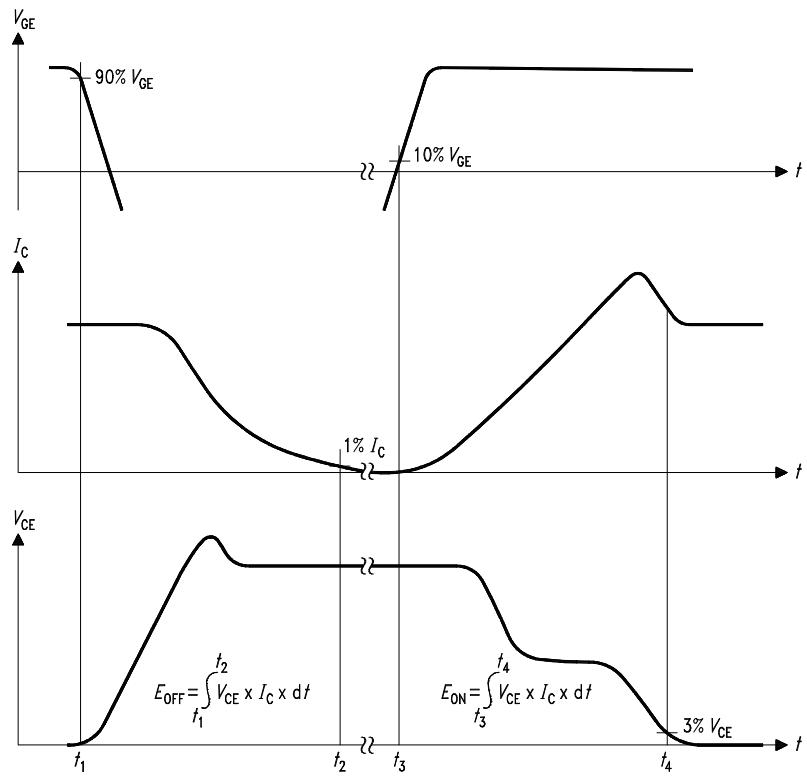
**Figure A. Definition of switching times**



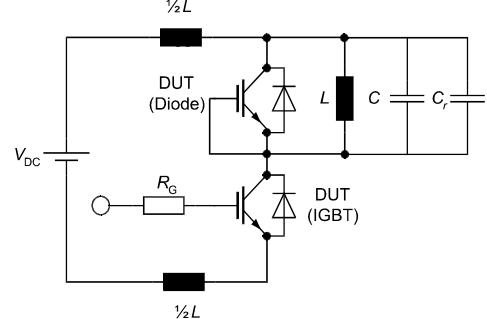
**Figure C. Definition of diodes switching characteristics**



**Figure D. Thermal equivalent circuit**



**Figure B. Definition of switching losses**



**Figure E. Dynamic test circuit**  
Leakage inductance  $L = 180\text{nH}$ ,  
Stray capacitor  $C_o = 40\text{pF}$ ,  
Relief capacitor  $C_r = 1\text{nF}$   
(only for ZVT switching)

Published by  
Infineon Technologies AG  
81726 Munich, Germany  
81726 München, Germany  
© 2009 Infineon Technologies AG  
All Rights Reserved.

#### Legal Disclaimer

The information given in this document shall in no event be regarded as a guarantee of conditions or characteristics. With respect to any examples or hints given herein, any typical values stated herein and/or any information regarding the application of the device, Infineon Technologies hereby disclaims any and all warranties and liabilities of any kind, including without limitation, warranties of non-infringement of intellectual property rights of any third party.

#### Information

For further information on technology, delivery terms and conditions and prices, please contact the nearest Infineon Technologies Office ([www.infineon.com](http://www.infineon.com)).

#### Warnings

Due to technical requirements, components may contain dangerous substances. For information on the types in question, please contact the nearest Infineon Technologies Office. Infineon Technologies components may be used in life-support devices or systems only with the express written approval of Infineon Technologies, if a failure of such components can reasonably be expected to cause the failure of that life-support device or system or to affect the safety or effectiveness of that device or system. Life support devices or systems are intended to be implanted in the human body or to support and/or maintain and sustain and/or protect human life. If they fail, it is reasonable to assume that the health of the user or other persons may be endangered.