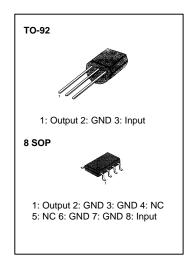
3-TERMINAL 0.1A POSITIVE VOLTAGE REGULATORS

The MC78LXX series of fixed voltage monolithic integrated circuit voltage regulators are suitable for application that required supply up to 100mA.

www.datasheet4u.com

FEATURES

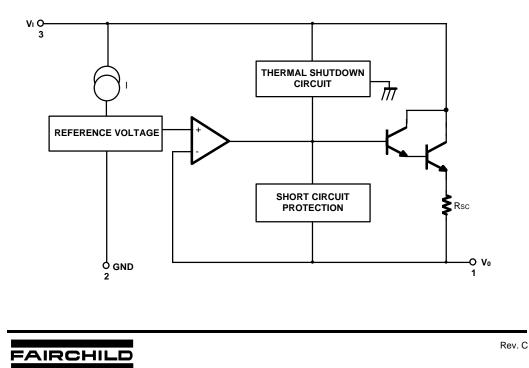
- Maximum Output Current of 100mA
 Output Voltage of 5V, 6V, 8V, 9V, 10V, 12V, 15V, 18V and 24V
- Thermal Overload Protection
- Short Circuit Current Limiting
- Output Voltage Offered in $\pm 5\%$ Tolerance



ORDERING INFORMATION

Device	Package	Operating Temperature
MC78LXXACP (LM78LXXACZ) (KA78LXXAZ)	TO-92	- 45 ~ + 125°C °
MC78LXXACD (KA78LXXAD)	8 SOP	0 ~ + 125°C

BLOCK DIAGRAM



©1999 Fairchild Semiconductor Corporation

SEMICONDUCTOR TM

ABSOLUTE MAXIMUM RATINGS ($T_A = 25 \degree C$, unless otherwise specified)

	Characteristic	Symbol	Value	Unit
	Input Voltage (for $V_0 = 5V, 8V$)	VI	30	V
	(for V _O = 12V, 15V)		35	V
	Operating Junction Temperature Range	TJ	0 ~ +150	°C
www.datasheet4	Storage Temperature Range	T _{STG}	-65 ~ +150	°C

LM78L05 ELECTRICAL CHARACTERISTICS

(V_I = 10V, I_O = 40mA, 0 $^{\circ}C \leq T_J \leq 125 \,^{\circ}C, C_I = 0.33 \,\mu\text{F}, C_O = 0.1 \mu\text{F},$ unless otherwise specified. (Note 1)

Characte	eristic	Symbol	Test	Conditions	Min	Тур	Max	Unit
Output Voltage		Vo	T _J = 25 °C	T _J = 25 °C		5.0	5.2	V
Line Devulation		414		$7V \le V_I \le 20V$		8	150	mV
Line Regulation		ΔV_{O}	$T_J = 25^{\circ}C$	$8V \le V_I \le 20V$		6	100	mV
		43.4	T 0500	$1 \text{mA} \le I_0 \le 100 \text{mA}$		11	60	mV
Load Regulation		ΔV_{O}	T _J = 25 °C	$1mA \leq I_O \leq 40mA$		5.0	30	mV
			$7V \le V_I \le 0V$	$1mA \le I_O \le 40mA$			5.25	V
Output Voltage		Vo	$7V \le V_1 \le V_{MAX}$ (Note 2)	$1mA \le I_0 \le 70mA$	4.75		5.25	V
Quiescent Current		la	T _J = 25 °C			2.0	5.5	mA
Quiescent Current	with line	ΔI_Q	$8V \leq V_I \leq 20V$				1.5	mA
Change	with load	ΔI_Q	$1 \text{mA} \le I_0 \le 40 \text{ m}$	A			0.1	mA
Output Noise Voltage	e	V _N	T _A = 25 °C, 10H	lz ≤ f ≤ 100KHz		40		$\mu V/V_0$
Temperature Coeffic	cient of V _o	$\Delta V_0 / \Delta T$	I _O = 5mA			-0.65		mV/°C
Ripple Rejection		RR	$f = 120Hz, 8V \le V_I \le 18V, T_J = 25^{\circ}C$		41	80		dB
Dropout Voltage		VD	T _J = 25 °C			1.7		V



LM78L06 ELECTRICAL CHARACTERISTICS

(V_I = 12V, I₀ = 40mA, $0^{\circ}C \le T_J \le 125^{\circ}C$, $C_I = 0.33\mu$ F, $C_0 = 0.1\mu$ F, unless otherwise specified. (Note 1)

	Characteri	stic	Symbol	т	est Conditions	Min	Тур	Max	Unit
	Output Voltage	Output Voltage		$T_J = 25 ^{\circ}C$		5.75	6.0	6.25	V
	Line Degulation			T 0700	$8.5V < V_1 < 20V$		64	175	mV
www.datasheet4u	Line Regulation		ΔV_{O}	T _J =25 °C	$9V \geq V_l \geq 20V$		54	125	mV
			41/	T 0500	$1mA < I_0 < 100mA$		12.8	80	mV
	Load Regulation		ΔV_{O}	T _J =25 °C	$1mA < I_O < 70mA$		5.8	40	mV
	Output Voltage		Vo	$8.5 < V_l < 20V,$	$1mA < I_O < 40mA$	5.7		6.3	.,
	Output Voltage		•0	$8.5 < V_I < V_{MAX}$ (Note), $1mA < I_O < 70mA$		5.7		6.3	V
	O discount O mont		la	T _J = 25 °C			3.9	6.0	mA
	Quiescent Current		IQ	T _J = 125 °C				5.5	IIIA
	Quiescent Current	with line	ΔI_Q	$9 < V_1 < 20V$				1.5	
	Change	with load	ΔI_Q	$1 \text{mA} < I_{O} < 40 \text{m}$	A			0.1	mA
	Output Noise Voltage	e	V _N	$T_A = 25 ^{\circ}C, 10R$	Hz < f < 100KHz		40		μV/V _O
	Temperature Coefficient of V_O		$\Delta V_0 / \Delta T$	I _O = 5mA			0.75		mV/°C
	Ripple Rejection	Ripple Rejection		f = 120Hz, 10V	$V < V_1 < 20V, T_J = 25 ^{\circ}C$	40	46		dB
	Dropout Voltage		VD	$T_J = 25 ^{\circ}C$			1.7		V

LM78L08 ELECTRICAL CHARACTERISTICS

(V_I = 14V, I_O = 40mA, 0 °C \leq T_J \leq 125 °C, C_I = 0.33 μ F, C_O = 0.1 μ F, unless otherwise specified. (Note 1)

Characteri	stic	Symbol	Test	Conditions	Min	Тур	Max	Unit
Output Voltage		Vo	T _J = 25 °C	T _J = 25 °C		8.0	8.3	V
				$10.5V \le V_l \le 23V$		10	175	mV
Line Regulation		ΔV_{O}	T」=25 °C	$11V \le V_I \le 23V$		8	125	mV
			T 0700	$1mA \le I_O \le 100mA$		15	80	mV
Load Regulation		ΔV_{O}	T _J =25 °C	$1mA \le I_O \le 40mA$		8.0	40	mV
			$10.5V \le V_I \le 23V$	$1mA \le I_O \le 40mA$	7.6		8.4	V
Output Voltage	Output Voltage Vo		$10.5V \le V_I \le V_{MAX}$ (Note 2)	$1mA \le I_0 \le 70mA$	7.6		8.4	v
Quiescent Current		lα	T _J = 25 °C			2.0	5.5	mA
Quiescent Current	with line	ΔI_Q	$11V \le V_1 \le 23V$				1.5	mA
Change	with load	ΔI_Q	$1mA \le I_O \le 40mA$				0.1	mA
Output Noise Voltag	e	V _N	$T_A = 25 ^{\circ}C, 10Hz \leq$	f ≤100KHz		60		μV/V _o
Temperature Coeffic	cient of Vo	$\Delta V_0 / \Delta T$	I _O = 5mA			-0.8		mV/°C
Ripple Rejection		RR	$f = 120Hz, 11V \le V_I \le 21V, T_J = 25 \degree C$		39	70		dB
Dropout Voltage		VD	T _J = 25 °C			1.7		V



LM78L09 ELECTRICAL CHARACTERISTICS

 $(V_{I} = 15V, I_{O} = 40mA, 0 \text{ }^{\circ}C \leq T_{J} \leq 125 \text{ }^{\circ}C, C_{I} = 0.33 \text{ } \mu\text{F}, C_{O} = 0.1 \mu\text{F}, \text{ unless otherwise specified. (Note 1)}$

	Characteri	stic	Symbol	Test	Conditions	Min	Тур	Max	Unit
	Output Voltage		Vo	$T_J = 25 ^{\circ}C$		8.64	9.0	9.36	V
	Line Degulation			T 0700	$11.5V \leq V_l \leq 24V$		90	200	mV
www.datasheet4u	Line Regulation		ΔV_{O}	T _J =25 °C	$13V \leq V_l \leq 24V$		100	150	mV
				T 0500	$1mA \leq I_O \leq 100mA$		20	90	mV
	Load Regulation		ΔV_{O}	T _J =25 °C	$1mA \leq I_O \leq 40mA$		10	45	mV
				$11.5V \leq V_l \leq 24V$	$1mA \leq I_O \leq 40mA$	8.55		9.45	V
	Output Voltage			$11.5V \le V_I \le V_{MAX}$ (Note 2)	$1\text{mA} \le I_0 \le 70\text{mA}$	8.55		9.45	V
	Quiescent Current		lα	T _J = 25 °C	•		2.1	6.0	mA
	Quiescent Current	with line	ΔI_Q	$13V \le V_I \le 24V$				1.5	mA
	Change	with load	ΔI_Q	$1mA \le I_O \le 40mA$				0.1	mA
	Output Noise Voltage	е	V _N	$T_A = 25 ^{\circ}C, 10Hz \leq$	f ≤ 100KHz		70		$\mu V/V_{O}$
	Temperature Coefficient of V_0		$\Delta V_0 / \Delta T$	I _O = 5mA			-0.9		mV/°C
	Ripple Rejection		RR	$f = 120Hz, 12V \le V_I \le 22V, T_J = 25 \degree C$		38	44		dB
	Dropout Voltage		VD	T _J = 25 °C			1.7		V

LM78L10 ELECTRICAL CHARACTERISTICS

(V_I = 16V, I_O = 40mA, $0^{\circ}C < T_J < 125^{\circ}C$, C_I = 0.33 μ F, C_O = 0.1 μ F, unless otherwise specified. (Note 1)

Characteris	tic	Symbol	т	est Conditions	Min	Тур	Мах	Unit
Output Voltage		Vo	T _J = 25 °C		9.6	10.0	10.4	V
Line Desulation		A) ($12.5 < V_1 < 25V$		100	220	mV
Line Regulation		ΔV_{O}	T _J =25 °C	$14V \geq V_l \geq 25V$		100	170	mV
		A) (T 0500	1mA < I _O < 100mA		20	94	mV
Load Regulation		ΔV_{O}	T _J =25 °C	$1mA < I_O < 70mA$		10	47	mV
Output Voltage		Vo	$12.5 < V_1 < 25V, 1mA < I_0 < 40mA$		9.5		10.5	
Oulput voltage		vo	$12.5 < V_I < V_{MAX}$ (Note), $1mA < I_O < 70mA$		9.5		10.5	V
			$T_J = 25 ^{\circ}C$			4.2	6.5	mA
Quiescent Current		lα	T _J = 125 °C				6.0	mA
Quiescent Current	with line	ΔI_Q	$12.5 < V_I < 25V$				1.5	mA
Change	with load	ΔI_Q	$1mA < I_0 < 40m$	A			0.1	IIIA
Output Noise Voltage	e	V _N	T _A = 25 °C, 10Hz < f < 100KHz			74		μV/V _O
Temperature Coeffic	ient of V _o	$\Delta V_{O} / \Delta T$	I _O = 5mA			0.95		mV/°C
Ripple Rejection		RR	f = 120Hz, 15V < V _I < 25V, T _J = 25 °C		38	43		dB
Dropout Voltage		VD	T _J = 25 °C			1.7		V



LM78L12 ELECTRICAL CHARACTERISTICS

(V_I = 19V, I_O = 40mA, 0 °C \leq T_J \leq 125 °C, C_I = 0.33 μ F, C_O = 0.1 μ F, unless otherwise specified. (Note 1)

	Characteri	stic	Symbol	Test	Conditions	Min	Тур	Max	Unit
	Output Voltage		Vo	$T_J = 25 ^{\circ}C$		11.5	12	12.5	V
	Line Degulation			T 0700	$14.5V \leq V_l \leq 27V$		20	250	mV
www.datasheet4	Line Regulation		ΔV_{O}	T _J =25 °C	$16V \leq V_I \leq 27V$		15	200	mV
				T 0500	$1mA \leq I_0 \leq 100mA$		20	100	mV
	Load Regulation		ΔV_{O}	T _J =25 °C	$1mA \leq I_0 \leq 40mA$		10	50	mV
				$14.5V \leq V_l \leq 27V$	$1mA \leq I_0 \leq 40mA$	11.4		12.6	V
	Output Voltage		Vo	$14.5V \le V_I \le V_{MAX}$ (Note 2)	$1mA \le I_O \le 70mA$	11.4		12.6	V
	Quiescent Current		la	T _J = 25 °C			2.1	6.0	mA
	Quiescent Current	with line	ΔI_Q	$16V \le V_l \le 27V$				1.5	mA
	Change	with load	ΔI_Q	$1mA \le I_O \le 40mA$				0.1	mA
	Output Noise Voltage	e	V _N	T_{A} = 25 °C, 10Hz \leq	f ≤ 100KHz		80		$\mu V/V_O$
	Temperature Coefficient of V_0		$\Delta V_0 / \Delta T$	I _O = 5mA			-1.0		mV/°C
	Ripple Rejection		RR	f = 120Hz, 15V ≤ V₁ ≤ 25V, T」 = 25 °C		37	65		dB
	Dropout Voltage		VD	T _J = 25 °C			1.7		V

LM78L15 ELECTRICAL CHARACTERISTICS

(V_I = 23V, I_O = 40mA, 0 °C \leq T_J \leq 125°C, C_I = 0.33 μ F, C_O = 0.1 μ F, unless otherwise specified. (Note 1)

Characteri	stic	Symbol	Test	Conditions	Min	Тур	Мах	Unit
Output Voltage		Vo	T _J = 25 °C	T _J = 25 °C		15	15.6	V
Line Develotion				$17.5V \le V_I \le 30V$		25	300	mV
Line Regulation		ΔV_{O}	T _J =25 °C	$20V \leq V_l \leq 30V$		20	250	mV
			T 0500	$1mA \le I_O \le 100mA$		25	150	mV
Load Regulation		ΔV_{O}	T _J =25 °C	$1mA \le I_O \le 40mA$		12	75	mV
			$17.5V \le V_I \le 30V$	$1mA \le I_O \le 40mA$	14.25		15.75	V
Output Voltage		Vo	$17.5V \le V_I \le V_{MAX}$ (Note 2)	$1mA \le I_0 \le 70mA$	14.25		15.75	V
Quiescent Current		Ι _Q	T _J = 25 °C	•		2.1	6.0	mA
Quiescent Current	with line	ΔI_Q	$20V \le V_I \le 30V$				1.5	mA
Change	with load	ΔI_Q	$1mA \le I_O \le 40mA$				0.1	mA
Output Noise Voltag	е	V _N	T _A = 25 °C, 10Hz ≤ f ≤ 100KHz			90		$\mu V/V_{O}$
Temperature Coeffic	ient of V _o	$\Delta V_0 / \Delta T$	I _O = 5mA			-1.3		mV/°C
Ripple Rejection		RR	$f = 120Hz, 18.5V \le V_I \le 28.5V, T_J = 25 \degree C$		34	60		dB
Dropout Voltage		VD	T _J = 25 °C			1.7		V



LM78L18 ELECTRICAL CHARACTERISTICS

 $(V_{I}=27V, I_{O}=40mA, 0\ ^{\circ}C \leq T_{J} \leq 125\ ^{\circ}C, C_{I}=0.33\ \mu\text{F}, C_{O}=0.1\mu\text{F}, \text{ unless otherwise specified.} (Note 1)$

	Characteri	stic	Symbol	Test	Conditions	Min	Тур	Max	Unit
	Output Voltage		Vo	$T_J = 25 ^{\circ}C$		17.3	18	18.7	V
	Line Desudation				$21V \leq V_l \leq 33V$		145	300	mV
.datasheet4u	Line Regulation		ΔV_{O}	T _J =25 °C	$22V \leq V_l \leq 33V$		135	250	mV
				T 0500	$1mA \le I_0 \le 100mA$		30	170	mV
	Load Regulation		ΔV_{O}	T _J =25 °C	$1mA \le I_O \le 40mA$		15	85	mV
				$21V \le V_I \le 33V$	$1mA \le I_O \le 40mA$	17.1		18.9	V
	Output Voltage		Vo	$21V \le V_I \le V_{MAX}$ (Note 2)	$1mA \le I_O \le 70mA$	17.1		18.9	V
	Quiescent Current		lq	T _J = 25 °C			2.2	6.0	mA
	Quiescent Current	with line	ΔI_Q	$21V \le V_I \le 33V$				1.5	mA
	Change	with load	ΔI_Q	$1mA \le I_O \le 40mA$				0.1	mA
	Output Noise Voltag	e	V _N	T _A = 25 °C, 10Hz ≤	≤f ≤ 100KHz		150		$\mu V/V_{O}$
	Temperature Coefficient of V_{O}		$\Delta V_0 / \Delta T$	I _O = 5mA			-1.8		mV/°C
	Ripple Rejection	Ripple Rejection		$f = 120Hz, 23V \le V_1 \le 33V, T_3 = 25 \degree C$		34	48		dB
	Dropout Voltage		VD	T _J = 25 °C			1.7		V

LM78L24 ELECTRICAL CHARACTERISTICS

 $(V_{I}=33V,\ I_{O}=40mA,\ 0\ ^{\circ}C\leq T_{J}\leq 125\ ^{\circ}C,\ C_{I}=0.33\ \mu F,\ C_{O}=0.1\mu F,\ unless \ otherwise \ specified.\ (Note\ 1)$

Characteri	stic	Symbol	Test	Test Conditions		Тур	Max	Unit
Output Voltage		Vo	T _J = 25 °C		23	24	25	V
				$27V \le V_I \le 38V$		160	300	mV
Line Regulation		ΔV_{O}	T _J =25 °C	$28V \leq V_I \leq 38V$		150	250	mV
			T 0500	$1mA \le I_0 \le 100mA$		40	200	mV
Load Regulation		ΔV_O	T _J =25 °C	$1mA \leq I_O \leq 40mA$		20	100	mV
			$27V \le V_I \le 38V$	$1mA \le I_0 \le 40mA$	22.8		25.2	V
Output Voltage	Output Voltage Vo		$27V \le V_I \le V_{MAX}$ (Note 2)	$1mA \le I_O \le 70mA$	22.8		25.2	V
Quiescent Current		lq	T _J = 25°C			2.2	6.0	mA
Quiescent Current	with line	ΔI_Q	$28V \le V_I \le 38V$				1.5	mA
Change	with load	ΔI_Q	$1mA \leq I_O \leq 40mA$				0.1	mA
Output Noise Voltage	e	V _N	$T_A = 25 \degree C$, $10Hz \le$	$T_A = 25 \degree C$, $10Hz \le f \le 100 \text{KHz}$		200		μV/V _O
Temperature Coeffic	cient of Vo	$\Delta V_{0} / \Delta T$	I _O = 5mA			-2.0		mV/°C
Ripple Rejection		RR	$f = 120Hz, 28V \le V_I \le 38V, T_J = 25 \degree C$		34	45		dB
Dropout Voltage		VD	T _J = 25 °C			1.7		V

Notes

www

1. The maximum steady state usable output current and input voltage are very dependent on the heat sinking and/or lead length of the package. The data above represent pulse test conditions with junction temperature as indicated at the initiation

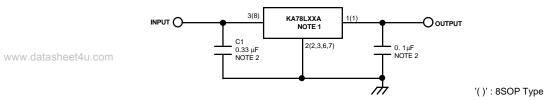
of tests.

2. Power dissipation \leq 0.75W.



SEMICONDUCTOR TM

TYPICAL APPLICATION



Notes

To specify an output voltage, substitute voltage value for "XX".
 Bypass Capacitors are recommend for optimum stability and transient response and should be located as close as possible to the regulator



www.DataSheet4U.com

TRADEMARKS

The following are registered and unregistered trademarks Fairchild Semiconductor owns or is authorized to use and is not intended to be an exhaustive list of all such trademarks.

	ACEx™
	CoolFET™
www.datasnee	^{t4} CROSSVOLT™
	E ² CMOS™
	FACT™
	FACT Quiet Series™
	FAST [®]
	FASTr™
	GTO™
	HiSeC™

ISOPLANAR™ MICROWIRE™ POP™ PowerTrench[®] QFET™ QS™ Quiet Series™ SuperSOT™-3 SuperSOT™-6 SuperSOT™-8 TinyLogic™ UHC™ VCX™

DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION. As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, or (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in significant injury to the user. 2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

PRODUCT STATUS DEFINITIONS

Definition of Terms

Datasheet Identification	Product Status	Definition
Advance Information	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	This datasheet contains preliminary data, and supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
Obsolete	Not In Production	This datasheet contains specifications on a product that has been discontinued by Fairchild semiconductor. The datasheet is printed for reference information only.