

DB-960-70W

70W / 26V / 925-960 MHz PA using 2x PD57045S The *LdmosST* FAMILY

PRELIMINARY DATA

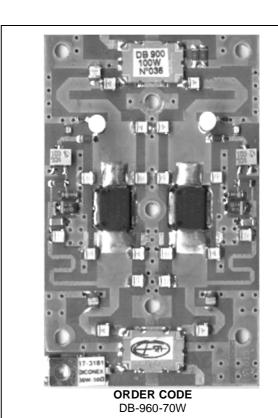
N-CHANNEL ENHANCEMENT-MODE LATERAL MOSFETs

- EXCELLENT THERMAL STABILITY
- COMMON SOURCE CONFIGURATION
- P_{OUT} = 70 W min. with 13 dB gain over 925-960 MHz
- 10:1 LOAD VSWR CAPABILITY
- BeO FREE AMPLIFIER.

DESCRIPTION

The DB-960-70W is a common source N-Channel enhancement-mode lateral Field-Effect RF power amplifier designed for GSM & E-GSM base station applications.

The DB-960-70W is designed in cooperation with Européenne de Télécommunications S.A (www.etsa.fr), for high gain and broadband performance operating in common source mode at 26 V, capable of withstanding load mismatch up to 10:1 all phases and with harmonics lower than 30 dBc.



MECHANICAL SPECIFICATION L=80 mm W=50 mm H=10 mm

ABSOLUTE MAXIMUM RATINGS (T_{CASE} = 25°C)

Symbol	Parameter	Value	Unit
V _{DD}	Supply voltage	32	V
I _D	Drain Current	9	Α
P _{DISS}	Power Dissipation	135	W
T _{CASE}	Operating Case Temperature	-20 to +85	°C
P _{amb}	Max. Ambient Temperature	+55	°C

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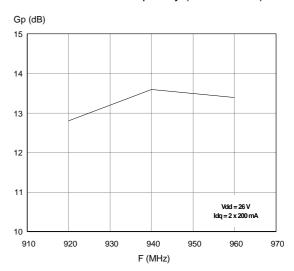
ELECTRICAL SPECIFICATION (T_{amb} = +25°C, Vdd = 26V, Idq = 2 x 200mA)

Symbol	Test Conditions	Min.	Тур.	Max.	Unit
FREQ.	Frequency Range	925		960	MHz
Gain	P _{OUT} = 75 W	12.5	13		dB
P _{1dB}	Over frequency range: 925 - 960 MHz	70	75		W
Flatness	Over frequency range and @ P _{OUT} = 75 W			+/- 0.5	dB
Flatness	P _{OUT} from 0.1W to 75W			1	dB
ND at P _{1dB}	P _{1dB}	45	50		%
IRTL	Input return Loss P _{OUT} from 0.1W to 75W		-20	-15	dB
Harmonic	P _{OUT} = 75 W		-40	-30	dBc
VSWR	Load Mismatch all phases @ P _{OUT} = 75 W	10:1			
Spurious	10:1 VSWR all phases and P _{OUT} from 0.1 to 75W			-76	dBc
IMD ₃	P _{OUT} = 75 WPEP			-25	dBc

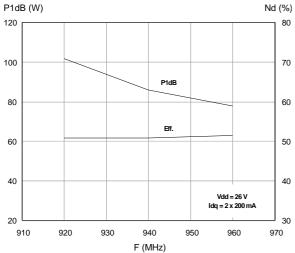
TYPICAL PERFORMANCE Output Power versus Input Power

Pout (W) 100 960 MHz 80 920 MHz Vdd = 26 V Idq = 2 x 200 mA 0 2 4 6 8 Pin (W)

Power Gain versus Frequency (Pout = 75W)

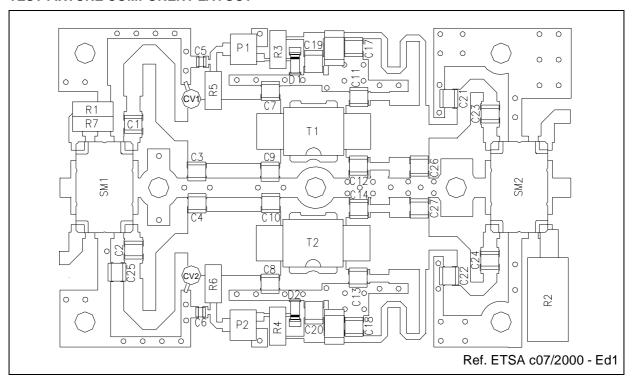


P1dB and Efficiency versus Frequency

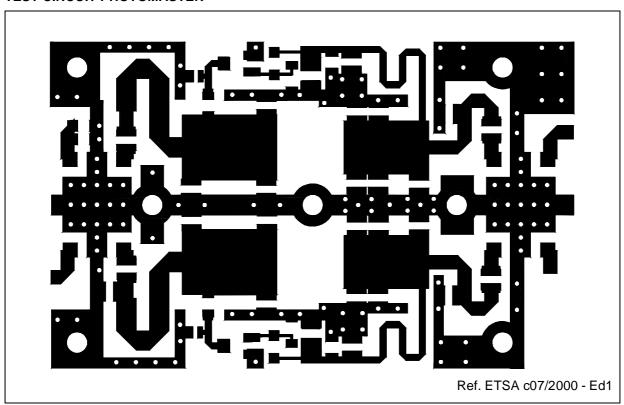


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TEST FIXTURE COMPONENT LAYOUT



TEST CIRCUIT PHOTOMASTER



TEST CIRCUIT COMPONENT PART LIST

COMPONENT	DESCRIPTION			
T1, T2	PD57045S TRANSISTOR			
C1, C2, C23, C24	47pF - 500V CERAMIC CHIP CAPACITOR			
C3, C4	2.2pF - 500V CERAMIC CHIP CAPACITOR			
C5, C6, C17, C18	100pF - 500V CERAMIC CHIP CAPACITOR			
C7, C8, C9, C10, C11, C12, C13, C14	10pF - 500V CERAMIC CHIP CAPACITOR			
C15, C16	100nF - 63V CERAMIC CHIP CAPACITOR			
C19, C20	1μF / 35V ELECTROLYTIC CAPACITOR			
C21, C22	4.7pF - 500V CERAMIC CHIP CAPACITOR			
C26, C27	3.3pF - 500V CERAMIC CHIP CAPACITOR			
C25	0.5pF - 500V CERAMIC CHIP CAPACITOR			
CV1, CV2	ADJUSTABLE CAPACITOR 0.6 - 4.5pF / 500V			
P1, P2	10K Ohms MULTITURN POTENTIOMETER			
R1,R7	100 Ohms 1/4W 1206 SMD CHIP RESISTOR			
R2	50 Ohms 30W - 4GHz LOAD			
R3, R4	4.7K Ohms 1/4W 1206 SMD CHIP RESISTOR			
R5, R6	10K Ohms 1/4W 1206 SMD CHIP RESISTOR			
D1, D2	ZENER DIODE 5V - 500 mW SOD80			
SM1, SM2	90° SMD HYBRID COUPLER ANAREN Xinger 1304-3			
BOARD	METCLAD MX3-30-C1/10C THK 0.762 mm Cu 35μ			
SUBSTRATE	TEFLON-GLASS Er = 2.55			
BACK SIDE	COPPER FLANGE 2 mm THICKNESS			
CERAMIC CHIP CAPACITORS	ATC100B or EQUIVALENT			

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